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DESIGN AND REAL-TIME IMPLEMENTATION OF A BASEBAND LPC CODER FOR--ETC(U)

FEB 80 R VISWANATHAN, J WOLF, L COSELL

DCA100-79-C-0003

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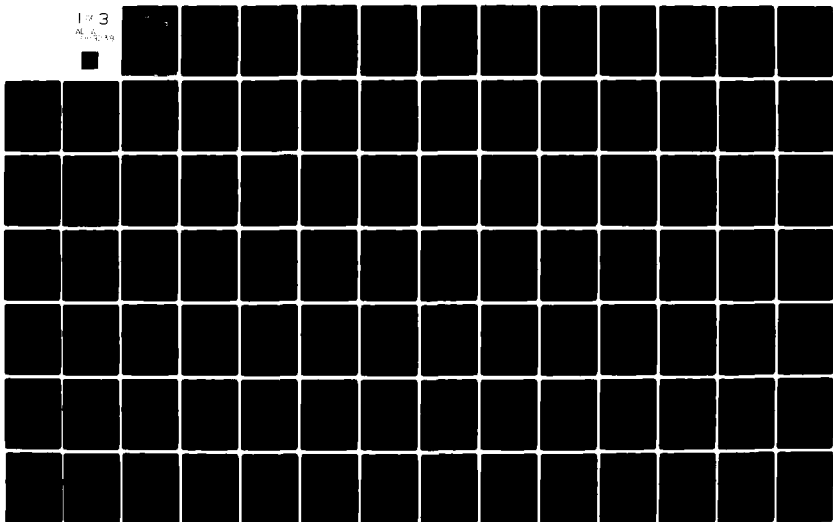
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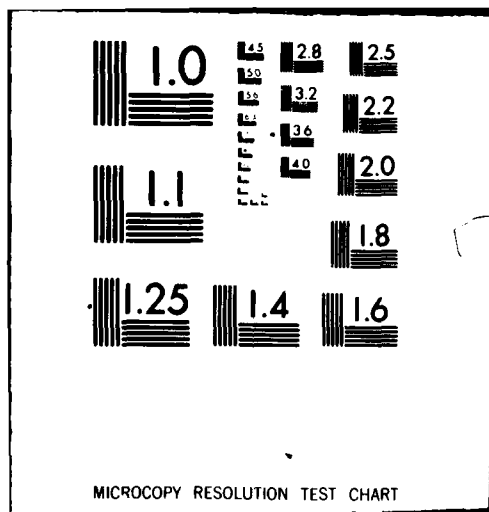
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**Bolt Beranek and Newman Inc.**



**LEVEL III**

12 B.S.

Report No. 4327 - vol

ADA 083238



**Design and Real-Time Implementation of a Baseband LPC Coder  
for Speech Transmission Over 9600 Bps Noisy Channels**

**Volume II: Program Listings**

**Final Report**

February 1980

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Prepared for:  
Defense Communications Agency

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18. SUPPLEMENTARY NOTES		
19. KEY WORDS (Continue on reverse side if necessary and identify by block number) speech coding, 9600 bps speech transmission, voice-excited coder, baseband coder, linear prediction, high-frequency regeneration, digital voice terminal, real-time speech coder.		
20. ABSTRACT (Continue on reverse side if necessary and identify by block number) This report describes the design and development of a real-time baseband LPC speech coder that transmits high-quality speech over a 9600 bps synchronous channel with bit-error rates of up to 1%. Presented are the results of our investigation of a number of aspects of the baseband LPC coder with the goal of maximizing the quality of the transmitted speech. Important among these aspects are: baseband width, baseband coding, cont'd.		

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20. Abstract (cont'd.)

high-frequency regeneration, and error-protection of important transmission parameters. The report also includes the system design, detailed documentation, and program listings of the MAP-300 real-time implementation of the optimized speech coder.

This report is bound in two volumes. Volume I contains the text of the report, and Volume II contains the program listings of the MAP-300 speech coder implementation.

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Report No. 4327

DESIGN AND REAL-TIME IMPLEMENTATION OF A BASEBAND LPC CODER  
FOR SPEECH TRANSMISSION OVER 9600 BPS NOISY CHANNELS

Final Report

Volume II: Program Listings

Authors: R. Viswanathan, J. Wolf, L. Cosell, K. Field,  
A. Higgins, and W. Russell

February 1980

Prepared for:  
The Defense Communications Agency

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2:

[88N-TENSEID]<MAP>88N300.MSO.61, 38-Dec-79 16:14:24, Ed: KFIELD  
MAP-300 BBN FUNCTIONS FOR SNAPII REL. 3.5

```

(00002) *MAP-300 BBN FUNCTIONS FOR SNAPII REL. 3.5
(00003) *
(00004) *
(00005) * DEFINE SYMBOLS FOR ARRAY FUNCTION ASSEMBLIES
(00006) *
(00007) *
(00008) * FROM THE SNAP-II EXECUTIVE --- REL. 3.05 --- 5/22/79
(00009) *
(00010) *
(00011) *
(00012) *
(00013) *
(00014) *
(00015) *
(00016) *
(00017) *
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(00052) *
(00053) *
(00054) *

```

AFDISORG = \$0E8  
 APSASS = \$245  
 APSBDR = \$EFA  
 APSBDR0 = \$F63  
 APSBDR1 = \$F20  
 APSBRL = \$240  
 APSCSSC = \$248  
 APSDONE = \$F01  
 APSDWER = \$FBC  
 APSGP = \$C  
 APSG1 = \$D  
 APSGFF = \$10  
 APSSAIO = \$A  
 APSSCLR = \$E  
 APSSSS = \$9  
 APSSBMO = \$4  
 APSSLA = \$1FFC8  
 APSR = \$1FFC8  
 BCT\$AD = \$684  
 BCT\$AT = \$686  
 BCT\$BA = \$582  
 BCT\$CB = \$6  
 BCT\$RC = \$F  
 CK4DBS = \$1AD6  
 CLR\$G0C1 = \$792  
 COS\$ = \$3198  
 CSPUSNOS = \$21FC  
 DMS = \$794  
 ERRORS = \$1AFA  
 FJIAS = \$3110  
 FDT\$ = \$7E8  
 FEOSS = \$304C  
 FFTSC8S2 = \$79A  
 FLC\$CLR = \$8  
 FLC\$CC = \$4  
 FLC\$C1 = \$5  
 FLC\$C2 = \$5  
 FLC\$C3 = \$7  
 FLC\$RI = \$11

PAGE 3: CERN-TEVEIDJ<MAP>BBH300.WSD-61, 30-Dec-79 16:14:24, ED: KFIELD  
 MAP-302 BBN FUNCTIONS FOR SNAPII REL. 3.5

00000020 (00055)	FLGSSET = \$20		
0000103C (00056)			
0000103C (00057)	GATHERS = \$103C		
00000001 (00058)			
00000001 (00059)	RS = 1		
00000000 (00060)			
00000502 (00061)	ISVTS = \$502		
00000502 (00062)			
0000107E (00063)	LOADSAP = \$107E		
0000100F (00064)	LOADSAP1 = \$100F		
00000000 (00065)			
00000006 (00066)	MSK\$0MB = \$6		
00000000 (00067)	MSK\$0BYT = \$FF00		
0000000F (00068)	MSK\$0BYT = \$00FF		
00000000 (00069)	MS\$ = 0		
00000000 (00070)			
00000010 (00071)	DOE = \$10		
00000010 (00072)			
000035EA (00073)	SI011\$=\$35EA		
0000074E (00074)	SHFTLSR5 = \$74E		
0000023FA (00075)	SINCOSST = \$23FA		
00003192 (00076)	SINS = \$3192		
00000302 (00077)	SVTS = \$302		
0000030E (00078)	SVTS0N1 = \$30E		
0001FFCE (00079)	SYS\$PLGS = \$1FFCE		
00000000 (00080)			
00000704 (00081)	TE4\$0 = \$704		
000021FE (00082)	TORS = \$21FE		
00000200 (00083)	TOESPTR = \$200		
00000000 (00084)			
00001C5A (00085)	VAL0UFS = \$1C5A		
000002070 (00086)	VSMAS\$=\$2070		
00000000 (00087)			
00000002 (00088)	WS = \$2		
00000000 (00089)			
0000192C (00090)	XFLS01 = \$192C		
00000000 (00091)			
0000070A (00092)	ZERO = \$70A		
00000000 (00093)			
00000000 (00094)			
00000000 (00095)			
00002200 (00096)	BL = TOESPTR		
00000000 (00097)			
00200 00106EE2 (00098)	ADDR TOESCUR(,1)	UPDATE TOP OF EXEC POINTER	
00000000 (00099)			
00000000 (00100)	#M = 3		
00000000 (00101)			
00000000 (00102)			
00000000 (00103)			
00000000 (00104)			

```

(00105) *ARRAY FUNCTION DISPATCH TABLE PATCHES
(00106) *
(00107) *
(00108) *
00000900 (00109)
001E60F2 (00110)
00902 001E60F6 (00111)
00904 001021FC (00112)
00000900 (00113) *
00000906 (00114)
001E6038 (00115)
00908 001E60C6 (00116)
0090A 001021FC (00117) *
0000090C (00118)
0090C 001E60F8 (00119)
0090E 001E618C (00121)
00910 001021FC (00122) *
00000912 (00123)
00000912 (00124)
00912 001E61D8 (00125)
00914 001E62EC (00126)
00916 0010666C (00127) *
0000096C (00128)
0096C 001E6692 (00130)
0096E 001E674C (00131)
00970 001021FC (00132) *
00000A5C (00133)
00000A5C (00134)
00A5C 001E67FE (00135)
00A5E 001E6838 (00136)
00A60 001021FC (00137) *
00000A80 (00138)
00A80 001E6876 (00140)
00A82 001E68CC (00141)
00A84 001021FC (00142) *
00000A80 (00143)
00A80 001E694C (00145)
00A82 001E6A38 (00146)
00A84 001021FC (00147) *
00000A92 (00148)
00A92 001E6AFC (00149)
00A94 001E6B70 (00151)
00A96 001021FC (00152) *
00000A62 (00153)
00A62 001E6D44 (00155)
00A64 001E6E62 (00156)
00A66 001021FC (00157)

#L = AFD$ORC+3*W$(132-128) ;FCB 132 (VLIST)
ADDR VLTS$(R7,1)
ADDR V320$(R7,1)
ADDR CSPUSNOS(,1,0)

#L = AFD$ORC+3*W$(133-128) ;FCB 133 (VKTOA)
ADDR VKTOA$(R7,1)
ADDR V110$(R7,1)
ADDR CSPUSNOS(,1,0)

#L = AFD$ORC+3*W$(134-128) ;FCB 134 (PRTR0)
ADDR PRTR0$(R7,1)
ADDR P2120$(R7,1)
ADDR CSPUSNOS(,1,0)

#L = AFD$ORC+3*W$(135-129) ;FCB 135 (MWLQ)
ADDR MWLFSAPU(R7,1)
ADDR MWLFSAPS(R7,1)
ADDR MWLQSSM(,1,1) ;(POST SUPPORT)

#L = AFD$ORC+3*W$(150-128) ;FCB 150 (VAPC)
ADDR VAPCS(R7,1)
ADDR AAPCS(R7,1)
ADDR CSPUSNOS(,1,0)

#L = AFD$ORC+3*W$(190-128) ;FCB 190 (DEAL)
ADDR DEALUS(R7,1)
ADDR DEALSS(R7,1)
ADDR CSPUSNOS(,1,0)

#L = AFD$ORC+3*W$(196-128) ;FCB 196 (VAPCI)
ADDR APCIUS(R7,1)
ADDR APCISS(R7,1)
ADDR CSPUSNOS(,1,0)

#L = AFD$ORC+3*W$(212-128) ;FCB #212 (PTAP)
ADDR PTUS(R7,1)
ADDR PTSS(R7,1)
ADDR CSPUSNOS(,1,0)

#L = AFD$ORC+3*W$(199-128) ;FCB #199 - ENRG)
ADDR ENUS(R7,1)
ADDR ENSS(R7,1)
ADDR CSPUSNOS(,1,0)

#L = AFD$ORC+3*W$(191-128) ;FCB 191 DCOR
ADDR DCRUS(R7,1)
ADDR DCRSS(R7,1)
ADDR CSPUSNOS(,1,0)

```

PAGE 5: [BBN-TENEXD]<MAP>BBN300.NSO.61, 30-Dec-79 16:14:24, Ed: KFIELD  
ARRAY FUNCTION DISPATCH TABLE PATCHES

(00158) \*

PAGE 6: [BBN-TENEXD]<MAP>BBN300.NSO.61, 30-Dec-79 16:14:24, Ed: KFIELD  
NON-ARRAY FUNCTION DISPATCH TABLE PATCHES

(00159) *	NON-ARRAY FUNCTION DISPATCH TABLE PATCHES
(00160) *	
(00161) *	
0000088A (00162)	HL = FDIS + (WS * 105)
0000088A 0000667E (00163)	ADDR MPIFF\$
(00164) *	
000009C6 (00165)	HL = FDIS + (WS * 111)
000009C6 000066BC (00166)	ADDR WPMBS\$
(00167) *	
	PCB 105 (MPIFF)
	PCB 111 (WPMBS)

PAGE 7: [BBN-TENEXD]<MAP>BBN300.NSO.61, 30-Dec-79 16:14:24, Ed: KFIELD  
APU, APS, AND C\$PU CODE FOR ADDED FUNCTIONS

(00168) *	APU, APS, AND C\$PU CODE FOR ADDED FUNCTIONS
(00169) *	
(00170) *	
(00171) *	
(00172) *	SET START ADDRESS FOR ADDED FUNCTIONS.
(00173) *	REL 3.5 LEAVES EMPTY SPACE (ON BUS 1) AT:
(00174) *	\$3800 - \$4000
(00175) *	\$4800 - \$4A00
(00176) *	\$5D00 - \$C000
(00177) *	PUT ADDED FUNCTIONS IN THIS SPACE.
(00178) *	
00005000 (00179)	HL = \$5000
(00180) *	



(00181) * DECODING TABLES	
85000 989999C1 (00182)	DATA -3.875
85002 8053F7C1 (00183)	DATA -1.666
85004 EA3076C8 (00184)	DATA -0.838
85006 9002F1C8 (00185)	DATA -0.233
85008 1002F1C8 (00186)	DATA 0.233
8500A 6A3076C8 (00187)	DATA 0.838
8500C 0053F7C1 (00188)	DATA 1.666
8500E 189999C1 (00189)	DATA 3.875
DKTAB1:	
85010 FA708C48 (00190)	DATA -0.9565597E+00
85012 F995AE48 (00191)	DATA -0.9498804E+00
85014 F89A2748 (00192)	DATA -0.9422844E+00
85016 F7968448 (00193)	DATA -0.9333929E+00
85018 F6E67C48 (00194)	DATA -0.9232912E+00
8501A F48378C8 (00195)	DATA -0.911774E+00
8501C F3827348 (00196)	DATA -0.8985123E+00
8501E F148ACF (00197)	DATA -0.8834394E+00
85020 EEE2724F (00198)	DATA -0.8662856E+00
85022 EC6458CF (00199)	DATA -0.8468127E+00
85024 E9921FCF (00200)	DATA -0.8247741E+00
85026 E663184F (00201)	DATA -0.7998995E+00
85028 E2CE5E4F (00202)	DATA -0.7719480E+00
8502A DEC0384F (00203)	DATA -0.7406369E+00
8502C D456094F (00204)	DATA -0.7057506E+00
8502E D56287C8 (00205)	DATA -0.6674094E+00
85030 CFED16C8 (00206)	DATA -0.6244229E+00
85032 C9F1FCCF (00207)	DATA -0.5776974E+00
85034 C36FE2CF (00208)	DATA -0.5268520E+00
85036 BC685448 (00209)	DATA -0.4719339E+00
85038 B4E02F48 (00210)	DATA -0.4130916E+00
8503A ACDFFB48 (00211)	DATA -0.3505854E+00
8503C A4740C48 (00212)	DATA -0.2847915E+00
8503E 98AC73CF (00213)	DATA -0.2162003E+00
85040 929CAD48 (00214)	DATA -0.1454864E+00
85042 89581148 (00215)	DATA -0.7309163E-01
85044 FFFFF39 (00216)	DATA -0.3725290E-01
85046 095816CF (00217)	DATA 0.7309162E-01
85048 129CAD48 (00218)	DATA 0.1454864E+00
8504A 18AC73CF (00219)	DATA 0.2162003E+00
8504C 24740C48 (00220)	DATA 0.2847915E+00
8504E 2CDFFB48 (00221)	DATA 0.3505854E+00
DKTAB2:	
85050 B5986048 (00222)	DATA -0.4188842E+00
85052 AD85174F (00223)	DATA -0.3556241E+00
85054 A4FE8748 (00224)	DATA -0.2890176E+00
85056 9C184248 (00225)	DATA -0.2194903E+00
85058 92E6824F (00226)	DATA -0.1476596E+00
8505A 09809648 (00227)	DATA -0.742368E-01
8505C 97FFF848 (00228)	DATA -0.1117587E-07
8505E 09809648 (00229)	DATA 0.742368E-01
85060 12E68248 (00230)	DATA 0.1476596E+00

05062	1C184140 (00234)	DATA	0.2194902E+00
05064	24FE9740 (00235)	DATA	0.2890176E+00
05066	20851740 (00236)	DATA	0.3556241E+00
05068	35986F40 (00237)	DATA	0.4188042E+00
0506A	3034CF40 (00238)	DATA	0.4781741E+00
0506C	44480C40 (00239)	DATA	0.5334735E+00
0506E	4AD2DA40 (00240)	DATA	0.5845597E+00
05070	5E0198C0 (00241)	DATA	0.6313964E+00
05072	5646E8C0 (00242)	DATA	0.6740390E+00
05074	583708C0 (00243)	DATA	0.7126174E+00
05076	5FA827C0 (00244)	DATA	0.7473192E+00
05078	63A18940 (00245)	DATA	0.7783729E+00
0507A	672C1E40 (00246)	DATA	0.8060339E+00
0507C	6A5029C0 (00247)	DATA	0.8305714E+00
0507E	6D1608C0 (00248)	DATA	0.8522588E+00
05080	6F80E4C0 (00249)	DATA	0.8713652E+00
05082	71AE5400 (00250)	DATA	0.8981499E+00
05084	73900AC0 (00251)	DATA	0.9028581E+00
05086	75364400 (00252)	DATA	0.9157186E+00
05088	76A60840 (00253)	DATA	0.9269419E+00
0508A	77E67R40 (00254)	DATA	0.9367199E+00
0508C	78FD2EC0 (00255)	DATA	0.9452265E+00
0508E	79EF5F40 (00256)	DATA	0.9526176E+00
05090	F3C82EC0 (00258)	DATA	-0.9045466E+00
05092	F1EAFAC0 (00259)	DATA	-0.8899835E+00
05094	EFC9FC00 (00260)	DATA	-0.8733463E+00
05096	ED5C8CC0 (00261)	DATA	-0.8543869E+00
05098	E9A97C00 (00262)	DATA	-0.8328428E+00
0509A	E77AF8C0 (00263)	DATA	-0.8084403E+00
0509C	E3F492C0 (00264)	DATA	-0.7809013E+00
0509E	0FF561C0 (00265)	DATA	-0.7499506E+00
050A0	088FD240 (00266)	DATA	-0.7153266E+00
050A2	06A12DC0 (00267)	DATA	-0.6767938E+00
050A4	012C1440 (00268)	DATA	-0.6341577E+00
050A6	082C0A40 (00269)	DATA	-0.5872015E+00
050A8	C49F0E40 (00270)	DATA	-0.5361040E+00
050AA	0862A400 (00271)	DATA	-0.4805695E+00
050AC	85E5F3C0 (00272)	DATA	-0.4210801E+00
050AE	ADC6F2C0 (00273)	DATA	-0.3576339E+00
050B0	A53D2400 (00274)	DATA	-0.2907050E+00
050B2	9C435C00 (00275)	DATA	-0.2200049E+00
050B4	930407C0 (00276)	DATA	-0.1495615E+00
050B6	898F9840 (00277)	DATA	-0.7469469E-01
050B8	8800F3B0 (00278)	DATA	-0.5960465E-07
050BA	F98F9740 (00279)	DATA	0.7469457E-01
050BC	13040640 (00280)	DATA	0.1485603E+00
050BE	1C4354C0 (00281)	DATA	0.2200040E+00
050C0	253D06C0 (00282)	DATA	0.2907048E+00
050C2	20C6F1C0 (00283)	DATA	0.3576338E+00
050C4	30E5F340 (00284)	DATA	0.4210800E+00
050C6	3D929400 (00285)	DATA	0.4905608E+00
050C8	449F0E40 (00286)	DATA	0.5361040E+00

DKTAB3:

050CA 482C940 (00287)	DATA	0.5872014E+00
050CC 512C1340 (00288)	DATA	0.6341576E+00
050CE 56A12DC0 (00289)	DATA	0.6767938E+00
DKTAB4:		
05000 A2027140 (00291)	DATA	-0.2720472E+00
05002 97081140 (00292)	DATA	-0.1839315E+00
05004 080F7640 (00293)	DATA	-0.9275703E-01
05006 FFFFCF39 (00294)	DATA	-0.3725290E-00
05008 080F7640 (00295)	DATA	0.9275702E-01
0500A 17081140 (00296)	DATA	0.1839315E+00
0500C 22027140 (00297)	DATA	0.2720472E+00
0500E 208AE40 (00298)	DATA	0.3550252E+00
050E0 37957E40 (00299)	DATA	0.4342497E+00
050E2 40084040 (00300)	DATA	0.5066010E+00
050E4 49464C40 (00301)	DATA	0.5724578E+00
050E6 500AAC0 (00302)	DATA	0.6316733E+00
050E8 57983F40 (00303)	DATA	0.6843337E+00
050EA 50870540 (00304)	DATA	0.7307078E+00
050EC 62868140 (00305)	DATA	0.7711945E+00
050EE 67340AC0 (00306)	DATA	0.8062757E+00
DKTAB5:		
050F0 C9C0E940 (00307)	DATA	-0.5761997E+00
050F2 C26759C0 (00308)	DATA	-0.5187790E+00
050F4 8A66CC0 (00310)	DATA	-0.4562622E+00
050F6 81C73240 (00311)	DATA	-0.3880915E+00
050F8 A8965640 (00312)	DATA	-0.3170879E+00
050FA 9EE02C0 (00313)	DATA	-0.2414554E+00
050FC 94D5AEC0 (00314)	DATA	-0.1627711E+00
050FE 8A7DAE40 (00315)	DATA	-0.8196465E-01
05E00 9E2E7180 (00316)	DATA	-0.575650E-04
05E02 0A79EEC0 (00317)	DATA	0.8184609E-01
05E04 140202C0 (00318)	DATA	0.1626590E+00
05E06 1EE47540 (00319)	DATA	0.2413470E+00
05E08 2892F1C0 (00320)	DATA	0.3169044E+00
05E0A 31C3FEC0 (00321)	DATA	0.3807939E+00
05E0C 3A63CFC0 (00322)	DATA	0.4561710E+00
05E0E 42649740 (00323)	DATA	0.5186948E+00
DKTAB6:		
05E10 A1972940 (00324)	DATA	-0.2624256E+00
05E12 96001940 (00325)	DATA	-0.1772491E+00
05E14 086F3940 (00326)	DATA	-0.8933100E-01
05E16 900003A (00327)	DATA	-0.7450581E-00
05E18 086F3940 (00328)	DATA	0.8933177E-01
05E1A 16801940 (00329)	DATA	0.1772491E+00
05E1C 21972940 (00330)	DATA	0.2624256E+00
05E1E 28FE5D40 (00331)	DATA	0.3437001E+00
05E20 35C6E5C0 (00332)	DATA	0.4201324E+00
05E22 3E0A3CC0 (00333)	DATA	0.4910351E+00
05E24 472A5840 (00334)	DATA	0.5559788E+00
05E26 4E8108C0 (00335)	DATA	0.6147767E+00
05E28 556F1640 (00336)	DATA	0.6674526E+00
05E2A 5B6A2E40 (00337)	DATA	0.7142044E+00
05E2C 60AF01C0 (00338)	DATA	0.7553408E+00

PAGE 11: CBBN-TENEXDJ<MAP>BBN300.MSD.61, 30-Dec-79 16:14:24, Ed: KFIELD  
DECODING TABLES

05E2E 6540AB40 (00340)	DATA	0.7912802E+00
05E30 003411 (00341)	DATA	-0.5017928E+00
05E32 003421 (00342)	DATA	-0.3916729E+00
05E34 003431 (00343)	DATA	-0.2690596E+00
05E36 003441 (00344)	DATA	-0.1370569E+00
05E38 003451 (00345)	DATA	-0.1490116E-07
05E3A 003461 (00346)	DATA	0.1370568E+00
05E3C 003471 (00347)	DATA	0.2690595E+00
05E3E 003481 (00348)	DATA	0.3916729E+00
05E40 003491 (00349)	DATA	-0.2313469E+00
05E42 003501 (00350)	DATA	-0.1172924E+00
05E44 003511 (00351)	DATA	-0.5757022E-04
05E46 003521 (00352)	DATA	0.1171789E+00
05E48 003531 (00353)	DATA	0.2312379E+00
05E4A 003541 (00354)	DATA	0.3392760E+00
05E4C 003551 (00355)	DATA	0.4390484E+00
05E4E 003561 (00356)	DATA	0.5290526E+00
05E50 003571 (00357)	DATA	
05E52 003581 (00358)	DATA	
05E54 003591 (00359)	DATA	
05E56 003601 (00360)	DATA	
05E58 003611 (00361)	DATA	
05E5A 003621 (00362)	DATA	
05E5C 003631 (00363)	DATA	
05E5E 003641 (00364)	DATA	
05E60 003651 (00365)	DATA	
05E62 003661 (00366)	DATA	
05E64 003671 (00367)	DATA	
05E66 003681 (00368)	DATA	
05E68 003691 (00369)	DATA	
05E6A 003701 (00370)	DATA	
05E6C 003711 (00371)	DATA	
05E6E 003721 (00372)	DATA	
05E70 003731 (00373)	DATA	
05E72 003741 (00374)	DATA	
05E74 003751 (00375)	DATA	
05E76 003761 (00376)	DATA	
05E78 003771 (00377)	DATA	
05E7A 003781 (00378)	DATA	
05E7C 003791 (00379)	DATA	
05E7E 003801 (00380)	DATA	
05E80 003811 (00381)	DATA	
05E82 003821 (00382)	DATA	
05E84 003831 (00383)	DATA	
05E86 003841 (00384)	DATA	
05E88 003851 (00385)	DATA	
05E8A 003861 (00386)	DATA	
05E8C 003871 (00387)	DATA	
05E8E 003881 (00388)	DATA	
05E90 003891 (00389)	DATA	
05E92 003901 (00390)	DATA	
05E94 003911 (00391)	DATA	
05E96 003921 (00392)	DATA	
05E98 003931 (00393)	DATA	
05E9A 003941 (00394)	DATA	
05E9C 003951 (00395)	DATA	
05E9E 003961 (00396)	DATA	
05EA0 003971 (00397)	DATA	
05EA2 003981 (00398)	DATA	
05EA4 003991 (00399)	DATA	
05EA6 004001 (00400)	DATA	
05EA8 004011 (00401)	DATA	
05EAA 004021 (00402)	DATA	
05EAC 004031 (00403)	DATA	
05EAE 004041 (00404)	DATA	
05EAC 004051 (00405)	DATA	
05EAE 004061 (00406)	DATA	
05EAC 004071 (00407)	DATA	
05EAE 004081 (00408)	DATA	
05EAC 004091 (00409)	DATA	
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05EAC 004111 (00411)	DATA	
05EAE 004121 (00412)	DATA	
05EAC 004131 (00413)	DATA	
05EAE 004141 (00414)	DATA	
05EAC 004151 (00415)	DATA	
05EAE 004161 (00416)	DATA	
05EAC 004171 (00417)	DATA	
05EAE 004181 (00418)	DATA	
05EAC 004191 (00419)	DATA	
05EAE 004201 (00420)	DATA	
05EAC 004211 (00421)	DATA	
05EAE 004221 (00422)	DATA	
05EAC 004231 (00423)	DATA	
05EAE 004241 (00424)	DATA	
05EAC 004251 (00425)	DATA	
05EAE 004261 (00426)	DATA	
05EAC 004271 (00427)	DATA	
05EAE 004281 (00428)	DATA	
05EAC 004291 (00429)	DATA	
05EAE 004301 (00430)	DATA	
05EAC 004311 (00431)	DATA	
05EAE 004321 (00432)	DATA	
05EAC 004331 (00433)	DATA	
05EAE 004341 (00434)	DATA	
05EAC 004351 (00435)	DATA	
05EAE 004361 (00436)	DATA	
05EAC 004371 (00437)	DATA	
05EAE 004381 (00438)	DATA	
05EAC 004391 (00439)	DATA	
05EAE 004401 (00440)	DATA	
05EAC 004411 (00441)	DATA	
05EAE 004421 (00442)	DATA	
05EAC 004431 (00443)	DATA	
05EAE 004441 (00444)	DATA	
05EAC 004451 (00445)	DATA	
05EAE 004461 (00446)	DATA	
05EAC 004471 (00447)	DATA	
05EAE 004481 (00448)	DATA	
05EAC 004491 (00449)	DATA	
05EAE 004501 (00450)	DATA	
05EAC 004511 (00451)	DATA	
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05EAC 004531 (00453)	DATA	
05EAE 004541 (00454)	DATA	
05EAC 004551 (00455)	DATA	
05EAE 004561 (00456)	DATA	
05EAC 004571 (00457)	DATA	
05EAE 004581 (00458)	DATA	
05EAC 004591 (00459)	DATA	
05EAE 004601 (00460)	DATA	
05EAC 004611 (00461)	DATA	
05EAE 004621 (00462)	DATA	
05EAC 004631 (00463)	DATA	
05EAE 004641 (00464)	DATA	
05EAC 004651 (00465)	DATA	
05EAE 004661 (00466)	DATA	
05EAC 004671 (00467)	DATA	
05EAE 004681 (00468)	DATA	
05EAC 004691 (00469)	DATA	
05EAE 004701 (00470)	DATA	
05EAC 004711 (00471)	DATA	
05EAE 004721 (00472)	DATA	
05EAC 004731 (00473)	DATA	
05EAE 004741 (00474)	DATA	
05EAC 004751 (00475)	DATA	
05EAE 004761 (00476)	DATA	
05EAC 004771 (00477)	DATA	
05EAE 004781 (00478)	DATA	
05EAC 004791 (00479)	DATA	
05EAE 004801 (00480)	DATA	
05EAC 004811 (00481)	DATA	
05EAE 004821 (00482)	DATA	
05EAC 004831 (00483)	DATA	
05EAE 004841 (00484)	DATA	
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05EAE 004861 (00486)	DATA	
05EAC 004871 (00487)	DATA	
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05EAE 004941 (00494)	DATA	
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05EAC 004971 (00497)	DATA	
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05EAC 005011 (00501)	DATA	
05EAE 005021 (00502)	DATA	
05EAC 005031 (00503)	DATA	
05EAE 005041 (00504)	DATA	
05EAC 005051 (00505)	DATA	
05EAE 005061 (00506)	DATA	
05EAC 005071 (00507)	DATA	
05EAE 005081 (00508)	DATA	
05EAC 005091 (00509)	DATA	
05EAE 005101 (00510)	DATA	
05EAC 005111 (00511)	DATA	
05EAE 005121 (00512)	DATA	
05EAC 005131 (00513)	DATA	
05EAE 005141 (00514)	DATA	
05EAC 005151 (00515)	DATA	
05EAE 005161 (00516)	DATA	
05EAC 005171 (00517)	DATA	
05EAE 005181 (00518)	DATA	
05EAC 005191 (00519)	DATA	
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05EAC 005231 (00523)	DATA	
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05EAC 005251 (00525)	DATA	
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05EAC 005271 (00527)	DATA	
05EAE 005281 (00528)	DATA	
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05EAE 005301 (00530)	DATA	
05EAC 005311 (00531)	DATA	
05EAE 005321 (00532)	DATA	
05EAC 005331 (00533)	DATA	
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05EAC 005351 (00535)	DATA	
05EAE 005361 (00536)	DATA	
05EAC 005371 (00537)	DATA	
05EAE 005381 (00538)	DATA	
05EAC 005391 (00539)	DATA	
05EAE 005401 (00540)	DATA	
05EAC 005411 (00541)	DATA	
05EAE 005421 (00542)	DATA	
05EAC 005431 (00543)	DATA	
05EAE 005441 (00544)	DATA	
05EAC 005451 (00545)	DATA	
05EAE 005461 (00546)	DATA	
05EAC 005471 (00547)	DATA	
05EAE 005481 (00548)	DATA	
05EAC 005491 (00549)	DATA	
05EAE 005501 (00550)	DATA	
05EAC 005511 (00551)	DATA	
05EAE 005521 (00552)	DATA	
05EAC 005531 (00553)	DATA	
05EAE 005541 (00554)	DATA	
05EAC 005551 (00555)	DATA	
05EAE 005561 (00556)	DATA	
05EAC 005571 (00557)	DATA	
05EAE 005581 (00558)	DATA	
05EAC 005591 (00559)	DATA	
05EAE 005601 (00560)	DATA	
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05EAC 005631 (00563)	DATA	
05EAE 005641 (00564)	DATA	
05EAC 005651 (00565)	DATA	
05EAE 005661 (00566)	DATA	
05EAC 005671 (00567)	DATA	
05EAE 005681 (00568)	DATA	
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05EAC 005711 (00571)	DATA	
05EAE 005721 (00572)	DATA	
05EAC 005731 (00573)	DATA	
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05EAC 005751 (00575)	DATA	
05EAE 005761 (00576)	DATA	
05EAC 005771 (00577)	DATA	
05EAE 005781 (00578)	DATA	
05EAC 005791 (00579)	DATA	
05EAE 005801 (00580)	DATA	
05EAC 005811 (00581)	DATA	
05EAE 005821 (00582)	DATA	
05EAC 005831 (00583)	DATA	
05EAE 005841 (00584)	DATA	
05EAC 005851 (00585)	DATA	
05EAE 005861 (00586)	DATA	
05EAC 005871 (00587)	DATA	
05EAE 005881 (00588)	DATA	
05EAC 005891 (00589)	DATA	
05EAE 005901 (00590)	DATA	
05EAC 005911 (00591)	DATA	
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05EAC 005931 (00593)	DATA	
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05EAC 005951 (00595)	DATA	
05EAE 005961 (00596)	DATA	
05EAC 005971 (00597)	DATA	
05EAE 005981 (00598)	DATA	
05EAC 005991 (00599)	DATA	
05EAE 006001 (00600)	DATA	
05EAC 006011 (00601)	DATA	
05EAE 006021 (00602)	DATA	
05EAC 006031 (00603)	DATA	
05EAE 006041 (00604)	DATA	
05EAC 006051 (00605)	DATA	
05EAE 006061 (00606)	DATA	
05EAC 006071 (00607)	DATA	
05EAE 006081 (00608)	DATA	
05EAC 006091 (00609)	DATA	
05EAE 006101 (00610)	DATA	
05EAC 006111 (00611)	DATA	
05EAE 006121 (00612)	DATA	
05EAC 006131 (00613)	DATA	
05EAE 006141 (00614)	DATA	
05EAC 006151 (00615)	DATA	
05EAE 006161 (00616)	DATA	
05EAC 006171 (00617)	DATA	
05EAE 006181 (00618)	DATA	
05EAC 006191 (00619)	DATA	
05EAE 006201 (00620)	DATA	
05EAC 006211 (00621)	DATA	
05EAE 006221 (00622)	DATA	
05EAC 006231 (00623)	DATA	
05EAE 006241 (00624)	DATA	

05E72	5486053E (00393)	DATA	0.2579453E-02
05E74	5D18213E (00394)	DATA	0.2041369E-02
05E76	608F563E (00395)	DATA	0.3129800E-02
05E78	78F9493E (00396)	DATA	0.3447686E-02
05E7A	7C71E08E (00397)	DATA	0.3797761E-02
05E7C	00914C3F (00398)	DATA	0.4183303E-02
05E7E	070000F (00399)	DATA	0.4600161E-02
05E80	0A65528F (00400)	DATA	0.5076071E-02
05E82	00738D3F (00401)	DATA	0.5591492E-02
05E84	0C9D303F (00402)	DATA	0.6159248E-02
05E86	0DE51C8F (00403)	DATA	0.6704653E-02
05E88	0F4E4C3F (00404)	DATA	0.7473562E-02
05E8A	100C293F (00405)	DATA	0.8232423E-02
05E8C	1292688F (00406)	DATA	0.9068338E-02
05E8E	14752E3F (00407)	DATA	0.9989130E-02
05E90	1688F63F (00408)	DATA	0.1100342E-01
05E92	18D28C8F (00409)	DATA	0.1212070E-01
05E94	1B57FE8F (00410)	DATA	0.1335143E-01
05E96	1E1EC43F (00411)	DATA	0.1470712E-01
05E98	212D863F (00412)	DATA	0.1620047E-01
05E9A	248C293F (00413)	DATA	0.1784546E-01
05E9C	20422C8F (00414)	DATA	0.1965747E-01
05E9E	2C58A88F (00415)	DATA	0.2165348E-01
05EA0	30D9668F (00416)	DATA	0.2385216E-01
05EA2	35CF308F (00417)	DATA	0.2627409E-01
05EA4	3B45E93F (00418)	DATA	0.2894194E-01
05EA6	414AA83F (00419)	DATA	0.3180068E-01
05EA8	47E8D83F (00420)	DATA	0.3511783E-01
05EAA	4F39608F (00421)	DATA	0.3968366E-01
05EAC	57440D3F (00422)	DATA	0.4251158E-01
05EAE	6021338F (00423)	DATA	0.4693033E-01
05E80	69E40E3F (00424)	DATA	0.5170441E-01
05E82	74A4873F (00425)	DATA	0.5695444E-01
05E84	0007C8C0 (00426)	DATA	0.6273756E-01
05E86	00D086C0 (00427)	DATA	0.6910708E-01
05E88	00E7740 (00428)	DATA	0.7612504E-01
05E8A	0A88C040 (00429)	DATA	0.8385473E-01
05E8C	00D2C1C0 (00430)	DATA	0.9236928E-01
05E8E	00061740 (00431)	DATA	0.1017484E+00
05EC0	0E58A240 (00432)	DATA	0.1120799E+00
05EC2	0FCD0CC0 (00433)	DATA	0.1234604E+00
05EC4	11695440 (00434)	DATA	0.1359964E+00
05EC6	132CD2C0 (00435)	DATA	0.14398054E+00
05EC8	151F43C0 (00436)	DATA	0.1650166E+00
05ECA	17444FC0 (00437)	DATA	0.1817722E+00
05ECC	19A11D40 (00438)	DATA	0.2002293E+00
05ECE	1C3852C0 (00439)	DATA	0.2205604E+00
05ED0	75C3258F (00440)	DATA	0.250112E-01
05ED2	15E3304F (00441)	DATA	0.1709973E+00
05ED4	23D84CC0 (00442)	DATA	0.2801300E+00
05ED6	30F40A40 (00443)	DATA	0.3824723E+00
05ED8	3CF486C0 (00444)	DATA	0.4762181E+00

DTQTAB:

PAGE 13: [88N-TENEXD]<MAP>88N380.MSD.61, 30-Dec-79 16:14:24, Ed: KFIELD  
DECODING TABLES

05EDA 478692C0 (00446)	DATA	0.5602592E+00
05EDC 512C140 (00447)	DATA	0.6341577E+00
05EDE 59596740 (00448)	DATA	0.6990409E+00
05EE0 60509040 (00449)	DATA	0.7524586E+00
05EE2 662C0DC0 (00450)	DATA	0.7902347E+00
05EE4 68001140 (00451)	DATA	0.8363363E+00
05EE6 6F132640 (00452)	DATA	0.8677719E+00
05EE8 725E09C0 (00453)	DATA	0.8935196E+00
05EEA 750DC8C0 (00454)	DATA	0.9144832E+00
05EEC 773A5F40 (00455)	DATA	0.9314609E+00
05EEE 78FB92C0 (00456)	DATA	0.9451774E+00
(00457) *		
(00458) *		
(00459)		

PAGE 14: [88N-TENEXD]<MAP>88E300-MS0-61, 3e-Dec-79 16:14:24, E0: KFIELD  
APU3-VLTSY VECTOR LATTICE SYNTHESIS FILTER

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      (00460) * APU3-VLTSY VECTOR LATTICE SYNTHESIS FILTER
000000003 (00461) FM=3
      (00462) *
      (00463) *
      (00464) *
      (00465) *
      (00466) *
      (00467) *
      (00468) *
      (00469) *
      (00470) *
      (00471) *
      (00472) *
      (00473) *
      (00474) *
      (00475) *10
      (00476) *
      (00477) *20
      (00478) *
      (00479) *
      (00480) * THERE ARE THREE INPUT VECTORS:
      (00481) * K (LENGTH 8) REFLECTION COEFFICIENTS (V BID)
      (00482) * G (LENGTH 9) FILTER MEMORY (U BID)
      (00483) * W (LENGTH N) INPUT RESIDUAL SAMPLES (W BID)
      (00484) * THERE ARE TWO OUTPUT ARRAYS:
      (00485) * G AS BEFORE
      (00486) * Y OUTPUT SYNTHETIC SPEECH SAMPLES (LENGTH N) (Y BID)
      (00487) *
      (00488) * INPUT STREAM:
      (00489) * G(7)
      (00490) * G(6)
      (00491) * ...
      (00492) * G(8)
      (00493) * K(7)
      (00494) * ...
      (00495) * ...
      (00496) * K(1)
      (00497) * W(1)
      (00498) * W(2)
      (00499) * ...
      (00500) * W(N).
      (00501) * OUTPUT STREAM:
      (00502) * Y(1)
      (00503) * Y(2)
      (00504) * ...
      (00505) * Y(N)
      (00506) * G(7)
      (00507) * G(6)
      (00508) * ...
      (00509) * G(8).
      (00510) *
      (00511) *
      (00512) *

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(00513) * REGISTER USAGE:
(00514) * LEFT RIGHT
(00515) * M0=C0 M0=C0
(00516) * M1=C3 M1=C2
(00517) * M2=C5 M2=C4
(00518) * M3=C7 M3=C6
(00519) * NOTE: THESE ARE BEGINNING VALUES, ONLY. AFTER EACH C IS USED FOR THE FINAL T1
(00520) * THE REGISTER IS THEN USED FOR TEMP STORAGE FOR F(J), AND THEN FOR
(00521) * THE G'S FOR THE NEXT INPUT SAMPLE
(00522) *
(00523) * M4=K2 M4=K1
(00524) * M5=K4 M5=K3
(00525) * M6=K6 M6=K5
(00526) * M7=K8 M7=K7
(00527) *
(00528) * A0=INPUT,TEMP A0=TEMP
(00529) * A1=C1 A1=C0
(00530) * A2=TEMP A2=TEMP
(00531) * A3=C3 A3=C2
(00532) * A4=TEMP A4=TEMP
(00533) * A5=C5 A5=C4
(00534) * A6=TEMP A6=TEMP
(00535) * A7=C7 A7=C6
(00536) *
(00537) *
(00538) *
(00539) *
(00540) *
(00541) *
(00542) *
(00543) *
(00544) *
(00545) * VLTSY$ BEGIN APU(VLTSY)
(00546) * #A=0
(00547) * VLTSY$A=#A
(00548) * MOV(IQ,M3)\NOP ;G7
(00549) * MOV(IQ,A7)\NOP ;G7
(00550) * NOP\MOV(IQ,M3) ; ,G6
(00551) * NOP\MOV(IQ,A7) ; ,G6
(00552) * MOV(IQ,M2)\NOP ;G5
(00553) * MOV(IQ,A5)\NOP ;G5
(00554) * NOP\MOV(IQ,M2) ; ,G4
(00555) * NOP\MOV(IQ,A5) ; ,G4
(00556) * MOV(IQ,M1)\NOP ;G3
(00557) * MOV(IQ,A3)\NOP ;G3
(00558) * NOP\MOV(IQ,M1) ; ,G2
(00559) * NOP\MOV(IQ,A3) ; ,G2
(00560) * MOV(IQ,M4)\NOP ;G1
(00561) * MOV(IQ,A1)\NOP ;G1
(00562) * NOP\MOV(IQ,M4) ; ,G0
(00563) * NOP\MOV(IQ,A1) ; ,G0
(00564) * MOV(IQ,M7)\NOP ;K8
(00565) * NOP\MOV(IQ,A7) ; ,K7

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A12 05F16 00000000 (005566)
A13 05F18 00000000 (005567)
A14 05F1A 00000000 (005568)
A15 05F1C 00000000 (005569)
A16 05F1E 00000000 (005570)
A17 05F20 00000000 (005571)
A18 05F22 00000000 (005572)
A19 05F24 00000000 (005573)
A1A 05F26 00000000 (005574)
A1B 05F28 00000000 (005575)
A1C 05F2A 00000000 (005576)
A1D 05F2C 00000000 (005577)
A1E 05F2E 00000000 (005578)
A1F 05F30 00000000 (005579)
A20 05F32 00000000 (005580)
A21 05F34 00000000 (005581)
A22 05F36 00000000 (005582)
A23 05F38 00000000 (005583)
A24 05F3A 00000000 (005584)
A25 05F3C 00000000 (005585)
A26 05F3E 00000000 (005586)
A27 05F40 00000000 (005587)
A28 05F42 00000000 (005588)
A29 05F44 00000000 (005589)
A2A 05F46 00000000 (005590)
A2B 05F48 00000000 (005591)
A2C 05F4A 00000000 (005592)
A2D 05F4C 00000000 (005593)
A2E 05F4E 00000000 (005594)
A2F 05F50 00000000 (005595)
A30 05F52 00000000 (005596)
A31 05F54 00000000 (005597)
A32 05F56 00000000 (005598)
A33 05F58 00000000 (005599)
A34 05F5A 00000000 (005600)
A35 05F5C 00000000 (005601)
A36 05F5E 00000000 (005602)
A37 05F60 00000000 (005603)
A38 05F62 00000000 (005604)
A39 05F64 00000000 (005605)
A3A 05F66 00000000 (005606)
A3B 05F68 00000000 (005607)
A3C 05F6A 00000000 (005608)
A3D 05F6C 00000000 (005609)
A3E 05F6E 00000000 (005610)
A3F 05F70 00000000 (005611)
A40 05F72 00000000 (005612)
A41 05F74 00000000 (005613)
A42 05F76 00000000 (005614)
A43 05F78 00000000 (005615)
A44 05F7A 00000000 (005616)
A45 05F7C 00000000 (005617)

MOV(IQA,M6)\NOP ;K6
NOP\MOV(IQA,M6) ; ,K5
MUL(M3,M7)\MUL(M3,M7) ;G7*K8,G6*K7
MOV(IQA,M5)\NOP ;K4
NOP\MOV(IQA,M5) ; ,K3
MOV(IQA,M4)\NOP ;K2
NOP\MOV(IQA,M4) ; ,K1
MOV(IQA,M3)\NOP ;F8=INPUT
MOV(P,A6)\NOP ;G7*K8
SUB(A8,A6)\NOP ;F8-G7*K8>F7
MUL(M2,M6)\MOV(A6),MUL(M2,M6) ;G5*K6,G4*K5
MOV(R,EX0)\NOP ;F7
NOP\MOV(EXI,A0) ; ,F7
NOP\SUB(A8,A6) ; ,F7-G6*K7>F6
KOP\MOV(R,M3) ; ,F6
MOV(A4),MUL(M1,M5)\MOV(A4),MUL(M3,M7) ;(G5*K6)G3*K4;(G4K5)F6K7
NOP\MOV(R,EX0) ; ,F6
MOV(EXI,A6)\NOP ;F5
SUB(A6,A4)\NOP ;F6-G5K6>F5
MOV(R,EX0)\NOP ;F5
MOV(R,M2)\MOV(EXI,A2) ;F5,F5
MOV(A2),MUL(M2,M6)\MOV(A6),MUL(M1,M5) ;(G3K4)F5K6,(F6K7)G2K3
KOP\SUB(A2,A4) ; ,F5-G4K5>F4
NOP\MOV(R,EX0) ; ,F4
MOV(EXI,A4)\NOP ;F4
SUB(A4,A2)\MOV(M2),ADD(A7,A6) ;F4-G3K4>F3,(F4)G6+P6K7>G7
MOV(A6),MUL(M2,M4)\MOV(A2),MUL(M2,M6) ;(F5K6)G1K2,(G2K3)F4K5
MOV(P,EX0)\MOV(R,EX0) ;F3,G7
MOV(EXI,M3)\MOV(EXI,A4) ;G7(NEW),F3
MOV(M1),ADD(A5,A6)\SUB(A4,A2) ;(F3)G5+P5K6>G6,F3-G2K3>F2
MOV(EXI,A7)\NOP ;G7(NEW)
MOV(AP),MUL(M1,M5)\MOV(A4),MUL(M0,M4) ;(G1K2)F3K4,(F4K5)G0K1
NOP\MOV(R,EX0) ; ,F2
MOV(EXI,A2)\NOP ;F2
MOV(EX0),SUB(A2,A0)\MOV(M1),ADD(A5,A4) ;(G6)F2-G1K2>F1,(F2)G4+
;F4K5>F5
NOP\MOV(EXI,A7) ; ,G6(NEW)
NOP\MOV(EXI,M3) ; ,G6(NEW)
MOV(R,EX0)\MOV(R,EX0) ;F1,G5
MOV(R,M0)\MOV(EXI,A2) ;F1,F1
MOV(A4),MUL(M0,M4)\MOV(A0),MUL(M1,M5) ;(F3K4)F1K2,(G0K1)F2K3
ADD(A3,A4)\SUB(A2,A0) ;G3+P3K4>G4,F1-G0K1>F0
MOV(EXI,A5)\NOP ;G5(NEW)
MOV(EXI,A2)\NOP ;G5(NEW)
MOV(R,EX0)\MOV(R,M0) ;G4,F0=GF(NEW)
MOV(P,A2)\MOV(A2),MUL(M0,M4) ;F1K2,F0K1
ADD(A1,A2)\MOV(EX0),ADD(A3,A2) ;G1+P1K2>G2,(F0)G2+P2K3>G3
MOV(EXI,M0)\MOV(EXI,M2) ;F0 OUT,G4(NEW)
MOV(EXI,A1)\MOV(EXI,A5) ;F0=G0,G4(NEW)
MOV(R,EX0)\NOP ;G2
JUMPS(LD0NE,F1) ;END IF INPUT USED UP
MOV(IQA,A0)\NOP ;F0(NEW)=INPUT
MUL(M3,M7)\MOV(A0),MUL(M3,M7) ;G7K8,(F0K1)G6K7

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PAGE 17: CBBN-TEMEXD<MAP>8BN303.MSO.61, 30-Dec-79 16:14:24, EQ: KFIELD
APU3-VLTSY VECTOR LATTICE SYNTHESIS FILTER

A46 05F7E 02204038 (00619) R(A1)\MOV(EXO),ADD(A1,A0) ;F0=G0,(G3)G0+F0K1>G1
A47 05F80 08490849 (00620) MOV(EXI,M1)\MOV(EXI,M1) ;G3(NEW),G2(NEW)
A48 05F82 08530853 (00621) MOV(EXI,A3)\MOV(EXI,A3) ;G3(NEW),G2(NEW)
A49 05F84 08900890 (00622) MOV(R,EXO)\MOV(R,EXO) ;G0=F0,G1
A4A 05F86 09400000 (00623) MOV(EXI,M0)\NOP ;G1(NEW)
A4B 05F88 08510851 (00624) MOV(EXI,A1)\MOV(EXI,A1) ;G1(NEW),G0(NEW)
A4C 05F8A 85560856 (00625) MOV(A6),MUL(M2,M6)\MOV(A6),MUL(M2,M6) ;(G7K8)G5K6,(G6K7)G4K5
A4D 05F9C 4E000000 (00626) SUB(A0,A6)\NOP ;F0-G7K8>F7
A4E 05F8E 10000010 (00627) JUMP(LOOP)

*
A4F 05F90 00000000 (00628) * FINISH LAST PART OF LAST LOOP, AND OUTPUT G'S
A50 05F92 02204038 (00630) NOP\MOV(P,A0) ; ,F0K1
A51 05F94 08530853 (00631) R(A1)\MOV(EXO),ADD(A1,A0) ;F0=G0,(G3)G0+F0K1
A52 05F96 02F002F8 (00632) MOV(EXI,A3)\MOV(EXI,A3) ;G3(NEW),G2(NEW)
A53 05F98 089C0000 (00633) MOV(EXO),R(A7)\MOV(EXO),R(A7) ;(G0)G7,(G1)G6
A54 05F9A 02A0028C (00634) MOV(R,DO)\NOP ;G7 OUT
A55 05F9C 089C0000 (00635) R(A5)\MOV(OQ),R(A5) ;G5,(G6 OUT),G4
A56 05FA2 00000000 (00636) MOV(R,DO)\NOP ;G5 OUT
A57 05FA0 089C0000 (00637) R(A3)\MOV(OQ),R(A3) ;G3 OUT
A58 05FA2 00000000 (00638) MOV(R,DO)\NOP ;G3 OUT
A59 05FA4 085C0000 (00639) NOP\MOV(R,OQ) ; ,G2 OUT
A5A 05FA6 00000000 (00640) MOV(EXI,OQ)\NOP ;G1 OUT
A5B 05FA8 20320000 (00641) NOP\MOV(EXI,OQ) ; ,G0 OUT
A5C 05FAA 00000000 (00642) CLEAR(RA)\NOP ;HALT
A5D 05FAC 10000000 (00643) NOP
A5E 05FAE 00000000 (00644) JUMP(0)

VLTSY$S2=#A-VLTSY$SA
END VLTSY$S2
EVEN

*
05FAE 00000000 (00645) *
05FAE 00000000 (00646) *
05FAE 00000000 (00647) *
05FAE 00000000 (00648) *
05FAE 00000000 (00649) *

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ADDRESS	INSTR	OPERATION	COMMENT
00703	*	GENERATE "V" ADDRESSES	
00704	*	LOAD(BR0,L21)	BR0 <= VECTOR V BASE ADDR
00705		LOAD(BR1,M\$S)	DUMMY LOAD
00706		LOAD(BR2,M\$S)	BR2 <= VECTOR V SPACING
00707		SUBL(BR2,1)	BR2 <= SPACING-1
00708	*	ADDL(BR0,7)	GEN ADDR OF V(7)
00709	#3	SUBL(BR2,1),JUMPP(#3)	ADD (7 * SPACING)
00710	*	ADD(BR0,L1P1)	BR0 <= ADDR(V(7)) + SPACING
00711	*	LOAD(BR1,7)	BR1 <= 7
00712	#4	SUB(BR0,L10,TF)	GEN ADDR(V(1))
00713	*	SUBL(BR1,1),JUMPP(#4)	LOOP 8 TIMES
00714	*	LOAD(BR1,7)	BR1 <= 7
00715	#4	SUB(BR0,L10,TF)	GEN ADDR(V(1))
00716	*	SUBL(BR1,1),JUMPP(#4)	LOOP 8 TIMES
00717	*	LOAD(BR1,7)	BR1 <= 7
00718	*	SUB(BR0,L10,TF)	GEN ADDR(V(1))
00719	*	SUBL(BR1,1),JUMPP(#5)	LOOP WBS TIMES
00720	*	LOAD(BR0,L3)	BR0 <= VECTOR W BASE ADDR
00721	*	LOAD(BR1,M\$S)	BR1 <= WBS-1
00722	*	SUB(BR0,M\$S)	BR2 <= W BASE MINUS SPACING
00723	#5	ADD(BR0,L11,TF)	GEN ADDR(W(1))
00724	*	SUBL(BR1,1),JUMPP(#5)	LOOP WBS TIMES
00725	*	WHEN DONE GENERATING INPUT ADDRESSES, CLEAR RI	
00726	#5	CLEAR(RI)	HALT INPUT
00727	*	NOP(0)	
00728	*	OUTPUT PROGRAM	
00729	*	OUTPUT PGM REG USAGE:	
00730	*	BR0: VECTOR ELEMENT ADDRESSES	
00731	*	BR1: BUFFER SIZES	
00732	*	BR2: BUFFER SPACINGS	
00733	*	BR3: SCRATCH REGISTER	
00734	*	TURN ON APU	
00735	*	GEN "Y" ADDRESSES	
00736	*	LOAD(BW0,L0)	BW0 <= Y BASE ADDR
00737	*	LOAD(BW1,M\$S)	DUMMY LOAD
00738	*	SUB(BW0,M\$S)	BW0 <= Y BASE MINUS SPACING
00739	*	GET WBS-1 (GENERATE WBS Y'S)	
00740	*	LOAD(BW3,L3)	SCRATCH <= W BASE
00741	*	LOAD(BW1,M\$S)	BR1 <= WBS-1
00742	*	SUB(BW0,M\$S)	
00743	*	GET WBS-1 (GENERATE WBS Y'S)	
00744	*	LOAD(BW3,L3)	SCRATCH <= W BASE
00745	*	LOAD(BW1,M\$S)	BR1 <= WBS-1
00746	*	SUB(BW0,M\$S)	
00747	*	GET WBS-1 (GENERATE WBS Y'S)	
00748	*	LOAD(BW3,L3)	SCRATCH <= W BASE
00749	*	LOAD(BW1,M\$S)	BR1 <= WBS-1
00750	*	SUB(BW0,M\$S)	
00751	*	GET WBS-1 (GENERATE WBS Y'S)	
00752	*	LOAD(BW3,L3)	SCRATCH <= W BASE
00753	*	LOAD(BW1,M\$S)	BR1 <= WBS-1
00754	*	SUB(BW0,M\$S)	
00755	*	GET WBS-1 (GENERATE WBS Y'S)	

PAGE 20: [BOM-TENEXD]<MAP>BOM300.VSO.61, 30-Dec-79 16:14:24, Ed: KFIELD  
APS3-V3200

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A23 05F0C 46700000 (00756) *          LOAD(BW3,MSS)          ;DUMMY LOAD
      (00757) *
A24 05F0E 48800006 (00758) #7          ADD(BW0,[0],TF)        ;GEN ADDR(Y(I))
A25 06000 4A1120B1 (00759) *          SUBL(BW1,1),JUMPP(#7)    ;LOOP WBS TIMES
      (00760) *
      (00761) *          GENERATE "U" ADDRESSES
      (00762) *
A26 060E2 4C401004 (00763) *          LOAD(BW0,[1])          ;BW0 <= VECTOR U BASE ADDRESS
A27 06004 4E500000 (00764) *          LOAD(BW1,MSS)          ;DUMMY LOAD
A28 06006 50600000 (00765) *          LOAD(BW2,MSS)          ;BW2 <= VECTOR U SPACING
A29 06008 52210031 (00766) *          SUBL(BW2,1)            ;BW2 <= SPACING-1
      (00767) *
A2A 0600A 5401003F (00768) #8          ADOL(BW0,7)           ;GEN ADDR OF U(7)
A2B 0600C 562120B1 (00769) *          SUBL(BW2,1),JUMPP(#8)    ;ADD (7*SPACING)
      (00770) *
A2C 0600E 580A900C (00771) *          ADD(BW0,[9])           ;BW0 <= ADDR(U(7)) + SPACING
      (00772) *
A2D 06010 5A500007 (00773) *          LOAD(BW1,7)            ;BW1 <= 7
A2E 06012 5C829004 (00774) #9          SUB(BW0,[9],TF)        ;GEN ADDR(U(1))
A2F 06014 5E1120B1 (00775) *          SUBL(BW1,1),JUMPP(#9)    ;LOOP 8 TIMES
      (00776) *
      (00777) *
      (00778) *
A30 06016 60200030 (00779) *          CLEAR(R0)              ;HALT OUTPUT
A31 06018 62000020 (00780) *          NOP(0)                  ;END OF MODULE
      (00781) *
      00006012 (00782) V3200$A=#C          ;ASSIGN VALUE TO CHAIN ANCHOR
      (00783) *          END          ;END OF MODULE
      (00784) *          #A-1
      (00785) *          CONSTR. INSTR. BLOCK
      (00786) *
      0601A 00000000 (00787) V3200$1 DATA 14F'0.0"
      ...
      00000000 (00788) V3200$2=#L-V3200$
      (00789) *

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M0	A[4-13](M-1)	A[4-13](1)
M1	A[4-13](M-2)	A[4-13](2) M>3
M2	A[4-13](M-3) M>5	A[4-13](3) M>5
M3	K[4]	K[4]
M4	A[4-13](M-4) M>7	A[4-13](4) M>7
M5	UNUSED	UNUSED
M6	UNUSED	UNUSED
M7	K[4]	K[4]
A0	K*A	K*A
A1	A[4](1) M>8	A[4](M) M>8
A2	A[4](2) M>2	A[4](M-1) M>2
A3	A[4](3) M>4	A[4](M-2) M>4
A4	A[4](4) M>6	A[4](M-3) M>6
A5	UNUSED	UNUSED
A6	UNUSED	UNUSED
A7	UNUSED	UNUSED

```
*****
(00835)
(00836) *
(00837)
(00838) EVEN
(00839) DATA
(00840) DATA
(00841) *
(00842) VKTODS REGIM
(00843) VKTODSSA
(00844) VKTODSSZ
(00845) APU(VKTODS)
(00846) #A=0
(00847)
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PAGE 22: [08N-TENEXD]<MAP>88N300.WSD-61, 30-Dec-79 16:14:24, Ed: KFIELD  
 APU3-VK10A COMPUTE PREDICTOR COEFS. FROM REFL-K COEFS.

00000000 (00843) *	VK10ASSA=#A	
00000000 (00844) *		
00000000 (00845) *		
A00 00000000 (00846)	MOV(IA,A1)\NOP	;K(1)=A(1)
A01 00000000 (00847)	NOP(A1)\NOP	Float IT
A02 00000000 (00848)	MOV(R,M0)\NOP	
A03 00000000 (00849)	K(+1)	GET 1.0 FOR A(0)
A04 00000000 (00850)	MOV(IA,A0)	K2
A05 00000000 (00851)	MOV(R,DQ)\NOP	A(0)
A06 00000000 (00852)	NOP(A0)	Float IT
A07 00000000 (00853)	MOV(R,M7)	
A08 00000000 (00854)	CALL(X23RD)	DO 2ND ITERATION (M=2)
A09 00000000 (00855)	CALL(X23RD)	DO THIRD ITERATION (M=4)
A10 00000000 (00856)	CALL(X4TH)	M=4
A11 00000000 (00857)	CALL(X5TH)	M=5
A12 00000000 (00858)	NOP(R,A3)	GET A(5)(3) READY
A13 00000000 (00859)	CALL(X6TH)	M=6
A14 00000000 (00860)	CALL(X7TH)	M=7
A15 00000000 (00861)	NOP(R,A4)	GET A(7)(4) READY
A16 00000000 (00862)	CALL(X8TH)	
A17 00000000 (00863)	R(A1)\NOP	
A18 00000000 (00864)	MOV(R,DQ)\NOP	A(1) OUT
A19 00000000 (00865)	R(A2)\NOP	
A20 00000000 (00866)	MOV(R,DQ)\NOP	A(2) OUT
A21 00000000 (00867)	R(A3)\NOP	
A22 00000000 (00868)	MOV(R,DQ)\NOP	A(3) OUT
A23 00000000 (00869)	R(A4)\R(A4)	
A24 00000000 (00870)	MOV(R,DQ)\NOP	A(4) OUT
A25 00000000 (00871)	NOP\MOV(R,DQ)	
A26 00000000 (00872)	NOP\R(A3)	A(5) OUT
A27 00000000 (00873)	NOP\MOV(R,DQ)	
A28 00000000 (00874)	NOP\R(A2)	A(6) OUT
A29 00000000 (00875)	NOP\MOV(R,DQ)	
A30 00000000 (00876)	NOP\R(A1)	A(7) OUT
A31 00000000 (00877)	NOP\MOV(R,DQ)	
A32 00000000 (00878)	NOP\R(A1)	A(8) OUT
A33 00000000 (00879)	NOP\MOV(R,DQ)	
A34 00000000 (00880)	CLEAR(RA)	
A35 00000000 (00881)	NOP	
A36 00000000 (00882)	JUMP(0)	
A37 00000000 (00883)		
A38 00000000 (00884)		
A39 00000000 (00885)		
A40 00000000 (00886)		
A41 00000000 (00887)		
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A289 00000000 (01135)		

PAGE 23: CBBN-TEENEXD<4P>8BN300.MSO.61, 30-Dec-79 16:14:24, Ed: KFIELD  
APU3-VKTOA COMPUTE PREDICTOR COEFS. FROM RSPLN COEFS.

A25	06082	85800000	(00896)	MUL(M3,M4)\NOP	ACM-13(M-4)*KCM3 ((M=8))
A26	06084	10800029	(00897) *	JUMP(X7TH)	
A27	06086	00000089	(00898)	NOP,MOV(R,EX0)	AL53(3)
A28	06088	00530000	(00899)	MOV(EX1,A3)\NOP	AL53(3)
A29	0608A	85708560	(00901)	MOV(A0),MUL(M2,M7)\MUL(M2,M7)	ACM-13(M-3)*KCM3\ACM-13(3)*KCM3
A2A	0609C	44000000	(00902)	ADD(A2,A4)\NOP	SUM FOR ACM3(4)
A2B	0608E	C0940000	(00903) *	MOV(R,A4)\NOP	STORE ACM3(4)
A2C	06090	1000002E	(00904)	JUMP(X5TH)	
A2D	06092	08520000	(00905)	MOV(EX1,A2)\NOP	AC33(2)
A2E	06094	84F0804F	(00906)	MOV(A0),MUL(M1,M7)\MOV(A0)	MUL(M1,M7)
A2F	06096	43004300	(00907) *	ADD(A0,A3)\ADD(A0,A3)	SUM FOR ACM3(3)\ACM3(M-3)
A30	06098	00980088	(00910)	MOV(R,EX0)\MOV(R,EX0)	
A31	0609A	084C084A	(00911) *	MOV(EX1,M4)\MOV(EX1,M2)	STORE ACM3(M-3)\ACM3(3)
A32	0609C	84708047	(00912)	MOV(A0),MUL(M0,M7)\MOV(A0)	MUL(M0,M7)
A33	0609E	42134214	(00913) *	MOV(A3),ADD(A0,A2)\MOV(A4)	ADD(A0,A2)
A34	062A0	00980088	(00915) *	MOV(R,EX0)\MOV(R,EX0)	
A35	060A2	084A0849	(00916) *	MOV(EX1,M2)\MOV(EX1,M1)	STORE ACM3(M-2)\ACM3(2)
A36	062A4	08980088	(00917) *	MOV(P,A0)\MOV(P,A0)	
A37	062A6	41241113	(00922) *	MOV(A2),ADD(A0,A1)\MOV(A3)	ADD(A0,A1)
A38	060A8	00980088	(00923) *	MOV(R,EX0)\MOV(R,EX0)	
A39	060AA	08490088	(00924) *	MOV(EX1,M1)\MOV(EX1,M0)	ACM3(M-1)\ACM3(1)
A3A	060AC	08F10082	(00925) *	MOV(R,A1)\MOV(R,A2)	ACM3(1)\ACM3(M-1)
A3B	060AE	08F70087	(00926)	MOV(IQA,A7)	ACM3(M),FIXED
A3C	060B0	32003200	(00931)	NORM(A7)	
A3D	060B2	00800089	(00932)	MOV(R,M0)\MOV(R,A1)	ACM3(M),FLOATING
A3E	060B4	08070087	(00933)	MOV(IQA,A7)	KCM+13,FIXED
A3F	060B6	32003200	(00934)	NORM(A7)	FLOAT IT
A40	062B8	08800088	(00935)	MOV(R,M3)	KCM+13,FLOATING
A41	060BA	089F0088	(00936) *	MOV(R,M7)	KCM+13,FLOATING
A42	060BC	A000A000	(00937) *	RETURN	
A43	060BE	00000043	(00938) *		
A44	060C0	00000043	(00939) *		
A45	060C2	00000043	(00940)		
A46	060C4	00000043	(00941)		
A47	060C6	00000043	(00942) *		
A48	060C8	00000043	(00943) *		
A49	060CA	00000043	(00944) *		
A50	060CB	00000043	(00945) *		
A51	060CD	00000043	(00946)		
A52	060CE	00000043	(00947)		
A53	060CF	00000043	(00948)		
A54	060D0	00000043	(00949)		
A55	060D2	00000043	(00950)		
A56	060D4	00000043	(00951)		
A57	060D6	00000043	(00952)		
A58	060D8	00000043	(00953)		
A59	060DA	00000043	(00954)		
A60	060DC	00000043	(00955)		
A61	060DE	00000043	(00956)		
A62	060E0	00000043	(00957)		
A63	060E2	00000043	(00958)		
A64	060E4	00000043	(00959)		
A65	060E6	00000043	(00960)		
A66	060E8	00000043	(00961)		
A67	060EA	00000043	(00962)		
A68	060EC	00000043	(00963)		
A69	060EE	00000043	(00964)		
A70	060F0	00000043	(00965)		
A71	060F2	00000043	(00966)		
A72	060F4	00000043	(00967)		
A73	060F6	00000043	(00968)		
A74	060F8	00000043	(00969)		
A75	060FA	00000043	(00970)		
A76	060FC	00000043	(00971)		
A77	060FE	00000043	(00972)		
A78	060FF	00000043	(00973)		
A79	06100	00000043	(00974)		



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(00945) * APS3-V1100K
(00946) *
(00947) * MAP-300 APS PROGRAM FOR VKTOA(Y,U)
(00948) * [WHERE Y'S ARE A'S; U'S ARE K'S]
(00949) * CODED BY KFIELD 4/79
(00950) *
(00951) * INPUT STREAM: U(0),U(1),...U(7),[F1]
(00952) * INPUT IS FIXED, LONG (TE) FORMAT
(00953) *
(00954) * OUTPUT STREAM: V(0),V(1),...V(8),[E0]
(00955) * OUTPUT IS FLOAT, LONG (TF) FORMAT
(00956) *
(00957) * BUFFER SIZES ARE IGNORED:
(00958) * 8 U'S ARE INPUT,
(00959) * 9 Y'S ARE OUTPUT
(00960) *
(00961) * HEADER BLOCK
(00962) *
(00963) *
(00964) *
(00965) *
(00966) * EVEN V1100K$1 ;PTR TO CONSTR. INSTR. BLK.
(00967) * ADDR 0 ;PTR TO SCALAR BLOCK (NO SCALARS)
(00968) * DATA 0 ;NUMBER OF SCALARS
(00969) * V1100K$2 ;MODULE SIZE
(00970) * ADDR V1100K$A ;PTR TO CHAIN ANCHOR
(00971) * EVEN
(00972) *
(00973) *
(00974) * V1100K$ BEGIN APS(V1100K)
(00975) *
(00976) * INPUT PROGRAM
(00977) *
(00978) * JSN(V1100K$2,P2) ;TURN ON OUTPUT GENERATION
(00979) * SET(RO) ;SET OUTPUT PC
(00980) *
(00981) * INPUT PCW REGISTER USAGE:
(00982) * BR0: VECTOR ELEMENT ADDRESSES
(00983) * BR1: BUFFER SIZES MINUS ONE (FIXED AT 7)
(00984) *
(00985) *
(00986) * GENERATE "U" ADDRESSES
(00987) *
(00988) *
(00989) * LOAD(BR0,[1]) ;BR0 <= VECTOR U BASE ADDR
(00990) * LOAD(BR1,M$) ;DUMMY LOAD
(00991) * SUB(BR0,M$) ;BR0 <= U BASE MINUS SPACING
(00992) * LOAD(BR1,7) ;SET SIZE-1 TO 7
(00993) *
(00994) * INPUT ADDRESS GENERATION LOOP
(00995) *
(00996) * #1 ADD(BR0,[9],TE) ;GEN U-ELEMENT ADDR (FIXD,LNG FORMAT)
(00997) * SUBL(BR1,1),JUMPP(#1) ;CONTINUE UNTIL LAST ELEM

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PAGE 26: C88N-TEMEXD1<MAP>8BN300.MSD.61, 30-Dec-79 16:14:24, EQ: KFIELD  
APU3 - PRTRB(Y,A,U,B,V) UPSAMPLE(3:1) WITH PERTURBATION

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(01040) * APU3 - PRTRB(Y,A,U,B,V) UPSAMPLE(3:1) WITH PERTURBATION
00000003 (01041) EN=3
(01042) *
(01043) * BINDS TO APS3 - P2120
(01044) *
(01045) * EQ.: (FOR I=0,(UBS-1)):
(01046) * Y(3I),Y(3I+1),Y(3I+2) =
(01047) *
(01048) * P=U(I)/0 IF --.5 <= (V(I)*(SA + (SB)*ABS(U(I))) < -.5
(01049) * U(I)/0,0 IF (V(I)*(SA + (SB)*ABS(U(I))) < --.5
(01050) * 0/0,U(I) IF (V(I)*(SA + (SB)*ABS(U(I))) >= .5
(01051) *
(01052) * WHERE Y IS PERTURBED UPSAMPLED OUTPUT VECTOR
(01053) * U IS DOWNSAMPLED INPUT VECTOR
(01054) * V IS VECTOR OF RANDOM NUMBERS
(01055) * SA,SB ARE EXTERNAL CONSTANTS
(01056) *
(01057) * INPUT STREAM: SA,SB,U(0),V(0),U(1),V(1),...,U(UBS-1),V(UBS-1),LF13
(01058) *
(01059) * OUTPUT STREAM: Y(0),Y(1),Y(2),...,Y((3*UBS)-1),LE0J
(01060) *
(01061) *
(01062) *
(01063) *
(01064) *
(01065) *
(01066) * PRTRBS BEGIN APU(PRTRB)
(01067) * SA=0
00000000 (01068) *
00000000 (01069) * PRTRBSA=#A
(01070) *
(01071) * #1
A00 000F8 160F1600 (01072) * K(-.5) ; R <= -.5
A01 000FA 00F000F0 (01073) * MOV(IQA,A0) ; A0 <= SA
A02 000FC 00E000E0 (01074) * MOV(IQA,M0) ; M0 <= SB
A03 000FE 00910091 (01075) * MOV(A1),K(-1.) ; A1 <= -.5 , R <= -1.
A04 00100 00920092 (01076) * MOV(R,A2) ; A2 <= -1.
A05 00102 00130013 (01077) * MOV(ZERO,A3) ; A3 <= 0.
(01078) *
A06 00104 00040000 (01079) * #2
A07 00106 00EC0000 (01279) * MOV(IQA,M4)\NOP ; LEFT:M4 <= U(I)
A08 00108 00ED0000 (01080) * MOV(IQA,M5)\NOP ; LEFT:M5 <= V(I)
(01081) *
A09 0010A 00000004 (01082) * NOP\MOV(IQA,A4) ; RIGHT:M4 <= U(I+1)
A0A 0010C 0000000C (01083) * NOP\MOV(IQA,M4) ; RIGHT:M4 <= U(I+1)
(01084) *
A0B 0010E 94009400 (01085) * MULABS(M0,M4) ; P <= SB * ABS(U(I))
(01086) *
A0C 00110 00000000 (01087) * NOP\MOV(IQA,M5) ; RIGHT:M5 <= V(I+1)
A0D 00112 00060006 (01088) * MOV(P,A6) ; A6 <= SB * ABS(U(I))
(01089) *
A0E 00114 40004000 (01090) * ADD(A0,A6) ; R <= SA + (SB * ABS(U(I)))
A0F 00116 00960096 (01091) * MOV(R,A6) ; MAKE SURE SUM IS .GE. 0
A10 00118 60006000 (01092) * MAX(A3,A6) ; (A3 = 0)

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PAGE 27: [BBN-TENEXD]<MAP>BBN300.MSD-61, 30-Dec-79 16:14:24, Ed: KFIELD  
APU3 - PTRB(Y,A,U,B,V) UPSAMPLE(3:1) WITH PERTURBATION

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A11 0611A 08090809 (01093)      MOV(R,M1)      ; M1<= SA + (SB * ABS(U(1)))
A12 0611C 84AF84A0 (01094)      MUL(M1,M5)      ; P <= V(1) * (SA + (SB ABS(U(1))))
A13 0611E 08B508B5 (01095)      MOV(P,A5)      ; A5 <= P
      (01096) *
A14 06120 45200000 (01097) * DD PERTURB ON LEFT BOARD DATA
      (01098) *
      (01099) LEFT: ADD(A1,A5)\NOP ; SEE IF THIS IS <-.5 (ADD .5, TEST FOR <0)
      (01100) * ; (DON'T WORRY ABOUT BOUNDARY CONDITION)
A15 06122 080E0900 (01101)      MOV(R,NULL)    ; DON'T DO JUMPS TIL ADD IS DONE
A16 06124 9115002C (01102)      JUMPS(LCASE1,T1) ; DO LEFT BOARD CASE 1: <-.5
A17 06126 45550000 (01103)      MOV(A5,ADD(A2,A5)\NOP ; SEE IF IT WAS >.5 (SUB .5+-.5(TO CANCEL
      (01104) * ; PREV ADD OF .5), TEST FOR >0)
A18 06128 080E0800 (01105)      MOV(R,NULL)    ; DON'T DO JUMPS TIL ADD IS DONE
A19 0612A 921E0031 (01106)      JUMPC(LCASE2,T1) ; DO LEFT BOARD CASE 2: >.5
      (01107) * ; ELSE DO LEFT BOARD CASE 3: >-.5 & <+.5
A1A 0612C 02000000 (01108) LCASE3: R(A4)\NOP ; R <= U(1)
A1B 0612E 081C0000 (01109)      MOV(ZERO,0Q)\NOP ; DON'T PERTURB
A1C 06130 089C0000 (01110)      MOV(R,0Q)\NOP
A1D 06132 081C0000 (01111)      MOV(ZERO,0Q)\NOP ; GO TEST RIGHT BOARD DATA
      (01112) *
      (01113) *
      (01114) *
A1E 06134 000E4520 (01115) RIGHT: NOP\ADD(A1,A5) ; DO PERTURBATION ON RIGHT BOARD DATA
A1F 06136 000E0800 (01116)      NOP\MOV(R,NULL)
A20 06138 911F0036 (01117)      JUMPS(RCASE1,T2)
A21 0613A 000E4555 (01118)      NOP\MOV(A5,ADD(A2,A5)
A22 0613C 000E0800 (01119)      NOP\MOV(R,NULL)
A23 0613E 901F003E (01120)      JUMPC(RCASE2,T2)
      (01121) *
A24 06140 000E0200 (01122) RCASE3: NOP\R(A4)
A25 06142 000E001C (01123)      NOP\MOV(ZERO,0Q)
A26 06144 000E009C (01124)      NOP\MOV(R,0Q)
A27 06146 000E001C (01125)      NOP\MOV(ZERO,0Q)
A28 06148 901D0006 (01126)      JUMPC(R2,F1)
A29 0614A 000E0000 (01127)      NOP
A2A 0614C 000E0000 (01128)      NOP
A2B 0614E 10000043 (01129)      JUMP(PDONE)
A2C 06150 02000000 (01130) LCASE1: R(A4)\NOP ; <-.5 , PERTURE BY -1
A2D 06152 089C0000 (01131)      MOV(R,0Q)\NOP
A2E 06154 081C0000 (01132)      MOV(ZERO,0Q)\NOP
A2F 06156 081C0000 (01133)      MOV(ZERO,0Q)\NOP
A30 06158 1000001E (01134)      JUMP(RIGHT) ; GO TEST RIGHT BOARD DATA
      (01135) *
A31 0615A 02000000 (01136) LCASE2: R(A4)\NOP ; >.5 , PERTURE BY +1
A32 0615C 081C0000 (01137)      MOV(ZERO,0Q)\NOP
A33 0615E 081C0000 (01138)      MOV(ZERO,0Q)\NOP
A34 06160 089C0000 (01139)      MOV(R,0Q)\NOP
A35 06162 1000001E (01140)      JUMP(RIGHT) ; GO TEST RIGHT BOARD DATA
      (01141) *
A36 06164 000E0200 (01142) RCASE1: NOP\R(A4)
A37 06166 000E009C (01143)      NOP\MOV(R,0Q)
A38 06168 0200001C (01144)      NOP\MOV(ZERO,0Q)
A39 0616A 000E001C (01145)      NOP\MOV(ZERO,0Q)

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PAGE 28: (8BN-TENEX)J<MAP>8BN300.WSD.61, 30-Dec-79 16:14:24, E0: KFIELD  
 AP03 - PRTRB(Y,A,U,B,V) UPSAMPLE(321) WITH PERTURBATION

A3A 0616C 901D0006	(01146)	JUMPC(R2,FI)
A3B 0616E 00000000	(01147)	NOP
A3C 06170 00000000	(01148)	NOP
A3D 06172 10000043	(01149)	JUMP(PRDONE)
	(01150) *	
A3E 06174 00000200	(01151)	RCASE2: NOP(R(A4))
A3F 06176 0000001C	(01152)	NOP(MOV(ZERO,00))
A40 06178 0000001C	(01153)	NOP(MOV(ZERO,00))
A41 0617A 0000009C	(01154)	NOP(MOV(R,00))
A42 0617C 901D0006	(01155)	JUMPC(R2,FI)
	(01156) *	
	(01157) *	
A43 0617E 20322032	(01158)	PRDONE: CLEAR(RA)
A44 06180 00000000	(01159)	NOP
A45 06182 10000000	(01160)	JUMP(0)
	(01161) *	
	(01162)	PRTRB\$SZ=#A-PRTRB\$SA
06184	(01163)	END PRTRB\$SZ
	(01164)	EVEN
	(01165) *	

```

PAGE 29: [BBN-TEWEXD]C<MAP>8BN300.MSD.61, 30-Dec-79 16:14:24, EQ: KFIELD
APS3 - P2120 APS PROGRAM FOR PRTRB(Y,A,U,B,V)

(01166) * APS3 - P2120 APS PROGRAM FOR PRTRB(Y,A,U,B,V)
(01167) *
(01168) *
(01169) * INPUT STREAM: SA,SB,U(C),V(0),U(1),V(1),...,U(UBS-1),V(UBS-1),[FI]
(01170) *
(01171) * OUTPUT STREAM: V(0),V(1),V(2),...,V((3*UBS)-1),[EO]
(01172) *
(01173) *
(01174) *
(01175) *
(01176) *
(01177) *
(01178) *
(01179) *
(01180) *
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(01207) *
(01208) *
(01209) *
(01210) *
(01211) *
(01212) *
(01213) *
(01214) *
(01215) *
(01216) *
(01217) *
(01218) *

06184 000061C0
06186 00006190
06188 0002
06189 004A
0618A 000061C0

EVEN P2120$1 ; CONSTR INSTR BLK
ADDR P2120$+2*P2120$S ; SCALAR BLK
DATA 2 ; NUMBER OF SCALARS
DATA P2120$2 ; MODULE SIZE
ADDR P2120$A ; PTR TO CHAIN ANCHOR
EVEN

P2120$ BEGIN APS(P2120)

INPUT PROGRAM
REGISTER USAGE:
BR0: SCALAR AND BUFFER 'U' ELEMENT ADDRESSES
BR1: BUFFER 'U' SIZE MINUS ONE
BR2: BUFFER 'V' ELEMENT ADDRESSES
BR3: SCRATCH

JSN(P2120$2,P2) ; SET OUTPUT PC
SET(RO) ; TURN ON OUTPUT GENERATION

P2120$S LOAD(BR0,M$S(1),TF) ; GEN SA ADDR
LOAD(BR0,M$S(1),TF) ; GEN SB ADDR
LOAD(BR0,[1]) ; LOAD U BASE ADDR
LOAD(BR1,M$S) ; LOAD U SIZE MINUS 1
SUB(BR0,M$S) ; SET BR0 TO U BASE MINUS SPACING
LOAD(BR2,[2]) ; LOAD V BASE ADDR
LOAD(BR3,M$S) ; DUMMY LOAD
SUB(BR2,M$S) ; SET BR2 TO V BASE MINUS SPACING
ADD(BR0,[9],TF) ; GEN U-ELEMENT ADDR
ADD(BR2,[10],TF) ; GEN V-ELEMENT ADDR
SUBL(BR1,1),JUMPP(#1) ; CONTINUE TIL UBS ELEMENTS
CLEAR(RI) ; HALT INPUT
NOP(0)

OUTPUT PGM
REGISTER USAGE:
(01218) *

```

PAGE 30: LBN-TEEXD<MAP>BBN300.4SO.61, 30-Dec-79 16:14:24, Ed: KFIELD  
APS3 - P2120 APS PROGRAM FOR PRTR(Y,A,U,B,V)

```

(01219) *      BW0: BUFFER 'Y' ELEMENT ADDRESSES
(01220) *      BW1: (3*UBS)-1
(01221) *      BW2: SCRATCH
(01222) *
APF 061AA 1E300F32 (01223) P2120S2 SET(RA)      ; TURN ON APU
A10 061AC 204000FA (01224) LOAD(BW0,[0])        ; LOAD Y BASE ADDR
A11 061AE 22600000 (01225) LOAD(BW2,MSS)        ; DUMMY LOAD
A12 06180 24020000 (01226) SUB(BW0,MSS)          ; SET BWP TO Y BASE MINUS SPACING
                                     ; DUMMY LOAD
A13 061B2 26601006 (01228) LOAD(BW2,[1])        ; LOAD U SEIZE MINUS ONE
A14 061B4 28500000 (01229) LOAD(BW1,MSS)        ; DUMMY LOAD
A15 061B6 2A600000 (01230) LOAD(BW2,MSS)        ; BW2 = BW1 = UBS-1
A16 061B8 2C210012 (01231) MOV8(BW2,BW1)        ; BW1 <= 2UBS-2
A17 061BA 2E11002A (01232) ADD8(BW1,BW1)        ; BW1 <= 3UBS-3
A18 061BC 3011002C (01233) ADD8(BW1,BW2)        ; BW1 <= 3UBS-1
A19 061BE 321A0002 (01234) ADD(BW1,2)           ; GEN Y-ELEMENT ADDR
A1A 061C0 340A900E (01235) *                    ; CONTINUE TIL UBS*3 ELEMENTS
A1B 061C2 36111A81 (01237) *                    ;
                                     SUBL(BW1,1),JUMPP(#3)
                                     ;
A1C 061C4 38200030 (01240) *                    ; HALT OUTPUT
A1D 061C6 3A000020 (01241) *                    ;
                                     CLEAR(R0)
                                     NOP(0)
                                     ;
                                     ; CHAIN ANCHOR
000061C0 (01243) *
000061C0 (01244) P2120SA=PC
061C8    (01245) *
                                     END      BA-1
061C8    (01246) *
061C8    (01247) *
061C8    (01248) * STORAGE BLK FOR CONSTR INSTRS
061C8    (01249) *
061C8 00000000 (01250) P2120SI DATA 7F'0.0"
...
0000004A (01251) P2120SZ=#L-P2120S      ; MODULE SIZE

```





PAGE 32: [BBN-TELEXD] <MAP> 88N300-MSO-61, 30-Dec-79 16:14:24, Ed: KFIELD  
 MWLF(Y,A,U,V) WEINER-LEVINSON MATRIX SOL-N WITH FXD PT OUTPUT

```

(01305) *
(01306) * MWLF-APU CALCULATION OF S(N)
(01307) * S(N)=R(N) + SUM(1,N-1) OF R(N-J)A(N-1,J)
(01308) * REGISTER CONTENTS AT START
(01309) * M4=P(N-1), M5=A5=E(N-1), A6=1, A7=TEST
(01310) *
(01311) * RCP(A5)
(01312) * MOV(IQA,A2)\NOP
(01313) * MOV(ZERO,M0)
(01314) * MUL(M0,M4)
(01315) * MOV(M1), R(A2)
(01316) * JUMPC(MWLFM,AF2)
(01317) *
(01318) * JUMPC(MWLFSE,AF3),SET
(01319) *
(01320) * CLEAR(AF3)
(01321) *
(01322) * #2
(01323) * MOV(IQA,M2)\NOP
(01324) * MOV(A0), MUL(M2,M6)
(01325) * MOV(A2), ADD(A0,A2)
(01326) *
(01327) * MWLFSE
(01328) * MOV(IQA,M7)\NOP
(01329) * MOV(A1), MUL(M3,M7)
(01330) * MOV(A2), ADD(A1,A2)
(01331) *
(01332) * JUMPC(F2,FWI)
(01333) *
(01334) * MOV(P,A0)
(01335) * MOV(A2), ADD(A0,A2)
(01336) *
(01337) * MWLFRE
(01338) *
(01339) *
(01340) * MWLF-APU CALCULATION OF P(N)
(01341) * P(N)=-S(N)/E(N-1)
(01342) * REGISTER CONTENTS AT START
(01343) * M4=P(N-1), A5=M5=E(N-1), A6=1, A7=TEST
(01344) * M1=RCP(E(N-1)), R=S(N)
(01345) *
(01346) *
(01347) * MUL(M1,M5)
(01348) * MOV(M2), K(2)
(01349) * MOV(P,A0)
(01350) * MOV(A1), SUB(A1,A0)
(01351) * MUL(M6), K(1)
(01352) * MUL(M1,M6)
(01353) * MOV(P,M1)
(01354) * MOV(A4), MUL(M1,M5)
(01355) * MOV(P,A0)
(01356) * MOV(A6), SUB(A6,A0)
(01357) * MOV(R,M6)

E* RCP(E)
M2=S(N)
A0=E* RCP(E)
A1=2
M6=2-E* RCP(E)
M1=M4=EIR,
EIR APPROX(1/E) TO 2*(-11)
A6=1
M6=1-EIR* E
  
```



PAGE 34: [88M-TEWERD]<MAP>88M308.MSO.61, 34-Dec-79 16:14:24, E8: KFIELD  
 WMLF(Y,A,U,V) WEINER-LEVINSON MATRIX SOL'N WITH FXD PT OUTPUT

```

(01411) *
(01412) * M8=M4=A4=P(N), M5=M5=E(N-1), A6=1, A7=TEST
(01413) * P=P(N)P(N),R=P(N)=A(N,N)
(01414) * DATA SEQUENCE
(01415) * DTPT P(N), E(N)
(01416) *
(01417) *
(01418) *
(01419) *
(01420) *
(01421) *
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(01458) *
(01459) *
(01460) *
(01461) *
(01462) *
(01463) *

A3C 06250 00800000
A3D 06252 40DC0000
A3E 06254 20A0208A
A3F 06256 05208520
A40 06258 74FC0000
A41 0625A 00800000
A42 0625C 008C0000
A43 0625E 20920099
A44 06260 00850005
A45 06262 00AD00AD
A46 06264 20482049
A47 06266 91100040
A48 06268 901E0004

A49 0626A 001C0000
A4A 0626C 001C0000
A4B 0626E 008C0000
A4C 06270 901C0049
A4D 06272 20322E32
A4E 06274 00000000
A4F 06276 10000055
A50 06278 00000000
A51 0627A 90160051
A52 0627C 10000017
A53 0627E 00E00000
A54 06280 10000039
A55 06282 160000F1
A56 06284 16061606
A57 06286 22852285
A58 06288 00970099
A59 0628A 30000070
A5A 0628C 00570000
A5B 0628E 30000070
A5C 06290 00570000
A5D 06292 30000070
A5E 06294 16001600
A5F 06296 22F722F7
A60 06298 00970099
A61 0629A 30000070
A62 0629C 00570000

      AR=P(N)P(N)
      OQ=A(N,M)
      M2=1-P(N)P(N)
      E(N-1)= [1-P(N)P(N)]J
      OQ=P(N)
      T1 SET IF /P(N)/>TEST
      OQ=E(N)
      AFI SET IF /P(N)/>TEST

      LET APS/DTPT GO
      GO BACK UNLESS DONE

      A(N+J)=E
      P(N+J)=B
      E(N+J)=E(N)

      K(+1)\MOV(IQA,A1)
      MOV(A6),K(+2)
      MOV(A5),INCRS(A5)
      MOV(R,A7)\MOV(R,EXO)
      CALL DOQ
      MOV(EXT,A7)\MOV
      CALL DOQ
      MOV(EXT,A7)\MOV
      CALL DOQ
      K(+1)
      MOV(A7),INCRS(A7)
      MOV(R,A7)\MOV(R,EXO)
      CALL DOQ
      MOV(EXT,A7)\MOV

      ;1 ;2**--15
      ;32
      ;32
      ;32
      ;32
      ;GET 16
      ;16

```



## MWLF-APS PROGRAM

PAGE 37: [BBN-TENEXD]<MAP>BBN300.MSO.61, 30-Dec-79 16:14:24, Ed: KFIELD  
MWLF-APS PROGRAM

```

A16 06310 2C000020 (01557)
A17 0631A 2E01003A (01558)
      (01559) *
      (01560) *
      (01561) *
      (01562) * MWLF = APS A(N,J) CALCULATION INPT
      (01563) * REGISTER CONTENTS AT START
      (01564) * BR0=R(1), BW0=2N
      (01565) * BR2=A(N-1), BR3=A(0)=AB-2
      (01566) *
      (01567) *
A18 0631C 302A0002 (01568)
A19 0631E 32210015 (01569)
A1A 06320 34202A40 (01570)
      (01571) *
A1B 06322 36191F50 (01572)
      (01573) *
A1C 06324 39AA0000 (01574) #3
A1D 06326 3AA20002 (01575)
A1E 06328 3C8A0002 (01576)
      (01577) *
A1F 0632A 3E191C04 (01578) MWLAE
      (01579) *
A20 0632C 4070102E (01580) MWLT
A21 0632E 42500000 (01581)
A22 06330 44320000 (01582)
A23 06332 46190020 (01583)
A24 06334 48A92578 (01584)
A25 06336 4A300037 (01585)
A26 06338 4C000020 (01586)
A27 0633A 4E190C00 (01587)
A28 0633C 502026F1 (01588)
A29 0633E 52000020 (01589)
      (01590) *
      (01591) * MWLF - APS OUTPUT
      (01592) *
      (01593) *
      (01594) * REGISTER CONTENTS AT START
      (01595) * BW0=2N, BW2=A(N)
      (01596) *
      (01597) *
A2A 06340 54300030 (01598) MWLOT
A2B 06342 56810C14 (01599)
A2C 06344 58210020 (01600)
A2D 06346 5A020794 (01601)
A2E 06348 5C910030 (01602)
A2F 0634A 5E113250 (01603)
      (01604) *
A30 0634C 60AA0002 (01605) #4
A31 0634E 62820002 (01606)
      (01607) *
A32 06350 64113004 (01608) MWLOE
A33 06352 66600026 (01609)

      NOP(0)
      ADDL(BW0,2)
      ;IIIIADDED BY KFIELD 11/79IIII
      BW0=2N

      MWLF = APS A(N,J) CALCULATION INPT
      REGISTER CONTENTS AT START
      BR0=R(1), BW0=2N
      BR2=A(N-1), BR3=A(0)=AB-2

      ADD(BR2,2)
      MOV8(BW2,BR2)
      JSN(MWLOT,P2)
      BR2=A(N)

      SET UP OUTPUT
      BR1=2N
      A(N-1,N-J+1)INPT
      A(N-1,N-J)INPT
      A(N-1,J)INPT

      BR3=A(1)=AB
      ASIZE=N, DONE
      BR3=A(0)=AB-2
      BR1=2N-2, MWDONE
      CLEANUP OF A(N) CALC*

      ;IIIIADDED BY KFIELD 11/79IIII

      SET(RO)
      MOV8(BW3,BW2,TF)
      SUB8(BW2,BW0)
      LOAD(BW1,DVYS(1),TF)
      ADDL(BW1,0,TF)
      MOV8(BW1,BW0), JUMP(MWLOE)
      BW3=A(N), OUTPUT
      BW2=A(0)
      OTPT DUMP
      OTPT DUMP
      BW1=2N

      ADD(BW2,2,TF)
      SUB(BW3,2,TF)
      SUBL(BW1,4), JUMPP(#4)
      LOAD(BW2,L0J,L)
      OTPT A(N,J)
      OTPT A(N,N-J)
      BW2=P(1)=PB

```

PAGE 38: [BBN-TENEID]<MAP>8BN388.MSD.61, 30-Dec-79 16:14:24, Ed: KFIELD  
MWLF-RPS PROGRAM

```

A34 06354 68500000 (01610)      LOAD(BW1,MSS)
A35 06356 6A220000 (01611)      SUB(BW2,MSS)
A36 06358 6C1A0001 (01612)      ADD(BW1,1)
A37 0635A 6E1A0020 (01613)      ADD8(BW2,BW0,TF)
A38 0635C 70A1002A (01614)      ADD8(BW2,BW1,TF)
                                     *
A39 0635E 72000020 (01615)      NOP(0)
A3A 06360 740003A0 (01616)      JMWPC(R5,AF0)
                                     *
A3B 06362 762003C0 (01617)      JUMP(RA+1,AF0),CLEAR
A3C 06364 780004C9 (01618)      JMWPC(MWLOC,AF1)
                                     *
A3D 06366 7A200031 (01619)      CLEAR(RI)
A3E 06368 7C000020 (01620)      NOP(0)
A3F 0636A 7E390010 (01621)      MOV8(BR3,BW0)
A40 0636C 80290010 (01622)      MOV8(BR2,BW0)
A41 0636E 82390022 (01623)      SUB8(BR3,BW1)
A42 06370 84390031 (01624)      SUB8(BR3,1)
                                     *
A43 06372 86401020 (01625)      LOAD(BW0,11)
A44 06374 88700000 (01626)      LOAD(BW3,MSS)
A45 06376 8A020000 (01627)      SUB8(BW0,MSS)
A46 06378 8C014060 (01628)      ADD8(BW0,BR2),JUMP(MWLOC)
                                     *
A47 0637A 8E0A0002 (01629)      ADD(BW0,2,TF)
A48 0637C 90210022 (01630)      SUB8(BW2,BW1)
A49 0637E 92AA0002 (01631)      ADD(BW2,2,TF)
A4A 06380 94A1002A (01632)      ADD8(BW2,BW1,TF)
A4B 06382 963947FA (01633)      ADD8(BR3,2), JUMPM(#6)
                                     *
A4C 06384 98200030 (01634)      CLEAR(R0)
A4D 06386 9A000020 (01635)      NOP(0)
A4E 06388 9C200040 (01636)      JSN(KQ50,P2)
A4F 0638A 9E300030 (01637)      SET(R0)
                                     *
A50 0638C A0C2003E (01638)      LOAD(BR0,SVTS+2*30(1),TF)
A51 0638E A240001C (01639)      LOAD(BR0,C01), JUMQUANT K'S
A52 06390 A4700000 (01640)      LOAD(BR3,MSS)
A53 06392 A6700000 (01641)      LOAD(BR3,MSS)
A54 06394 A8090032 (01642)      SUB8(BW0,2)
A55 06396 AA5263EC (01643)      LOAD(BR1,KOTAB(1))
A56 06398 AC190032 (01644)      SUB8(BR1,2)
A57 0639A AE600020 (01645)      LOAD(BR2,2)
A58 0639C A070001F (01646)      LOAD(BR3,31)
A59 0639E B289003A (01647)      ADD8(BW0,2,TF)
A5A 063A0 B499003A (01648)      ADD8(BR1,2,TF)
A5B 063A2 B699003A (01649)      ADD8(BR1,2,TF)
A5C 063A4 B8395AB1 (01650)      SUB8(BR3,1),JUMPP(K1LP)
A5D 063A6 BA295081 (01651)      SUB8(BR2,1),JUMPP(K123)
                                     *
A5E 063A8 BC600002 (01652)      LOAD(BR2,2)
A5F 063AA BE70000F (01653)      LOAD(BR3,15)
A60 063AC C009003A (01654)      ADD8(BW0,2,TF)
                                     *
A61 063AE C2000000 (01655)      LOAD(BR2,2)
A62 063B0 C4000000 (01656)      LOAD(BR3,15)
A63 063B2 C6000000 (01657)      ADD8(BW0,2,TF)
A64 063B4 C8000000 (01658)      LOAD(BR2,2)
A65 063B6 CA000000 (01659)      LOAD(BR3,15)
A66 063B8 CC000000 (01660)      ADD8(BW0,2,TF)
A67 063BA CE000000 (01661)      LOAD(BR2,2)
A68 063BC CF000000 (01662)      LOAD(BR3,15)
A69 063BE D0000000 (01663)      ADD8(BW0,2,TF)
A70 063C0 D2000000 (01664)      LOAD(BR2,2)
A71 063C2 D4000000 (01665)      LOAD(BR3,15)
A72 063C4 D6000000 (01666)      ADD8(BW0,2,TF)
A73 063C6 D8000000 (01667)      LOAD(BR2,2)
A74 063C8 DA000000 (01668)      LOAD(BR3,15)
A75 063CA DC000000 (01669)      ADD8(BW0,2,TF)
A76 063CE DE000000 (01670)      LOAD(BR2,2)
A77 063D0 E0000000 (01671)      LOAD(BR3,15)
A78 063D2 E2000000 (01672)      ADD8(BW0,2,TF)
A79 063D4 E4000000 (01673)      LOAD(BR2,2)
A80 063D6 E6000000 (01674)      LOAD(BR3,15)
A81 063D8 E8000000 (01675)      ADD8(BW0,2,TF)
A82 063DA EA000000 (01676)      LOAD(BR2,2)
A83 063DC EC000000 (01677)      LOAD(BR3,15)
A84 063DE EE000000 (01678)      ADD8(BW0,2,TF)
A85 063E0 F0000000 (01679)      LOAD(BR2,2)
A86 063E2 F2000000 (01680)      LOAD(BR3,15)
A87 063E4 F4000000 (01681)      ADD8(BW0,2,TF)
A88 063E6 F6000000 (01682)      LOAD(BR2,2)
A89 063E8 F8000000 (01683)      LOAD(BR3,15)
A90 063EA FA000000 (01684)      ADD8(BW0,2,TF)
A91 063EC FC000000 (01685)      LOAD(BR2,2)
A92 063EE FE000000 (01686)      LOAD(BR3,15)
A93 063F0 FF000000 (01687)      ADD8(BW0,2,TF)
A94 063F2 00000000 (01688)      LOAD(BR2,2)
A95 063F4 02000000 (01689)      LOAD(BR3,15)
A96 063F6 04000000 (01690)      ADD8(BW0,2,TF)
A97 063F8 06000000 (01691)      LOAD(BR2,2)
A98 063FA 08000000 (01692)      LOAD(BR3,15)
A99 063FC 0A000000 (01693)      ADD8(BW0,2,TF)
A100 063FE 0C000000 (01694)      LOAD(BR2,2)
A101 06400 0E000000 (01695)      LOAD(BR3,15)
A102 06402 10000000 (01696)      ADD8(BW0,2,TF)
A103 06404 12000000 (01697)      LOAD(BR2,2)
A104 06406 14000000 (01698)      LOAD(BR3,15)
A105 06408 16000000 (01699)      ADD8(BW0,2,TF)
A106 0640A 18000000 (01700)      LOAD(BR2,2)
A107 0640C 1A000000 (01701)      LOAD(BR3,15)
A108 0640E 1C000000 (01702)      ADD8(BW0,2,TF)
A109 06410 1E000000 (01703)      LOAD(BR2,2)
A110 06412 20000000 (01704)      LOAD(BR3,15)
A111 06414 22000000 (01705)      ADD8(BW0,2,TF)
A112 06416 24000000 (01706)      LOAD(BR2,2)
A113 06418 26000000 (01707)      LOAD(BR3,15)
A114 0641A 28000000 (01708)      ADD8(BW0,2,TF)
A115 0641C 2A000000 (01709)      LOAD(BR2,2)
A116 0641E 2C000000 (01710)      LOAD(BR3,15)
A117 06420 2E000000 (01711)      ADD8(BW0,2,TF)
A118 06422 30000000 (01712)      LOAD(BR2,2)
A119 06424 32000000 (01713)      LOAD(BR3,15)
A120 06426 34000000 (01714)      ADD8(BW0,2,TF)
A121 06428 36000000 (01715)      LOAD(BR2,2)
A122 0642A 38000000 (01716)      LOAD(BR3,15)
A123 0642C 3A000000 (01717)      ADD8(BW0,2,TF)
A124 0642E 3C000000 (01718)      LOAD(BR2,2)
A125 06430 3E000000 (01719)      LOAD(BR3,15)
A126 06432 40000000 (01720)      ADD8(BW0,2,TF)
A127 06434 42000000 (01721)      LOAD(BR2,2)
A128 06436 44000000 (01722)      LOAD(BR3,15)
A129 06438 46000000 (01723)      ADD8(BW0,2,TF)
A130 0643A 48000000 (01724)      LOAD(BR2,2)
A131 0643C 4A000000 (01725)      LOAD(BR3,15)
A132 0643E 4C000000 (01726)      ADD8(BW0,2,TF)
A133 06440 4E000000 (01727)      LOAD(BR2,2)
A134 06442 50000000 (01728)      LOAD(BR3,15)
A135 06444 52000000 (01729)      ADD8(BW0,2,TF)
A136 06446 54000000 (01730)      LOAD(BR2,2)
A137 06448 56000000 (01731)      LOAD(BR3,15)
A138 0644A 58000000 (01732)      ADD8(BW0,2,TF)
A139 0644C 5A000000 (01733)      LOAD(BR2,2)
A140 0644E 5C000000 (01734)      LOAD(BR3,15)
A141 06450 5E000000 (01735)      ADD8(BW0,2,TF)
A142 06452 60000000 (01736)      LOAD(BR2,2)
A143 06454 62000000 (01737)      LOAD(BR3,15)
A144 06456 64000000 (01738)      ADD8(BW0,2,TF)
A145 06458 66000000 (01739)      LOAD(BR2,2)
A146 0645A 68000000 (01740)      LOAD(BR3,15)
A147 0645C 6A000000 (01741)      ADD8(BW0,2,TF)
A148 0645E 6C000000 (01742)      LOAD(BR2,2)
A149 06460 6E000000 (01743)      LOAD(BR3,15)
A150 06462 70000000 (01744)      ADD8(BW0,2,TF)
A151 06464 72000000 (01745)      LOAD(BR2,2)
A152 06466 74000000 (01746)      LOAD(BR3,15)
A153 06468 76000000 (01747)      ADD8(BW0,2,TF)
A154 0646A 78000000 (01748)      LOAD(BR2,2)
A155 0646C 7A000000 (01749)      LOAD(BR3,15)
A156 0646E 7C000000 (01750)      ADD8(BW0,2,TF)
A157 06470 7E000000 (01751)      LOAD(BR2,2)
A158 06472 80000000 (01752)      LOAD(BR3,15)
A159 06474 82000000 (01753)      ADD8(BW0,2,TF)
A160 06476 84000000 (01754)      LOAD(BR2,2)
A161 06478 86000000 (01755)      LOAD(BR3,15)
A162 0647A 88000000 (01756)      ADD8(BW0,2,TF)
A163 0647C 8A000000 (01757)      LOAD(BR2,2)
A164 0647E 8C000000 (01758)      LOAD(BR3,15)
A165 06480 8E000000 (01759)      ADD8(BW0,2,TF)
A166 06482 90000000 (01760)      LOAD(BR2,2)
A167 06484 92000000 (01761)      LOAD(BR3,15)
A168 06486 94000000 (01762)      ADD8(BW0,2,TF)
A169 06488 96000000 (01763)      LOAD(BR2,2)
A170 0648A 98000000 (01764)      LOAD(BR3,15)
A171 0648C 9A000000 (01765)      ADD8(BW0,2,TF)
A172 0648E 9C000000 (01766)      LOAD(BR2,2)
A173 06490 9E000000 (01767)      LOAD(BR3,15)
A174 06492 A0000000 (01768)      ADD8(BW0,2,TF)
A175 06494 A2000000 (01769)      LOAD(BR2,2)
A176 06496 A4000000 (01770)      LOAD(BR3,15)
A177 06498 A6000000 (01771)      ADD8(BW0,2,TF)
A178 0649A A8000000 (01772)      LOAD(BR2,2)
A179 0649C AA000000 (01773)      LOAD(BR3,15)
A180 0649E AC000000 (01774)      ADD8(BW0,2,TF)
A181 064A0 AE000000 (01775)      LOAD(BR2,2)
A182 064A2 B0000000 (01776)      LOAD(BR3,15)
A183 064A4 B2000000 (01777)      ADD8(BW0,2,TF)
A184 064A6 B4000000 (01778)      LOAD(BR2,2)
A185 064A8 B6000000 (01779)      LOAD(BR3,15)
A186 064AA B8000000 (01780)      ADD8(BW0,2,TF)
A187 064AC BA000000 (01781)      LOAD(BR2,2)
A188 064AE BC000000 (01782)      LOAD(BR3,15)
A189 064B0 BE000000 (01783)      ADD8(BW0,2,TF)
A190 064B2 C0000000 (01784)      LOAD(BR2,2)
A191 064B4 C2000000 (01785)      LOAD(BR3,15)
A192 064B6 C4000000 (01786)      ADD8(BW0,2,TF)
A193 064B8 C6000000 (01787)      LOAD(BR2,2)
A194 064BA C8000000 (01788)      LOAD(BR3,15)
A195 064BC CA000000 (01789)      ADD8(BW0,2,TF)
A196 064BE CC000000 (01790)      LOAD(BR2,2)
A197 064C0 CE000000 (01791)      LOAD(BR3,15)
A198 064C2 CF000000 (01792)      ADD8(BW0,2,TF)
A199 064C4 D0000000 (01793)      LOAD(BR2,2)
A200 064C6 D2000000 (01794)      LOAD(BR3,15)
A201 064C8 D4000000 (01795)      ADD8(BW0,2,TF)
A202 064CA D6000000 (01796)      LOAD(BR2,2)
A203 064CC D8000000 (01797)      LOAD(BR3,15)
A204 064CE DA000000 (01798)      ADD8(BW0,2,TF)
A205 064D0 DC000000 (01799)      LOAD(BR2,2)
A206 064D2 DE000000 (01800)      LOAD(BR3,15)
A207 064D4 E0000000 (01801)      ADD8(BW0,2,TF)
A208 064D6 E2000000 (01802)      LOAD(BR2,2)
A209 064D8 E4000000 (01803)      LOAD(BR3,15)
A210 064DA E6000000 (01804)      ADD8(BW0,2,TF)
A211 064DC E8000000 (01805)      LOAD(BR2,2)
A212 064DE EA000000 (01806)      LOAD(BR3,15)
A213 064E0 EC000000 (01807)      ADD8(BW0,2,TF)
A214 064E2 EE000000 (01808)      LOAD(BR2,2)
A215 064E4 F0000000 (01809)      LOAD(BR3,15)
A216 064E6 F2000000 (01810)      ADD8(BW0,2,TF)
A217 064E8 F4000000 (01811)      LOAD(BR2,2)
A218 064EA F6000000 (01812)      LOAD(BR3,15)
A219 064EC F8000000 (01813)      ADD8(BW0,2,TF)
A220 064EE FA000000 (01814)      LOAD(BR2,2)
A221 064F0 FC000000 (01815)      LOAD(BR3,15)
A222 064F2 FE000000 (01816)      ADD8(BW0,2,TF)
A223 064F4 00000000 (01817)      LOAD(BR2,2)
A224 064F6 02000000 (01818)      LOAD(BR3,15)
A225 064F8 04000000 (01819)      ADD8(BW0,2,TF)
A226 064FA 06000000 (01820)      LOAD(BR2,2)
A227 064FC 08000000 (01821)      LOAD(BR3,15)
A228 064FE 0A000000 (01822)      ADD8(BW0,2,TF)
A229 06500 0C000000 (01823)      LOAD(BR2,2)
A230 06502 0E000000 (01824)      LOAD(BR3,15)
A231 06504 10000000 (01825)      ADD8(BW0,2,TF)
A232 06506 12000000 (01826)      LOAD(BR2,2)
A233 06508 14000000 (01827)      LOAD(BR3,15)
A234 0650A 16000000 (01828)      ADD8(BW0,2,TF)
A235 0650C 18000000 (01829)      LOAD(BR2,2)
A236 0650E 1A000000 (01830)      LOAD(BR3,15)
A237 06510 1C000000 (01831)      ADD8(BW0,2,TF)
A238 06512 1E000000 (01832)      LOAD(BR2,2)
A239 06514 20000000 (01833)      LOAD(BR3,15)
A240 06516 22000000 (01834)      ADD8(BW0,2,TF)
A241 06518 24000000 (01835)      LOAD(BR2,2)
A242 0651A 26000000 (01836)      LOAD(BR3,15)
A243 0651C 28000000 (01837)      ADD8(BW0,2,TF)
A244 0651E 2A000000 (01838)      LOAD(BR2,2)
A245 06520 2C000000 (01839)      LOAD(BR3,15)
A246 06522 2E000000 (01840)      ADD8(BW0,2,TF)
A247 06524 30000000 (01841)      LOAD(BR2,2)
A248 06526 32000000 (01842)      LOAD(BR3,15)
A249 06528 34000000 (01843)      ADD8(BW0,2,TF)
A250 0652A 36000000 (01844)      LOAD(BR2,2)
A251 0652C 38000000 (01845)      LOAD(BR3,15)
A252 0652E 3A000000 (01846)      ADD8(BW0,2,TF)
A253 06530 3C000000 (01847)      LOAD(BR2,2)
A254 06532 3E000000 (01848)      LOAD(BR3,15)
A255 06534 40000000 (01849)      ADD8(BW0,2,TF)
A256 06536 42000000 (01850)      LOAD(BR2,2)
A257 06538 44000000 (01851)      LOAD(BR3,15)
A258 0653A 46000000 (01852)      ADD8(BW0,2,TF)
A259 0653C 48000000 (01853)      LOAD(BR2,2)
A260 0653E 4A000000 (01854)      LOAD(BR3,15)
A261 06540 4C000000 (01855)      ADD8(BW0,2,TF)
A262 06542 4E000000 (01856)      LOAD(BR2,2)
A263 06544 50000000 (01857)      LOAD(BR3,15)
A264 06546 52000000 (01858)      ADD8(BW0,2,TF)
A265 06548 54000000 (01859)      LOAD(BR2,2)
A266 0654A 56000000 (01860)      LOAD(BR3,15)
A267 0654C 58000000 (01861)      ADD8(BW0,2,TF)
A268 0654E 5A000000 (01862)      LOAD(BR2,2)
A269 06550 5C000000 (01863)      LOAD(BR3,15)
A270 06552 5E000000 (01864)      ADD8(BW0,2,TF)
A271 06554 60000000 (01865)      LOAD(BR2,2)
A272 06556 62000000 (01866)      LOAD(BR3,15)
A273 06558 64000000 (01867)      ADD8(BW0,2,TF)
A274 0655A 66000000 (01868)      LOAD(BR2,2)
A275 0655C 68000000 (01869)      LOAD(BR3,15)
A276 0655E 6A000000 (01870)      ADD8(BW0,2,TF)
A277 06560 6C000000 (01871)      LOAD(BR2,2)
A278 06562 6E000000 (01872)      LOAD(BR3,15)
A279 06564 70000000 (01873)      ADD8(BW0,2,TF)
A280 06566 72000000 (01874)      LOAD(BR2,2)
A281 06568 74000000 (01875)      LOAD(BR3,15)
A282 0656A 76000000 (01876)      ADD8(BW0,2,TF)
A283 0656C 78000000 (01877)      LOAD(BR2,2)
A284 0656E 7A000000 (01878)      LOAD(BR3,15)
A285 06570 7C000000 (01879)      ADD8(BW0,2,TF)
A286 06572 7E000000 (01880)      LOAD(BR2,2)
A287 06574 80000000 (01881)      LOAD(BR3,15)
A288 06576 82000000 (01882)      ADD8(BW0,2,TF)
A289 06578 84000000 (01883)      LOAD(BR2,2)
A290 0657A 86000000 (01884)      LOAD(BR3,15)
A291 0657C 88000000 (01885)      ADD8(BW0,2,TF)
A292 0657E 8A000000 (01886)      LOAD(BR2,2)
A293 06580 8C000000 (01887)      LOAD(BR3,15)
A294 06582 8E000000 (01888)      ADD8(BW0,2,TF)
A295 06584 90000000 (01889)      LOAD(BR2,2)
A296 06586 92000000 (01890)      LOAD(BR3,15)
A297 06588 94000000 (01891)      ADD8(BW0,2,TF)
A298 0658A 96000000 (01892)      LOAD(BR2,2)
A299 0658C 98000000 (01893)      LOAD(BR3,15)
A300 0658E 9A000000 (01894)      ADD8(BW0,2,TF)
A301 06590 9C000000 (01895)      LOAD(BR2,2)
A302 06592 9E000000 (01896)      LOAD(BR3,15)
A303 06594 A0000000 (01897)      ADD8(BW0,2,TF)
A304 06596 A2000000 (01898)      LOAD(BR2,2)
A305 06598 A4000000 (01899)      LOAD(BR3,15)
A306 0659A A6000000 (01900)      ADD8(BW0,2,TF)
A307 0659C A8000000 (01901)      LOAD(BR2,2)
A308 0659E AA000000 (01902)      LOAD(BR3,15)
A309 065A0 AC000000 (01903)      ADD8(BW0,2,TF)
A310 065A2 AE000000 (01904)      LOAD(BR2,2)
A311 065A4 B0000000 (01905)      LOAD(BR3,15)
A312 065A6 B2000000 (01906)      ADD8(BW0,2,TF)
A313 065A8 B4000000 (01907)      LOAD(BR2,2)
A314 065AA B6000000 (01908)      LOAD(BR3,15)
A315 065AC B8000000 (01909)      ADD8(BW0,2,TF)
A316 065AE BA000000 (01910)      LOAD(BR2,2)
A317 065B0 BC000000 (01911)      LOAD(BR3,15)
A318 065B2 BE000000 (01912)      ADD8(BW0,2,TF)
A319 065B4 C0000000 (01913)      LOAD(BR2,2)
A320 065B6 C2000000 (01914)      LOAD(BR3,15)
A321 065B8 C4000000 (01915)      ADD8(BW0,2,TF)
A322 065BA C6000000 (01916)      LOAD(BR2,2)
A323 065BC C8000000 (01917)      LOAD(BR3,15)
A324 065BE CA000000 (01918)      ADD8(BW0,2,TF)
A325 065C0 CC000000 (01919)      LOAD(BR2,2)
A326 065C2 CE000000 (01920)      LOAD(BR3,15)
A327 065C4 CF000000 (01921)      ADD8(BW0,2,TF)
A328 065C6 D0000000 (01922)      LOAD(BR2,2)
A329 065C8 D2000000 (01923)      LOAD(BR3,15)
A330 065CA D4000000 (01924)      ADD8(BW0,2,TF)
A331 065CC D6000000 (01925)      LOAD(BR2,2)
A332 065CE D8000000 (01926)      LOAD(BR3,15)
A333 065D0 DA000000 (01927)      ADD8(BW0,2,TF)
A334 065D2 DC000000 (01928)      LOAD(BR2,2)
A335 065D4 DE000000 (01929)      LOAD(BR3,15)
A336 065D6 E0000000 (01930)      ADD8(BW0,2,TF)
A337 065D8 E2000000 (01931)      LOAD(BR2,2)
A338 065DA E4000000 (01932)      LOAD(BR3,15)
A339 065DC E6000000 (01933)      ADD8(BW0,2,TF)
A340 065DE E8000000 (01934)      LOAD(BR2,2)
A341 065E0 EA000000 (01935)      LOAD(BR3,15)
A342 065E2 EC000000 (01936)      ADD8(BW0,2,TF)
A343 065E4 EE000000 (01937)      LOAD(BR2,2)
A344 065E6 F0000000 (01938)      LOAD(BR3,15)
A345 065E8 F2000000 (01939)      ADD8(BW0,2,TF)
A346 065EA F4000000 (01940)      LOAD(BR2,2)
A347 065EC F6000000 (01941)      LOAD(BR3,15)
A348 065EE F8000000 (01942)      ADD8(BW0,2,TF)
A349 065F0 FA000000 (01943)      LOAD(BR2,2)
A350 065F2 FC000000 (01944)      LOAD(BR3,15)
A351 065F4 FE000000 (01945)      ADD8(BW0,2,TF)
A352 065F6 00000000 (01946)      LOAD(BR2,2)
A353 065F8 02000000 (01947)      LOAD(BR3,15)
A354 065FA 04000000 (01948)      ADD8(BW0,2,TF)
A355 065FC 06000000 (01949)      LOAD(BR2,2)
A356 065FE 08000000 (01950)      LOAD(BR3,15)
A357 06600 0A000000 (01951)      ADD8(BW0,2,TF)
A358 06602 0C000000 (01952)      LOAD(BR2,2)
A359 06604 0E000000 (01953)      LOAD(BR3,15)
A360 06606 10000000 (01954)      ADD8(BW0,2,TF)
A361 06608 1200000
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 MLF-APS PROGRAM

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A61 063AE C299003A (01663) K4LP: ADDL(BR1,2,TF) ;THRESH
A62 063B0 C499003A (01664) ADDL(BR1,2,TF) ;VALUE
A63 063B2 C6390181 (01665) SUBL(BR3,1),JUMPP(K4LP) ;DONE WITH THIS TABLE?
A64 063B4 C8295F81 (01666) SUBL(BR2,1),JUMPP(K456) ;YES, DONE WITH K6?
A65 063B6 CA600001 (01667) * LOAD(BR2,1) ;YES, NUMBER OF LENGTH 8 TABLES -1
A66 063B8 CC700007 (01668) * LOAD(BR3,7) ;TABLE LENGTH
A67 063BA CE89003A (01671) K78: ADDL(BR0,2,TF) ;K7 OR K8
A68 063BC D099003A (01672) K7LP: ADDL(BR1,2,TF) ;THRESH
A69 063BE D299003A (01673) ADDL(BR1,2,TF) ;VALUE
A6A 063C0 D4396081 (01674) SUBL(BR3,1),JUMPP(K7LP)
A6B 063C2 D6296681 (01675) SUBL(BR2,1),JUMPP(K78)
A6C 063C4 D8200031 (01676) CLEAR(RI)
A6D 063C6 DA000020 (01677) NOP(0)
A6E 063C8 DC300032 (01679) * OUTPUT PROGRAM
A6F 063CA DE40003C (01681) SET(RA)
A70 063CC E0500000 (01682) LOAD(BW0,13) ;OUTPUT ARRAY (U)
A71 063CE E2500000 (01683) LOAD(BW1,M$$) UNUSED
A72 063D0 E4500007 (01684) LOAD(BW1,7) ;UNUSED
A73 063D2 E6010031 (01685) SUBL(BW0,1) ;NUMBER OF DOUBLE OUTPUTS -1
A74 063D4 E9010039 (01686) KQOLP: ADDL(BW0,1,TE) ;KCODE(K)
A75 063D6 E0010039 (01687) ADDL(BW0,1,TE) ;KQVANT(K)
A76 063D8 EC117481 (01688) SUBL(BW1,1),JUMPP(KQOLP) ;DONE?
A77 063DA EE200030 (01689) CLEAR(RD)
A78 063DC F0000020 (01691) * NOP(0)
A79 063DE F0000020 (01692) *
A80 063E0 F0000020 (01693) * MLFSA=#C
A81 063E2 F0000020 (01694) *
A82 063E4 F0000020 (01695) * END
A83 063E6 F0000020 (01696) *
A84 063E8 F0000020 (01697) * STORAGE FOR CONSTRUCTED INSTRUCTIONS
A85 063EA F0000020 (01698) *
A86 063EC F0000020 (01699) MLFSI DATA 7F'0.0"
A87 063EE F0000020 (01700) *
A88 063F0 F0000020 (01701) * MLFSZ=#L-MLFSAPS
A89 063F2 F0000020 (01702) *
A90 063F4 F0000020 (01703) KQAB: DATA -0.95333365E+00
A91 063F6 F0000020 (01704) DATA -0.9565597E+00
A92 063F8 F0000020 (01705) DATA -0.9461753E+00
A93 063FA F0000020 (01706) DATA -0.9498004E+00
A94 063FC F0000020 (01707) DATA -0.9379499E+00
A95 063FE F0000020 (01708) DATA -0.9422044E+00
A96 06400 F0000020 (01709) DATA -0.9285137E+00
A97 06402 F0000020 (01710) DATA -0.9333929E+00
A98 06404 F0000020 (01711) DATA -0.9177035E+00
A99 06406 F0000020 (01712) DATA -0.9232912E+00
A00 06408 F0000020 (01713) DATA -0.9053387E+00
A01 0640A F0000020 (01714) DATA -0.9117274E+00

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06404	P2138CC0 (01715)	DATA	-0.8912216E+00
06406	F3027340 (01716)	DATA	-0.8905123E+00
06408	F0847EC0 (01717)	DATA	-0.8751372E+00
06409	F1148AC0 (01718)	DATA	-0.8034394E+00
0640C	EDAD67C0 (01719)	DATA	-0.8560544E+00
0640E	EED27240 (01720)	DATA	-0.8662856E+00
06410	EB0642C0 (01721)	DATA	-0.8361286E+00
06412	EC645BC0 (01722)	DATA	-0.8468127E+00
06414	EB068440 (01723)	DATA	-0.8127046E+00
06416	E9210C0 (01724)	DATA	-0.8247701E+00
06418	E41632C0 (01725)	DATA	-0.7863228E+00
0641A	E6631840 (01726)	DATA	-0.7998995E+00
0641C	EDC4340 (01727)	DATA	-0.7567219E+00
0641E	E2CEEE40 (01728)	DATA	-0.7719408E+00
06420	DCAB8FC0 (01729)	DATA	-0.7236557E+00
06422	DECD3040 (01730)	DATA	-0.7406369E+00
06424	D7EC36C0 (01731)	DATA	-0.6868962E+00
06426	DA56940 (01732)	DATA	-0.7857506E+00
06428	D2805540 (01733)	DATA	-0.6462504E+00
0642A	D50287C0 (01734)	DATA	-0.6570694E+00
0642C	CD0864C0 (01735)	DATA	-0.6015745E+00
0642E	CFED16C0 (01736)	DATA	-0.6244229E+00
06430	C6C1CA40 (01737)	DATA	-0.5527899E+00
06432	C9F1FC0 (01738)	DATA	-0.5776974E+00
06434	BFFC91C0 (01739)	DATA	-0.4998953E+00
06436	C36FE2C0 (01740)	DATA	-0.5268520E+00
06438	B8B3E2C0 (01741)	DATA	-0.4429897E+00
0643A	BC885440 (01742)	DATA	-0.4719339E+00
0643C	B0EE68C0 (01743)	DATA	-0.3822757E+00
0643E	B4E02F40 (01744)	DATA	-0.4130916E+00
06440	A8069040 (01745)	DATA	-0.3100714E+00
06442	ACDFB40 (01746)	DATA	-0.3505854E+00
06444	A01A90C0 (01747)	DATA	-0.2508107E+00
06446	A4740C40 (01748)	DATA	-0.2847915E+00
06448	972C3FC0 (01749)	DATA	-0.1810379E+00
0644A	98AC73C0 (01750)	DATA	-0.2162003E+00
0644C	8E0090C0 (01751)	DATA	-0.1F93938E+00
0644E	929CAD40 (01752)	DATA	-0.1454064E+00
06450	CAF2313F (01753)	DATA	-0.3559476E-01
06452	89581140 (01754)	DATA	-0.7309163E-01
06454	4AF22EBF (01755)	DATA	0.3659474E-01
06456	FFFF39 (01756)	DATA	-0.3725290E-00
06458	0E0090C0 (01757)	DATA	0.1093938E+00
0645A	095810C0 (01758)	DATA	0.7309162E-01
0645C	172C3FC0 (01759)	DATA	0.1810379E+00
0645E	129CAD40 (01760)	DATA	0.1454064E+00
06460	201A90C0 (01761)	DATA	0.2508107E+00
06462	18AC73C0 (01762)	DATA	0.2162003E+00
06464	28069040 (01763)	DATA	0.3100714E+00
06466	24740C40 (01764)	DATA	0.2847915E+00
06468	30EE68C0 (01765)	DATA	0.3822757E+00
0646A	2CDFB40 (01766)	DATA	0.3505854E+00
0646C	B19F26C0 (01767)	DATA	-0.3876694E+00

PAGE 41: [BBN-TELEXIDJ<MAP>BBN300.MSD.61, 30-Dec-79 16:14:24, Ed: KFIELD  
MULF-APS PROGRAM

0646E	85986040	(01768)	DATA	-0.4188042E+00
06470	A94EE640	(01769)	DATA	-0.3227203E+00
06472	A0851740	(01770)	DATA	-0.3556241E+00
06474	A0962C40	(01771)	DATA	-0.2545829E+00
06476	A4FE8740	(01772)	DATA	-0.2890176E+00
06478	97876C40	(01773)	DATA	-0.1838203E+00
0647A	9C184240	(01774)	DATA	-0.2194903E+00
0647C	8E3883C0	(01775)	DATA	-0.1118997E+00
0647E	92E68240	(01776)	DATA	-0.1476596E+00
06480	CC1F973F	(01777)	DATA	-0.3716963E-01
06482	89889640	(01778)	DATA	-0.7423668E-01
06484	4C1F933F	(01779)	DATA	0.3716908E-01
06486	97FFFFBA	(01780)	DATA	-0.1117507E-07
06488	8E3883C0	(01781)	DATA	0.1118997E+00
0648A	89889640	(01782)	DATA	0.7423665E-01
0648C	17876C40	(01783)	DATA	0.1838203E+00
0648E	12E68240	(01784)	DATA	0.1476596E+00
06490	20962C40	(01785)	DATA	0.2545829E+00
06492	1C184140	(01786)	DATA	0.2194902E+00
06494	294EE640	(01787)	DATA	0.3227203E+00
06496	24FE8740	(01788)	DATA	0.2890176E+00
06498	319F25C0	(01789)	DATA	0.3876593E+00
0649A	20851740	(01792)	DATA	0.3556241E+00
0649C	397852C0	(01791)	DATA	0.4408045E+00
0649E	35986040	(01792)	DATA	0.4188042E+00
064A0	40CFE2C0	(01793)	DATA	0.5063442E+00
064A2	3034CF40	(01794)	DATA	0.4781741E+00
064A4	479F43C0	(01795)	DATA	0.5595479E+00
064A6	4480C40	(01796)	DATA	0.5334735E+00
064A8	40E39440	(01797)	DATA	0.6005077E+00
064AA	40D20A40	(01798)	DATA	0.5845597E+00
064AC	539D37C0	(01799)	DATA	0.6532354E+00
064AE	58D190C0	(01800)	DATA	0.6313964E+00
064B0	58CF47C0	(01801)	DATA	0.6938257E+00
064B2	5646E8C0	(01802)	DATA	0.6740390E+00
064B4	5D7F09C0	(01803)	DATA	0.7304394E+00
064B6	5B3708C0	(01804)	DATA	0.7126174E+00
064B8	61B36040	(01805)	DATA	0.7632866E+00
064BA	5FA827C0	(01806)	DATA	0.7473192E+00
064BC	65744540	(01807)	DATA	0.7926108E+00
064BE	63A18940	(01808)	DATA	0.7783729E+00
064C0	68CA6040	(01809)	DATA	0.8186760E+00
064C2	672C1E40	(01810)	DATA	0.8050339E+00
064C4	68EE98C0	(01811)	DATA	0.8417544E+00
064C6	6A5829C0	(01812)	DATA	0.8305714E+00
064C8	6E59E2C0	(01813)	DATA	0.8621101E+00
064CA	6D16D0C0	(01814)	DATA	0.8522588E+00
064CC	70A4E340	(01815)	DATA	0.8900320E+00
064CE	6F08E4C0	(01816)	DATA	0.8713652E+00
064D0	72A7E640	(01817)	DATA	0.8957489E+00
064D2	71AE540	(01818)	DATA	0.8981499E+00
064D4	746AAF0	(01819)	DATA	0.9095458E+00
064D6	7390DAC0	(01820)	DATA	0.9028581E+00

06408 75F47240 (01821)	DATA	0.9215224E+00
0640A 75364440 (01822)	DATA	0.9157106E+00
0640C 7748C940 (01823)	DATA	0.9320003E+00
0640E 76A60840 (01824)	DATA	0.9269419E+00
06408 7876AF40 (01825)	DATA	0.9411220E+00
064E2 77E67040 (01826)	DATA	0.9367199E+00
064E4 797A8C40 (01827)	DATA	0.9490524E+00
064E6 70F02EC0 (01828)	DATA	0.9452265E+00
064E8 7A5C34C0 (01829)	DATA	0.9559389E+00
064EA 79E75F40 (01830)	DATA	0.9526176E+00
064EC 72E19640 (01831)	DATA	-0.0975094E+00
064EE 73E02EC0 (01832)	DATA	-0.9045466E+00
064F0 70E36240 (01833)	DATA	-0.0019392E+00
064F2 71EAFAC0 (01834)	DATA	-0.0641738E+00
064F4 7E9D3840 (01835)	DATA	-0.0098352E+00
064F6 7FC9CFC0 (01836)	DATA	-0.0733463E+00
064F8 7EC086C0 (01837)	DATA	-0.0439549E+00
064FA 7E5C8CC0 (01838)	DATA	-0.0543869E+00
064FC 7E170EC0 (01839)	DATA	-0.0210162E+00
064FE 7EA997C0 (01840)	DATA	-0.0320428E+00
06500 7E53040 (01841)	DATA	-0.7950802E+00
06502 777AF8C0 (01842)	DATA	-0.0084483E+00
06504 7E08040 (01843)	DATA	-0.7650692E+00
06506 7E3F492C0 (01844)	DATA	-0.7009013E+00
06508 7DD6A840 (01845)	DATA	-0.7331133E+00
0650A 7F7E61C0 (01846)	DATA	-0.7499506E+00
0650C 7D28F240 (01847)	DATA	-0.6965621E+00
0650E 7D8FD240 (01848)	DATA	-0.7153266E+00
06510 7D37C4C0 (01849)	DATA	-0.6559988E+00
06512 7D612DC0 (01850)	DATA	-0.6767938E+00
06514 7E3D9DC0 (01851)	DATA	-0.6112554E+00
06516 7D2C1440 (01852)	DATA	-0.6341577E+00
06518 7F72DC0 (01853)	DATA	-0.5622308E+00
0651A 7C2C0A40 (01854)	DATA	-0.5872815E+00
0651C 7C123E640 (01855)	DATA	-0.5089081E+00
0651E 7C4F0E40 (01856)	DATA	-0.5361048E+00
06520 79C688C0 (01857)	DATA	-0.4513713E+00
06522 7D062A40 (01858)	DATA	-0.4806569E+00
06524 7B59AC0 (01859)	DATA	-0.3090195E+00
06526 7B5F3C0 (01860)	DATA	-0.4210801E+00
06528 7A980140 (01861)	DATA	-0.3245756E+00
0652A 7ADC6F2C0 (01862)	DATA	-0.3576339E+00
0652C 7AC70C40 (01863)	DATA	-0.2560897E+00
0652E 7A550240 (01864)	DATA	-0.2907050E+00
06530 77A80E40 (01865)	DATA	-0.1049325E+00
06532 7C4355C0 (01866)	DATA	-0.2200049E+00
06534 7E4E0F40 (01867)	DATA	-0.1117020E+00
06536 730407C0 (01868)	DATA	-0.1495605E+00
06538 7C9828BF (01869)	DATA	-0.3739962E-01
0653A 7B0F9840 (01870)	DATA	-0.7469469E-01
0653C 7C981A3F (01871)	DATA	-0.3739949E-01
0653E 7B00003B (01872)	DATA	-0.5960465E-07
06540 7E4E0EC0 (01873)	DATA	-0.1117819E+00

06542 0989740 (01874)	DATA	0.7469457E+01
06544 17A0040 (01875)	DATA	0.189324E+00
06546 1304060 (01876)	DATA	0.1485603E+00
06548 20C78A0 (01877)	DATA	0.2560095E+00
0654A 1C43540 (01878)	DATA	0.2200048E+00
0654C 2980800 (01879)	DATA	0.3245755E+00
0654E 253D0C0 (01880)	DATA	0.2907048E+00
06550 31E59A0 (01891)	DATA	0.3098194E+00
06552 20C61C0 (01882)	DATA	0.3576330E+00
06554 39C6880 (01883)	DATA	0.4513712E+00
06556 35E5F30 (01884)	DATA	0.4210000E+00
06558 4123E50 (01885)	DATA	0.5089000E+00
0655A 3086290 (01886)	DATA	0.4066560E+00
0655C 47F72D0 (01887)	DATA	0.5622300E+00
0655E 449F0E0 (01888)	DATA	0.5361040E+00
06560 4E3D9D0 (01889)	DATA	0.6112553E+00
06562 482C090 (01890)	DATA	0.5872814E+00
06564 53F7C30 (01891)	DATA	0.6559987E+00
06566 512C130 (01892)	DATA	0.6341576E+00
06568 5920F20 (01893)	DATA	0.6965621E+00
0656A 56A12D0 (01894)	DATA	0.6767938E+00
0656C 903E150 (01895)	DATA	-0.2284572E+00
0656E A2D2710 (01896)	DATA	-0.2720472E+00
06570 918E230 (01897)	DATA	-0.1306380E+00
06572 978B110 (01898)	DATA	-0.1039315E+00
06574 DF3ABF (01899)	DATA	-0.4547871E-01
06576 88DF760 (01900)	DATA	-0.9275703E-01
06578 5F3039F (01901)	DATA	0.4647870E-01
0657A FFFFF39 (01902)	DATA	-0.3725290E-09
0657C 118E3C0 (01903)	DATA	0.1306380E+00
0657E 08DF760 (01904)	DATA	0.9275702E-01
06580 1D3E150 (01905)	DATA	0.2284572E+00
06582 178B110 (01906)	DATA	0.1039315E+00
06584 2843200 (01907)	DATA	0.3145486E+00
06586 22D2710 (01908)	DATA	0.2720472E+00
06588 32A8390 (01909)	DATA	0.3957588E+00
0658A 2D88AE0 (01910)	DATA	0.3558252E+00
0658C 3C50DCC0 (01911)	DATA	0.4712177E+00
0658E 37957E0 (01912)	DATA	0.4342497E+00
06590 452A650 (01913)	DATA	0.5403564E+00
06592 40D84D0 (01914)	DATA	0.5066010E+00
06594 4D2B880 (01915)	DATA	0.6028952E+00
06596 49464C0 (01916)	DATA	0.5724578E+00
06598 5453E10 (01917)	DATA	0.6588098E+00
0659A 50DA8C0 (01918)	DATA	0.6316733E+00
0659C 5AA90DC0 (01919)	DATA	0.7082841E+00
0659E 57983F0 (01920)	DATA	0.684337E+00
065A0 6036570 (01921)	DATA	0.7516584E+00
065A2 5D87D50 (01922)	DATA	0.7307070E+00
065A4 650A5C0 (01923)	DATA	0.7893787E+00
065A6 62B6810 (01924)	DATA	0.7711946E+00
065A8 6935890 (01925)	DATA	0.8219520E+00
065AA 67340AC0 (01926)	DATA	0.8062757E+00

PAGE 44: CBBM-TENEXDJ<MAP>8BM300.MSO.61, 30-Dec-79 16:14:24, Ed: KFIELD  
 HWLF-APS PROGRAM

065AC C62948C0 (01927)	DATA	-0.5401349E+00
065AS C9C0E940 (01928)	DATA	-0.5761997E+00
065B0 8E7895C0 (01929)	DATA	-0.4081465E+00
065B2 C26759C0 (01930)	DATA	-0.5107790E+00
065B4 062A27C0 (01931)	DATA	-0.4231615E+00
065B6 BA66CC00 (01932)	DATA	-0.4562622E+00
065B8 AD3FD340 (01933)	DATA	-0.3535103E+00
065BA 81C73240 (01934)	DATA	-0.3088915E+00
065BC A3CD64C0 (01935)	DATA	-0.2797056E+00
065BE A8965640 (01936)	DATA	-0.3170879E+00
065C0 99E98CC0 (01937)	DATA	-0.2024399E+00
065C2 9E0802C0 (01938)	DATA	-0.2414554E+00
065C4 0FB05640 (01939)	DATA	-0.1225689E+00
065C6 94D5AEC0 (01940)	DATA	-0.1627711E+00
065C8 D420863F (01941)	DATA	-0.4107802E-01
065CA 8A7DAE40 (01942)	DATA	-0.0196046E-01
065CC 53E4723F (01943)	DATA	0.4096307E-01
065CE 9E2E7100 (01944)	DATA	-0.5756650E-04
065D0 0FAC9F40 (01945)	DATA	0.1224555E+00
065D2 0A79EEC0 (01946)	DATA	0.0194609E-01
065D4 19E5EEC0 (01947)	DATA	0.2023295E+00
065D6 14D202C0 (01948)	DATA	0.1626590E+00
065D8 23C9E9C0 (01949)	DATA	0.2795994E+00
065DA 1EE47540 (01950)	DATA	0.2413470E+00
065DC 2D3C86C0 (01951)	DATA	0.3534896E+00
065DE 2892F1C0 (01952)	DATA	0.3169844E+00
065E0 36270F40 (01953)	DATA	0.4230670E+00
065E2 31C3FEC0 (01954)	DATA	0.3087938E+00
065E4 3E78B5C0 (01955)	DATA	0.480588E+00
065E6 3A63CFC0 (01956)	DATA	0.4561710E+00
065E8 46264C0 (01957)	DATA	0.5480543E+00
065EA 42649740 (01958)	DATA	0.5186548E+00
065EC 9C316340 (01959)	DATA	-0.2202572E+00
065EE A1972940 (01960)	DATA	-0.2624256E+00
065F0 91184540 (01961)	DATA	-0.133532E+00
065F2 96801940 (01962)	DATA	-0.1772491E+00
065F4 0BA8843F (01963)	DATA	-0.4475537E-01
065F6 8B6F3940 (01964)	DATA	-0.0933180E-01
065F8 5BA8B13F (01965)	DATA	0.4475535E-01
065FA 9000003A (01966)	DATA	-0.7450581E-00
065FC 11184540 (01967)	DATA	0.133532E+00
065FE 0B6F3940 (01968)	DATA	0.0933177E-01
06600 1C316340 (01969)	DATA	0.2202572E+00
06602 16801940 (01970)	DATA	0.1772491E+00
06604 260C0C40 (01971)	DATA	0.3036151E+00
06606 21972940 (01972)	DATA	0.2624256E+00
06608 30F012C0 (01973)	DATA	0.3825786E+00
0660A 28FE5D40 (01974)	DATA	0.3437001E+00
0660C 3A60A440 (01975)	DATA	0.4563077E+00
0660E 35C6E5C0 (01976)	DATA	0.4201324E+00
06610 431B3D40 (01977)	DATA	0.5242688E+00
06612 3EDA3CC0 (01978)	DATA	0.4910351E+00
06614 4006EE40 (01979)	DATA	0.5861490E+00

06616	472A5040 (01980)	DATA	0.5559780E+00
06618	522806C0 (01991)	DATA	0.6418713E+00
0661A	4E8180C0 (01982)	DATA	0.6147767E+00
0661C	588484C0 (01983)	DATA	0.6915499E+00
0661E	556F1640 (01984)	DATA	0.6674526E+00
06620	5E232640 (01985)	DATA	0.7354477E+00
06622	586A80C0 (01986)	DATA	0.7142804E+00
06624	63103C40 (01987)	DATA	0.7739330E+00
06626	60AF01C0 (01988)	DATA	0.7553408E+00
06628	675A3940 (01989)	DATA	0.8074409E+00
0662A	65481840 (01990)	DATA	0.7912802E+00
0662C	89663FC0 (01991)	DATA	-0.4484329E+00
0662E	C0318F40 (01992)	DATA	-0.5017928E+00
06630	AA7754C0 (01993)	DATA	-0.3317667E+00
06632	82225640 (01994)	DATA	-0.3916729E+00
06634	9A1C2FC0 (01995)	DATA	-0.2039852E+00
06636	A27080C0 (01996)	DATA	-0.2698596E+00
06638	88D02FC0 (01997)	DATA	-0.6805332E-01
0663A	918814C0 (01998)	DATA	-0.1370569E+00
0663C	80D02F40 (01999)	DATA	0.6805329E-01
0663E	9FF0F080 (02000)	DATA	-0.1490116E-07
06640	1A1C2FC0 (02001)	DATA	0.2039852E+00
06642	118B13C0 (02002)	DATA	0.1378560E+00
06644	2A7753C0 (02003)	DATA	0.3317666E+00
06646	22708AC0 (02004)	DATA	0.2698595E+00
06648	39663FC0 (02005)	DATA	0.4484329E+00
0664A	32225640 (02006)	DATA	0.3916729E+00
0664C	96635440 (02007)	DATA	-0.1749063E+00
0664E	9D9CC640 (02008)	DATA	-0.2313469E+00
06650	F895288F (02009)	DATA	-0.5987801E-01
06652	8F030FC0 (02010)	DATA	-0.1172924E+00
06654	78580F3F (02011)	DATA	0.5976326E-01
06656	9E2EF180 (02012)	DATA	-0.5757022E-04
06658	165FAC40 (02013)	DATA	0.1747947E+00
0665A	0EFF07C0 (02014)	DATA	0.1171789E+00
0665C	24A11840 (02015)	DATA	0.2801662E+00
0665E	1D993440 (02016)	DATA	0.2312379E+00
06660	31F598C0 (02017)	DATA	0.3983075E+00
06662	286D0540 (02018)	DATA	0.3392760E+00
06664	3E1F76C0 (02019)	DATA	0.4953352E+00
06666	38328CC0 (02020)	DATA	0.4390484E+00
06668	48FA0F40 (02021)	DATA	0.5701312E+00
0666A	43B7FEC0 (02022)	DATA	0.5290526E+00

PAGE 46: C88N-TEHEXIDJ<MAP28BN3N6.WSO.51, 30-Dec-79 16:14:24, Ed: KFIELD  
 WMLQSSSH - SPECIAL INTERMEDIATE SUPPORT

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(02023) * WMLQSSSH - SPECIAL INTERMEDIATE SUPPORT
(02024) *
0666C 06606671 (02025) WMLQSSSH      CALL    R6,WVSS$1
0666E 0F70      (02026)              RET
0665F 80000FB1  (02027)              JMP APSD0N5
(02028) *
(02029) *
06671 90606678 (02030) WVSS$1: MOVIR R6,INST-2
06673 0070      (02031)          CLR R7
06674 056FFFC0 (02032)          LPROCL R6,R7,APSSLA      ;LOAD NOP INTO 1ST WD APS
06676 F863PFC5 (02033)          MOVLM $31,$VSSFLGS      ;SET RI
06678 0570      (02034)          RETURN
06679 0000      (02035)          EVEN
0667A 0000      (02036)          INST: DATA $0          ;NOP(2)
0667B 0020      (02037)          DATA $20
(02038) *
(02039) *
(02040) *
0667C 0570      (02041)          RETURN
  
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PAGE 47: CBBN-TELEXOJ<MAP>B8N300.WSO.S1, 34-Dec-79 16:14:24, Ed: KFIELD  
MPIFFS MODULE TO PROCESS THE MPIFF(ISA,ISB,FLID) FCB

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(02042) * MPIFFS  MODULE TO PROCESS THE MPIFF(ISA,ISB,FLID) FCB
(02043) *
(02044) * EXECUTES FUNCTION LIST "FLID" IF (ISA .NE. 0) .AND.
(02045) * (ISB .EQ. 0)
(02046) *
(02047) * FCB FORMAT (16 BIT FORMAT SHOWN)
(02048) *
(02049) * WORD LEFT BYTE RIGHT BYTE
(02050) * -----
(02051) * 0: PTR TO NEXT FCB AND FUNCTION LIST FLAG (LSB)
(02052) * 1: 105 ISA
(02053) * 2: 0 ISB
(02054) * 3: 0 FLID
(02055) * 4: 0
(02056) * 5: 0

PROGRAM REGISTER USAGE:
R1: PTR TO WORD 1 OF FCB ENTRY (SET ON ENTRY)
R2: FUNCTION LIST ID
R4: SCALAR A ID
R5: SCALAR B ID

3667D 0000 EVEN
0667E 704200FF MOVNR R4,R1,MSKSRBVT
0668F F0520001 MOVNR R5,H3(R1)
06692 F0220002 MOVNR R2,W5(R1)
06684 E2000502 CMPNZ ISVT5(P4)
06686 8110668A JMP IFFS1,NEZ
06688 0E70 RETURN
06689 0000 EVEN
0669A E20A0502 CMPNZ ISVT5(R5)
0668C 8010192C JAP XFLS01,EQZ
0668E 0E70 RETURN
0669F 0000 EVEN

```



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(02094) * APU3-VAPC
(02095) * THIS ROUTINE PERFORMS THE APC (ADAPTIVE PREDICTIVE CODING) FUNCTION
(02096) *
(02097) * NOTE: THIS ROUTINE MODIFIES ITS APS ROUTINE CODE.
(02098) * PITCH IS INPUT, CONVERTED TO INTEGER, AND WRITTEN TO THE
(02099) * BUS 1 COPY OF THE APS INSTRUCTION TO BE MODIFIED.
(02099) * WHEN THE WRITE HAS COMPLETED, THE BUS1 COPY IS READ
(02091) * AND WRITTEN TO APS PSEUDO MEMORY.
(02092) *
(02093) * VAPC COPIES CODED PITCH, CODED TAP, CODED GAIN AND CODED REFL'N
(02094) * COEFFS TO OUTPUT BUFFER (Y ARRAY). THEN THE MAIN LOOP COMPUTES
(02095) * CODED BASEBAND RESIDUAL SAMPLES AS FOLLOWS, WHERE X(N) IS THE INPUT
(02096) * AND Y(N) IS THE NORMALIZED (I.E. DIVIDED BY GAIN) BASEBAND
(02097) * RESIDUAL SAMPLE:
(02098) *
(02099) *  $Y(N) = G * Y(N) = X(N) - (TAP * D(N - PITCH))$ 
(02100) *  $Y(N) = Y(N) / G$ 
(02101) *  $VQ(N) = QUANT(Y(N))$ 
(02102) *  $VCODE(N) = CODE(Y(N))$ 
(02103) *  $D(N) = D(N - FRAME SIZE) = G * VQ(N) - Y(N)$ 
(02104) *
(02105) * MEMORY USAGE FOR MAIN LOOP:
(02106) * A0 YCODE Q2
(02107) * A1 T1 Q1
(02108) * A2 T2 Q3
(02109) * A3 T3 Q3
(02110) * A4 Y Y
(02111) * A5 [VCODE] 2*-15
(02112) * A6 4 [VQ]
(02113) * A7 T*D(N-P)/YG [VCODE]
(02114) *
(02115) * M0 TAP UNUSED
(02116) * M1 I/G C
(02117) * M2 UNUSED UNUSED
(02118) * M3 UNUSED UNUSED
(02119) * M4 D(N-PITCH) UNUSED
(02120) * M5 X-T*0 YQ
(02121) * M6 UNUSED UNUSED
(02122) * M7 UNUSED
(02123) * EVEN
(02124) * DATA VAPCSSA
(02125) * DATA VAPCSSZ
(02126) * VAPCS BEGIN APU(VAPC)
(02127) * PA=0
(02128) *
(02129) *
(02130) * CHANGE PITCH TO INTEGER:
(02131) * MULT BY (2*-15), ALIGN.
(02132) *
(02133) * VAPCSSA:
(02134) * MOV(10A,M1) \ NOP ; (2*-15)
(02135) * MOV(10A,M5) \ NOP ; FLT PT PITCH
(02136) * MUL(M1,M5) \ NOP

```



```

A25 0660C 000000F2 (02190)      NOP\MOV(IQA,A2)      ;YQ2=1.666
A26 0660E 000000F3 (02191)      NOP\MOV(IQA,A3)      ;YQ3=3.875
      (02192) *
      (02193) *
      (02194) *
      (02195) * DO MAIN APCLP
A27 06650 00F00000 (02196)      MOV(IQA,A0)\NOP      ;X(N)
A28 06652 00E00000 (02197)      MOV(IQA,A4)\NOP      ;D(N-PITCH)
A29 06654 04000000 (02198)      MUL(M0,M4)\NOP      ;TAP*D(N-PITCH)
A2A 06656 00F71600 (02199)      MOV(IQA,A7)\K(+1)    ;TWO FAKE INS SO INPUT DOESN'T
      (02200) *                      GET TOO FAR AHEAD OF OUTPUT\GET 1.0
A2B 06658 00E00000 (02201)      MOV(IQA,NULL)\NOP
A2C 0665A 00870000 (02202)      MOV(P,A7)\NOP      ;STORE TAP*D(N-PITCH)
A2D 0665C 4F000995 (02203)      SUB(AE,A7)\MOV(R,A5) ;X(N)-T*D(N-PITCH)\STORE 1.
A2E 0665E 00800000 (02204)      MOV(R,M5)\NOP      ;Y(N)*G
A2F 066F0 04000400 (02205)      MUL(M1,M5)          ;Y(N)*G*(1/G)
A30 066F2 00972097 (02206)      MOV(R,A7)           ;SAVE Y(N)*G
A31 066F4 00840000 (02207)      MOV(P,A4)\NOP      ;SAVE Y(N)
      (02208) *                      QUANTIZE AND CODE Y(N)
A32 066F6 16C00017 (02209)      K(+4)\MOV(ZERO,A7)  ;GET CONSTANT 4, FOR LATER\CODE P
A33 066F8 74360200 (02210)      MOV(A6)\MAXABS(A1,A4)\R(AE) ;Y.GE.BIN1\GET YQ0
A34 066FA 00800018 (02211)      MOV(P,EXO)\MOV(ZERO,EXO) ;TRANSFER V(N)\CODE P
A35 066FC 00020854 (02212)      NOP\MOV(EXI,A4)      ;WILL NEED Y(N) EVENTUALLY
A36 066FE 00804786 (02213)      MOV(R,NULL)\MOV(A6),ADD(A5,A7) ;MAKE SURE MAXABS
      (02214) *                      IS DONE\GEN CODE 1
A37 06700 001E0042 (02215)      JUMPC(OUT,T1)       ;JUMP IF BIN1 GE Y
A38 06702 00000098 (02216)      NOP\MOV(R,EXO)      ;CODE1
A39 06704 74400237 (02217)      MAXABS(A2,A4)\MOV(A7),R(A1) ;TEST BIN2>Y\GET YQ1
A3A 06706 00804786 (02218)      MOV(R,NULL)\MOV(A6),ADD(A5,A7) ;MAKE SURE MAXABS DONE
      (02219) *                      ;GEN CODE 2
A3B 06708 001E0042 (02220)      JUMPC(OUT,T1)       ;JUMP IF BIN2 GE Y
A3C 0670A 00000099 (02221)      NOP\MOV(R,EXO)      ;CODE2
A3D 0670C 74600257 (02222)      MAXABS(A3,A4)\MOV(A7),R(A2) ;BIN3\GET YQ2,
      (02223) *                      GEN CODE3
A3E 0670E 00804786 (02224)      MOV(R,NULL)\MOV(A6),ADD(A5,A7) ;MAKE SURE MAXABS DONE
      (02225) *                      ;GEN CODE 3
A3F 06710 9C1E0042 (02226)      JUMPC(OUT,T1)       ;JUMP IF BIN3 GE Y
A40 06712 00000270 (02227)      NOP\MOV(EXO),R(A3)   ;EXO=CODE=3,GET YQ3
A41 06714 00000096 (02228)      NOP\MOV(R,A6)        ;A6=YQ=YQ1*
      (02229) *                      ;SIGN(Y)
A42 06716 12000000 (02230)      ABS(A4)\NOP          ;CODE 4
A43 06718 00550017 (02231)      MOV(EXI,A5)\MOV(ZERO,A7) ;MAKE SURE ABS DONE
A44 0671A 00800000 (02232)      MOV(R,NULL)\NOP      ;A+ OR - CODE\GET SIGN Y
A45 0671C 55001200 (02233)      ADDST(A6,A5)\ABS(A4) ;NEED -1 SOON
A46 0671E 00950000 (02234)      MOV(A5),K(-1)\NOP    ;STORE -1,GET SIGN Y
A47 06720 12960000 (02235)      MOV(A6),ABS(A4)\NOP   ;MAKE SURE ADD OPS ARE DONE
A48 06722 00800000 (02236)      MOV(R,NULL)\MOV(P,NULL) ;DECR IF Y NEG\SIGN(Y)=SIGN(Y)
A49 06724 55A05600 (02237)      ADDLT(A5,A6)\ADDST(A7,A6) ;R=CODE (A - 7)\R=YQ
      (02238) *                      ;FIND DIFFERENCE OF QUANTIZED Y AND ACTUAL Y
A4A 06726 2A100000 (02239)      MOV(A0),DECRE(A0)\MOV(R,M5) ;START TO RT SHFT CODE\STORE YQ
A4B 06728 2A100400 (02240)      MOV(A0),DECRE(AE)\MUL(M1,M5) ;8 BITS RT\YQ
A4C 0672A 2A100000 (02241)      MOV(A0),DECRE(A0)\NOP ;*2**--12
A4D 0672C 2A100000 (02242)      MOV(A0),DECRE(AE)\NOP ;16 BITS RT

```

```

A4E 0672E 40100000 (02243)
A4F 06730 3A1008B9 (02244)
A50 06732 289C0000 (02245)
A51 06734 08540000 (02246)
A52 06736 4F800000 (02247)
A53 06738 089C0000 (02248)
A54 0673A 089C0000 (02249)
A55 0673C 90100027 (02250)
A56 0673E 20322032 (02252)
A57 06740 10000000 (02253)
A58 06742 00000000 (02254)
      06744      (02255)
      20000000 (02256)
      20000000 (02257)

MOV(A3),ADD(A0,A0)\NOP
MOV(A0),ALIGN(A0)\MOV(P,END)
MOV(R,0Q)\NOP
MOV(CX1,A4)\NOP
SUB(A4,A7)\NOP
MOV(R,0Q)\NOP
MOV(R,0Q)\NOP
JUMPP(CAPCLP,FI)

CLEAR(RA)
JUMPP(P)
NOP

END
VAPCS$Z=#A-VAPCSSA

```







PAGE 55: [BBN-TEWEXD]CHAP>BBN3BC.HSO.61, 30-Dec-79 16:14:24, Ed: KFIELD  
APS3-AAPC (VAPC)



PAGE 56: C8BN-TEHEX0J<MAP>8BK300.W50.61, 3A-Dec-79 16:14:24, Ed: KFIELD  
 APU3-DEALU (DEAL)

```

    (02416) * APU3-DEALU (DEAL)
    (02417) *APU PCM FOR DEAL
    (02418) *BINDS TO APS-DEALS
    (02419) *DEAL OUT PARAMS FROM INPUT ARRAY TO PITCH, TAP.
    (02420) *MULTIPLY NEXT INPUTS (BASEBAND SAMPLES) BY GAIN, AND OUTPUT PRODUCT
    (02421) EVEN
    (02422) DATA DEALUSSA
    (02423) DATA DEALUSSZ
    (02424) *
    (02425) DEALUS BEGIN APU(DEALU)
    (02426) PA=0
    (02427) DEALUSSA MOV(1QA,A1)\NOP
    (02428) MOV(4,A1)
    (02429) MOV(R,QQ)\NOP
    (02430) MOV(1QA,QQ)\NOP
    (02431) MOV(1QA,M4)
    (02432) MOV(1QA,QQ)\NOP
    (02433) MOV(1QA,QQ)\NOP
    (02434) MOV(1QA,QQ)\NOP
    (02435) MOV(1QA,QQ)\NOP
    (02436) MOV(1QA,QQ)\NOP
    (02437) MOV(1QA,QQ)\NOP
    (02438) MOV(1QA,QQ)\NOP
    (02439) MOV(1QA,M0)\NOP
    (02440) MOV(1QA,M0)\NOP
    (02441) MOV(1QA,M0)\NOP
    (02442) MUL(M0,M4)
    (02443) MOV(1QA,M1)\NOP
    (02444) MOV(1QA,M1)\NOP
    (02445) MOV(1QA,M0)\NOP
    (02446) MOV(1QA,M0)\NOP
    (02447) MOV(1QA,M0)\NOP
    (02448) MOV(1QA,M0)\NOP
    (02449) MOV(1QA,M0)\NOP
    (02450) JUMPC(2,EO)
    (02451) CLEAR(RA)
    (02452) NOP
    (02453) DEALUSSZ=#A-DEALUSSA
    (02454) END
  
```

PAGE 57: LBN-TEHEXOJ<YAP>88BK3JZ.450-61, 34-Dec-79 16:14:24, 2d: KFIELD  
APS3-DEAL

```

(02454) * APS3-DEAL
(02455) * DEAL(Y,A,U,B,V)
(02456) * WHERE Y IS BASEBAND SAMPLES *GAIN,A IS RECEIVED PITCH,U IS RECVD K'S,
(02457) *B IS RECVD TAP,AND W IS RESOURCE BUFFER,HOWE OF ALL INPUT PARAMS
(02458) *INPUT STREAM - V(N), WHERE THESE ARE LONG FLOATING NUMBERS
(02459) *W CONSISTS OF PITCH,TAP,GAIN,3 K'S,AND 60 88 SAMPLES
(02460) *
(02461) *OUTPUT STREAM - PITCH,TAP,GAIN, K(1)...K(8),88-GAIN(1),...88-GAIN(60)
(02462) *
      EVEN
      ADDR DEALSS1
      ADDR DEALSS + 2*DEALSSS
      DATA 2
      DATA DEALSSZ
      EVEN
      ADDR DEALSSA
      EVEN
      DEALSS BEGIN APS(DEALS)
      JSK(DEALSS3,P2)
      SET(RO)
      LOAD(BW0,I2J)
      LOAD(BW1,MSS)
      SUB(BW0,MSS)
      *INPUT LOOP
      #1
      ADDR(BW0,I2J,TF)
      SUBL(BW1,I1),JUMPP(#1)
      CLEAR(RI)
      NOP(0)
      DEALSS3 SET(CRA)
      (02471) DEALSS
      (02472)
      (02473)
      (02474)
      (02475)
      (02476)
      (02477)
      (02478)
      (02479)
      (02480)
      (02481)
      (02482)
      (02483)
      (02484)
      (02485)
      (02486)
      (02487)
      (02488)
      (02489)
      *OUTPUT LOOP 1
      #5
      ADDR(BW0,I2J,TF)
      SUBL(BW1,I1),JUMPP(#5)
      LOAD(BW0,I2J)
      LOAD(BW1,MSS)
      SUB(BW0,MSS)
      *OUTPUT LOOP 2
      #6
      ADDR(BW0,I2J,TF)
      SUBL(BW1,I1),JUMPP(#6)
      DEALSS1=PC
      CLEAR(PO)
      NOP(0)
      END
      DEALSS1 DATA 5F'0.3"
      ...
      004J0C3C (02503) DEALSSZ=HL-DEALSS

```

PAGE 58: (88H-TENEXD)<VAP>88H300-4SD-61, 30-Dec-79 16:14:24, E3: KFIELD  
APU3-APCIU (VIAPC)

```

(02504) * APU3-APCIU (VIAPC)
(02505) * APU PGM FOR APCI
(02506) * BINDS TO APCIS
(02507) * EQN: Y(N)=Y(N-YSIZE)+X(N)+TAP*Y(N-PITCH)
(02508) * CALLED VIAPC(V,TAP,X,PITCH)
(02509) *
(02510) *
(02511) * NOTE: THIS ROUTINE MODIFIES ITS APS ROUTINE CODE.
(02512) * PITCH IS INPUT, CONVERTED TO INTEGER, AND WRITTEN TO THE
(02513) * BUS 1 COPY OF THE APS INSTRUCTION TO BE MODIFIED.
(02514) * WHEN THE WRITE HAS COMPLETED, THE BUS1 COPY IS READ
(02515) * AND WRITTEN TO APS PSEUDO MEMORY.
(02516) *
(02517) *
(02518) * EVEN
(02519) * DATA APCIUSSA
(02520) * DATA APCIUSSZ
(02521) *
(02522) * APCIU$ BEGIN APU(APCIU)
(02523) * A=P
(02524) *
(02525) *
(02526) * CHANGE PITCH TO INTEGER:
(02527) * MULT BY (2**15), ALIGN.
(02528) *
(02529) * APCIUSSA:
(02530) * MOV(IGA,V1) \ NOP ; (2**15)
(02531) * MOV(IGA,V5) \ NOP ; FLT PT PITCH
(02532) * MUL(M1,M5) \ NOP
(02533) * MOV(P,A6) \ NOP ; R <= FWD PITCH
(02534) * ALIGN(A6) \ NOP
(02535) *
(02536) * SEND FWD PITCH TO BUS1 SO AS TO
(02537) * MODIFY BUS1 COPY OF APS INSTR.
(02538) *
(02539) * MOV(P,OQ) \ NOP ; OQ <= FWD PITCH
(02540) *
(02541) * WAIT TIL BUS1 COPY IS MODIFIED,
(02542) * THEN READ IT AND WRITE TO APS PSEUDO MEM.
(02543) * (ALSO CLR W1 SO APS INPUT PGM WILL CONTINUE)
(02544) * (IT WAS WAITING FOR WRITE TO COMPLETE.)
(02545) *
(02546) * VAPCISW1 NOP
(02547) * JUMPC(VAPCISW1,OQE) ; LOOP WHILE DONE
(02548) * NOP
(02549) * NOP
(02550) * NOP
(02551) * NOP
(02552) * NOP
(02553) * CLEAR(W1)
(02554) * NOP
(02555) *
(02556) * MOV(IGA,OQ) \ NOP ; READ MODIFIED VERSION

```

3 AND WRITE TO APS PSEUDO MEM.

(02557) \*  
(02558) \*  
(2559) \* WAIT TIL APS COPY WRITTEN  
(02560) \* (CLR HI AGAIN)

LOC	WAPCIW2	WAPCIW2	NOP
A10	00000000	(02562)	JUPPC(WAPCIW2,OQE)
A11	00898	00180010	NOP
A12	0089A	00000000	JUPPC(WAPCIW2,OQE)
A13	0689C	00000000	NOP
A14	0689E	00000000	NOP
A15	068A0	00000000	NOP
A16	063A2	20000000	NOP
A17	068A4	20372037	CLEAR(WI)
A18	068A6	00000000	NOP

(02571) \*

```

A119 069A0 08E90000 (02572)
A11A 068AA 08EC000C (02574)
A11B 068AC 040E0000 #1 (02575)
A11C 068AE 080E0000 (02576)
A11D 068B0 08B10000 (02577)
A11E 068B2 08B10000 (02578)
A11F 068B4 08B10000 (02579)
A120 068B6 08F20000 (02580)
A121 068B8 089C0000 (02581)
A122 068BA 089C0000 (02582)
A123 068BC 901C0018 (02583)
A124 068BE 20320032 (02584)
A125 068C0 080E0000 (02585)
A126 068C2 080E0000 (02586)
A127 068C4 080E0000 (02587)
A128 068C6 080E0000 (02588)
A129 068C8 080E0000 (02589)
A130 068CA 080E0000 (02590)
A131 068CB 080E0000 (02591)
A132 068CD 080E0000 (02592)
A133 068CE 080E0000 (02593)
A134 068CF 080E0000 (02594)
A135 068D0 080E0000 (02595)
A136 068D2 080E0000 (02596)
A137 068D4 080E0000 (02597)
A138 068D6 080E0000 (02598)
A139 068D8 080E0000 (02599)
A140 068DA 080E0000 (02600)
A141 068DC 080E0000 (02601)
A142 068DE 080E0000 (02602)
A143 068DF 080E0000 (02603)
A144 068E0 080E0000 (02604)
A145 068E2 080E0000 (02605)
A146 068E4 080E0000 (02606)
A147 068E6 080E0000 (02607)
A148 068E8 080E0000 (02608)
A149 068EA 080E0000 (02609)
A150 068EC 080E0000 (02610)
A151 068EE 080E0000 (02611)
A152 068EF 080E0000 (02612)
A153 068F0 080E0000 (02613)
A154 068F2 080E0000 (02614)
A155 068F4 080E0000 (02615)
A156 068F6 080E0000 (02616)
A157 068F8 080E0000 (02617)
A158 068FA 080E0000 (02618)
A159 068FC 080E0000 (02619)
A160 068FE 080E0000 (02620)
A161 068FF 080E0000 (02621)
A162 06900 080E0000 (02622)
A163 06902 080E0000 (02623)
A164 06904 080E0000 (02624)
A165 06906 080E0000 (02625)
A166 06908 080E0000 (02626)
A167 0690A 080E0000 (02627)
A168 0690C 080E0000 (02628)
A169 0690E 080E0000 (02629)
A170 06910 080E0000 (02630)
A171 06912 080E0000 (02631)
A172 06914 080E0000 (02632)
A173 06916 080E0000 (02633)
A174 06918 080E0000 (02634)
A175 0691A 080E0000 (02635)
A176 0691C 080E0000 (02636)
A177 0691E 080E0000 (02637)
A178 06920 080E0000 (02638)
A179 06922 080E0000 (02639)
A180 06924 080E0000 (02640)
A181 06926 080E0000 (02641)
A182 06928 080E0000 (02642)
A183 0692A 080E0000 (02643)
A184 0692C 080E0000 (02644)
A185 0692E 080E0000 (02645)
A186 06930 080E0000 (02646)
A187 06932 080E0000 (02647)
A188 06934 080E0000 (02648)
A189 06936 080E0000 (02649)
A190 06938 080E0000 (02650)
A191 0693A 080E0000 (02651)
A192 0693C 080E0000 (02652)
A193 0693E 080E0000 (02653)
A194 06940 080E0000 (02654)
A195 06942 080E0000 (02655)
A196 06944 080E0000 (02656)
A197 06946 080E0000 (02657)
A198 06948 080E0000 (02658)
A199 0694A 080E0000 (02659)
A200 0694C 080E0000 (02660)
A201 0694E 080E0000 (02661)
A202 06950 080E0000 (02662)
A203 06952 080E0000 (02663)
A204 06954 080E0000 (02664)
A205 06956 080E0000 (02665)
A206 06958 080E0000 (02666)
A207 0695A 080E0000 (02667)
A208 0695C 080E0000 (02668)
A209 0695E 080E0000 (02669)
A210 06960 080E0000 (02670)
A211 06962 080E0000 (02671)
A212 06964 080E0000 (02672)
A213 06966 080E0000 (02673)
A214 06968 080E0000 (02674)
A215 0696A 080E0000 (02675)
A216 0696C 080E0000 (02676)
A217 0696E 080E0000 (02677)
A218 06970 080E0000 (02678)
A219 06972 080E0000 (02679)
A220 06974 080E0000 (02680)
A221 06976 080E0000 (02681)
A222 06978 080E0000 (02682)
A223 0697A 080E0000 (02683)
A224 0697C 080E0000 (02684)
A225 0697E 080E0000 (02685)
A226 06980 080E0000 (02686)
A227 06982 080E0000 (02687)
A228 06984 080E0000 (02688)
A229 06986 080E0000 (02689)
A230 06988 080E0000 (02690)
A231 0698A 080E0000 (02691)
A232 0698C 080E0000 (02692)
A233 0698E 080E0000 (02693)
A234 06990 080E0000 (02694)
A235 06992 080E0000 (02695)
A236 06994 080E0000 (02696)
A237 06996 080E0000 (02697)
A238 06998 080E0000 (02698)
A239 0699A 080E0000 (02699)
A240 0699C 080E0000 (02700)
A241 0699E 080E0000 (02701)
A242 069A0 080E0000 (02702)
A243 069A2 080E0000 (02703)
A244 069A4 080E0000 (02704)
A245 069A6 080E0000 (02705)
A246 069A8 080E0000 (02706)
A247 069AA 080E0000 (02707)
A248 069AC 080E0000 (02708)
A249 069AE 080E0000 (02709)
A250 069B0 080E0000 (02710)
A251 069B2 080E0000 (02711)
A252 069B4 080E0000 (02712)
A253 069B6 080E0000 (02713)
A254 069B8 080E0000 (02714)
A255 069BA 080E0000 (02715)
A256 069BC 080E0000 (02716)
A257 069BE 080E0000 (02717)
A258 069C0 080E0000 (02718)
A259 069C2 080E0000 (02719)
A260 069C4 080E0000 (02720)
A261 069C6 080E0000 (02721)
A262 069C8 080E0000 (02722)
A263 069CA 080E0000 (02723)
A264 069CC 080E0000 (02724)
A265 069CE 080E0000 (
```

**FOR GOOD LUCK**

APCIUSSZ=#A-APCIUSSA  
NOP  
END

PAGE 68: (88M-1ENEXD) <MAP> 8BN3R0.WSO.61, 30-Dec-79 16:14:24, E3: KFIELD  
 APS-APCIS INVERSE APC FUNCTION (VIAPC)

```

(02590) * APS-APCIS INVERSE APC FUNCTION (VIAPC)
(02591) * EQN - Y(N)=Y(N-YSIZE)+X(N)+TAP*Y(N-PITCH)
(02592) * INPUT STREAM:
(02593) * (2*-15),PITCH,MOD INSTR,TAP,
(02594) * Y(N-PITCH),X(N)=U(N),DU44Y=U(N),...
(02595) * OUTPUT STREAM:
(02596) * FAD PITCH,MODIFIED INSTR,
(02597) * Y(1),Y(1-YSIZE),Y(2),Y(2-YSIZE),....
(02598) * CALL APCIC(Y,TAP,X,PITCH)
(02599) *
(02600) *
(02601) * NOTE: THIS ROUTINE MODIFIES ITS APS ROUTINE CODE.
(02602) * PITCH IS INPUT, CONVERTED TO INTEGER, AND WRITTEN TO THE
(02603) * BUS 1 COPY OF THE APS INSTRUCTION TO BE MODIFIED.
(02604) * WHEN THE WRITE HAS COMPLETED, THE BUS1 COPY IS READ
(02605) * AND WRITTEN TO APS PSEUDO MEMORY.
(02606) *
(02607) * HEADER BLOCK
(02608) * EVEN
(02609) * ADDR APCISS
(02610) * ADDR APCISS + 2*APCIS$
(02611) * DATA 2
(02612) * DATA APCISS$
(02613) * ADDR APCISSA
(02614) * EVEN
(02615) * APCISS BEGIN APS(APCIS)
(02616) *
(02617) * LEAVE NON-FUNCTIONAL INSTR IN SO DINK WON'T
(02618) * HURT ANYTHING.
(02619) *
(02620) * LOAD(BR0,0)
(02621) * JSN(APCISS$3,P2)
(02622) * SET(RO)
(02623) *
(02624) * GEN (2*-15)
(02625) *
(02626) * LOAD(BR3,SVTSUM1(1),TF)
(02627) *
(02628) * APCISS LOAD(BR0,M$$(1))
(02629) * LOAD(BR1,M$$(1))
(02630) *
(02631) *
(02632) *
(02633) *
(02634) *
(02635) *
(02636) *
(02637) *
(02638) *
(02639) *
(02640) *
(02641) *
(02642) *
  
```

A00 068CC 00400000  
 A01 068CE 02020000  
 A02 068D0 04300030  
  
 A03 068D2 06F203CE  
 A04 068D4 08420000  
 A05 068D6 0A520000  
  
 A06 068D8 0C990013  
  
 A07 068DA 0E300037  
 A08 068DC 10000023  
 A09 068DE 12000020  
  
 \* GEN "MODIFIED INSTR ADDR"

PAGE 61: [98N-TENEXD]<4AP>88N300-MSD-51, 30-Dec-79 16:14:24, ED: KFIELD  
APS-APCIS INVERSE APC FUNCTION (VIAPC)

```

APA 068E0 14F268FE (02643) *      LOAD(BR3,APCIS5H(1),TF)
      (02644) *
      (02645) *      WAIT ON WRITE AGAIN
      (02646) *
APB 068E2 16300E37 (02647) *      SET(WI)
APC 068E4 10E0302F (02648) *      NOP(0)
APD 068E6 1A000E20 (02649) *      NOP(0)
AOE 063E8 1C890011 (02651) *      WCVB(BR0,8P0,IF)      ;GEN TAP
      (02652) *
      (02653) *
APF 068E8 1E000020 (02654) *      NOP(0)
APG 068EC 20000020 (02655) *      NOP(0)
A11 069EE 22000020 (02656) *      NOP(0)
A12 068FF 24000020 (02657) *      NOP(0)
A13 068F2 26000020 (02658) *      NOP(0)
A14 068F4 28000020 (02659) *      NOP(0)
A15 068F6 2A000020 (02660) *      NOP(0)
A16 068F8 2C000020 (02661) *      NOP(0)
A17 068FA 2E000020 (02662) *      NOP(0)
A19 068FC 30000020 (02663) *      NOP(0)
      (02664) *
      (02665) *      ;!!!!!!NEXT INSTR MODIFIED BY OUTPUT P34!!!!!!
      (02666) *
      (02667) *      APCISS4 = #L
      (02668) *      LOAD(BR0,M55)      ;LOAD PITCH
      (02669) *
A19 069FE 32400000 (02670) *      ADD(BR0,BR3)      ;DOUBLE FOR H*NRD ADDR
A1A 06900 34000020 (02671) *      LOAD(BR1,C73)      ;V BASE = V(0)
A1B 06902 36000000 (02672) *      LOAD(BR2,M55)      ;VBS
A1C 06904 38000000 (02673) *      SUB(BR1,M55)      ;SUBTR SPACING
A1D 06906 3A120000 (02674) *      SUB(BR1,BR0)      ;V(0-2-2*PITCH)
A1E 06908 3C100021 (02675) *      LOAD(BR0,C13)      ;X(0)=U BASE
A1F 0690A 3E401029 (02676) *      LOAD(BR2,M55)      ;U SIZE-1
A20 0690C 40600020 (02677) *      SUB(BR2,M55)      ;U SPACING
A21 0690E 42000000 (02678) *      ;INPUT LOOP
      (02679) *      ADD(BR1,B3,TF)      ;X(N-PITCH)
A22 06910 44000000 (02680) *      ADD(BR2,C9,TF)      ;X(N)
A23 06912 46000000 (02681) *      WCVB(BR0,8P0,TF)      ;DOW*V
A24 06914 48000011 (02682) *      SUBL(BR2,1),JUMPP(01)      ;COUNT SAMPLE N AND LOOP
A25 06916 4A2020B1 (02683) *      CLEAR(P1)
A26 06918 4C2000F31 (02684) *      NOP(0)
A27 0691A 4E000020 (02685) *      ;OUTPUT PROGRAM
      (02686) *      (02687) APCISS3 SET(RA)
A28 0691C 52300032 (02688) *
      (02689) *      ; GEN 9US1 ADDR OF INPUT PCM INSTR TO
      (02690) *      BE MODIFIED (LEFT H*NRD)
      (02691) *
A29 0691E 537268FF (02692) *      LOAD(BW3,APCIS5H*1(1),TE)
      (02693) *
      (02694) *      ;WRITE MODIFIED INSTR TO PSEUDO MEM
      (02695) *

```

PAGE 62: IBBN-TEREXD)MAP>BN389.WS.61, 30-Dec-79 16:14:24, Ed: KFIELD  
APS-APCIS INVERSE APC FUNCTION (VIAPC)

```

A2A 06920 54F3FC0 (02696)      LOAD(BW3,APSSLA(1),TF)
      (02697) *
      (02698) *
A26 06922 5640010 (02699)      LOAD(BW0,TF)      ;Y ARRAY
A2C 06924 5050000 (02700)      LOAD(BW1,MS)      ;Y SIZE-1
A2D 06926 5A02F00 (02701)      SUB(BW0,MS)      ;MINDS SPACING
A2E 06928 5C110039 (02702)      ADDL(BW1,1)      ;SIZE Y
A2F 0692A 5E210010 (02703)      MOVW(BW2,BW0)      ;GET Y BASE -2
A30 0692C 60210022 (02704)      SUBW(BW2,BW1)      ;Y-VSIZE-2
A31 0692E 62210022 (02705)      SUBW(BW2,BW1)      ;Y-2*YSIZE-2
A32 06930 64110031 (02706)      SUBL(BW1,1)      ;YSIZE-1
      ;
      * SPACING FOR Y MUST BE 21111
      * OUTPUT LOOP
A33 06932 66A0010 (02709)      ADD(BW0,TF)      ;Y(N)
A34 06934 68A0002 (02710)      ADD(BW2,TF)      ;Y(N)
A35 06936 6A113381 (02711)      SUBL(BW1,1),JUMPP(#2) ;Y(N-VSIZE)
      0006934 (02712)      APCISSA=PC      ;COUNT IT AND LOOP
      0006934 (02713)      CLEAR(R0)      ;CHAIN ANCHOR
A36 06938 6C200030 (02714)      NOP(R0)
A37 0693A 6E000020 (02715)      END
      000693A (02716)      APCSS1 DATA 7F'0.0'-
      000693C (02717)
      ...
      00000075 (02717) APCISSZ=AL-APCIS

```

```

PAGE 63:  EBBN-TSMEXD3CHAP28BN380.4SD.61, 30-Dec-79 15:14:24, Ed: KFIELD
          APU3-PTAP(V,A,U,B,V,C,N)      COMPUTE PITCH AND TAP
                                         AND DO PITCH REMOVAL
(02718) *      APU3-PTAP(V,A,U,B,V,C,N)      COMPUTE PITCH AND TAP
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PAGE 64: [BBN-TELEXOJ]<MAP>884302.450.61, 30-Dec-79 15:14:24, ED: KFIELD
APU3-PTAP(Y,A,O,B,V,C,W) COMPUTE PITCH AND TAP

A01 0694E 09F72094 (02771)  \ A4=1
A02 06950 09152896 (02772)  \ A5=1
A03 06952 16A016A0 (02773)  \ R=2
A04 06954 08C029F7 (02774)  \ A7=U1
A05 06956 089502F5 (02775)  \ A5=2, P=U1
A06 06958 91100010 (02776)  \ A5=2, P=U1
A07 0695A 08F00000 (02777) *
A08 0695C 08F40097 (02778) *
A09 0695E 0811F0041 (02779) *
A0A 06960 080044A0 (02780) *
A0B 06962 91100015 (02781) *
A0C 06964 089708F0 (02782) *
A0D 06966 0811E003E (02783) *
A0E 06968 44A060F4 (02784) *
A0F 0696A 90150007 (02785) *
A10 0696C 02E00000 (02786) *
A11 0696E 08910998 (02787) *
A12 06970 0811F0041 (02788) *
A13 06972 08520003 (02789) *
A14 06974 10000019 (02790) *
A15 06976 080002E0 (02791) *
A16 06978 08910890 (02792) *
A17 0697A 0811E003E (02793) *
A18 0697C 08520000 (02794) *
A19 0697E 622002C2 (02795) *
A1A 06980 089F3000 (02796) *
A1B 06982 9110001F (02797) *
A1C 06984 62C00000 (02798) *
A1D 06986 08950000 (02799) *
A1E 06988 10000021 (02800) *
A1F 0698A 08000090 (02801) *
A20 0698C 08550000 (02802) *
A21 0698E 16C00000 (02803) *
A22 06990 16940000 (02804) *
A23 06992 44010000 (02805) *
A24 06994 41B50000 (02806) *
A25 06996 089C0000 (02807) *
A26 06998 20372037 (02808) *
A27 0699A 08E00000 (02809) *

MOV(1QA,A7) \ MOV(R,A4) \ A4=1
MOV(ZERO,A6) \ MOV(R,A6) \ A5=1
K(2) \ R=2
NOP \ MOV(1QA,A7) \ A7=U1
MOV(R,A5) \ MOV(A5),R(A7) \ A5=2
JUMPS(PTUS1,F+1) \ A5=2, P=U1
PTUSP \ MOV(1QA,A0) \ NOP
MOV(A4),MAX(A7,A0) \ MOV(R,A7) \ A4=LOC, R=MAX \ A7=MAX
CALLS(PTUS4,T2) \ IF NEW MAX
NOP \ ADD(A5,A4) \ R=LOC
JUMPS(PTUS2,F+1) \ EXIT IF OPS IS 000
MOV(R,A7) \ MOV(1QA,A0) \ A7=MAX
CALLS(PTUS3,T1) \ IF NEW MAX
ADD(A5,A4) \ MOV(A4),MAX(A7,A0) \ A4=LOC, R=MAX
JUMPC(PTUS0,F+1) \ EXIT IF OPS IS 000
EXIT IF LOOP BOTTOM
R=MAX \ ---
A1=MAX \ EXO=MAX
--- \ IF NEW MAX
A2=MAX \ ---
TO COMMON CLEAN UP
IF EXIT LOOP MIDDLE
--- \ R=MAX
A1=MAX \ EXO=MAX
IF NEW MAX \ ---
A2=MAX \ ---
COMMON CLEAN UP
R=U-MAX \ R=LOC(U-MAX)
W7 = U(MAX) \ ---
IF FROM RIGHT 801RD
R=LOC(U-MAX) \ ---
A5 = MAX \ ---
--- \ EXO = MAX
A5 = MAX \ ---
ADD 5 TO MAX TO GET PITCH
K(4) \ NOP
MOV(A4),K(1) \ NOP
MOV(A1),ADD(A5,A4) \ NOP
MOV(A5),ADD(A5,A1) \ NOP
MOV(R,0Q) \ NOP
R = 4 \ ---
A4 = 4, R = 1 \ ---
A1 = 1, R = 4+MAX \ ---
A5 = 4+MAX, R = 5+MAX \ ---
0Q = SA = 5+MAX = PITCH
GENERATE INTEGER PITCH: (MULT BY (2**15), THEN ALIGN)
(RESTART APS INPUT PCY BY CLEARING "WI")
CLEAR(WI)
MOV(1QA,W0) \ NOP
; W0 <= (2**15)

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PAGE 65: 108N-TENEXD)C4AP>38W300.MSD.61, 30-Dec-79 16:14:24, E3: KFIELD
APU3-PTAP(Y,A,U,B,V,C,W)
; W4 <= PITCH
; P <= PITCH*(2**--15)
; A6 <= PITCH*(2**--15)
; R <= FWD PITCH (RSHFT JUST IN L H)

MOV(R,W4) \ NOP
MUL(M3,W4) \ NOP
MOV(P,A6) \ NOP
ALIGN(A6) \ NOP
SEND TO BUS1 TO MODIFY BUS1 COPY OF APS INSTR.
MOV(R,OQ) \ NOP
; OQ <= INTEGER PITCH
WAIT TIL BUS1 COPY IS MODIFIED, THEN READ
IT AND WRITE TO APS PSEUDO MEMORY.
(ALSO CLEAR W1 SO APS INPUT PCW WILL CONTINUE.)
(IT WAS WAITING FOR WRITE TO COMPLETE.)
NOP
NOP
NOP
JUMPC(PTUSW1,OQE)
; LOOP WHILE OUTPUT QUEUE NOT EMPTY
NOP
NOP
NOP
NOP
NOP
NOP
NOP
CLEAR(W1)
NOP
MOV(IGA,OQ) \ NOP
; THEN READ MODIFIED APS INSTR
; AND WRITE TO APS PSEUDO MEM.
WAIT TIL APS COPY IS WRITTEN
NOP
JUMPC(PTUSW2,OQE)
SET UP REGS FOR DIVIDE: U(MAX)/U(0)
; 47 ALREADY = U(MAX)
GET W(P) FROM IO
MS = W(P) \ ---
A0 = W(P) \ ---
GO DO DIVIDE TO GET TAP
JUMP(PTUS8)
SURROUTINES
R=LOC
A6=LOC
R(A4) \ NOP
MOV(P,A6) \ NOP
RETURN
NOP \ R(A4)
NOP \ MOV(5,A6)
PSTUPN

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IE 66: [88N-TEMEXD]<NAP>88M300.MSD.61, 30-Dec-79 16:14:24, Ed: KFIELD  
APUS-PTAP(Y,A,U,B,V,C,W)

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A44 06904 16A00000 PTUS8 K(2) \ NOP
A45 06906 08960000 MOV(R,A6) \ NOP
A46 06908 2E000000 RCP(A0) \ NOP
A47 0690A 08800000 MOV(R,M0) \ NOP
A48 0690C 04400000 MUL(M0,M6) \ NOP
A49 0690E 08B10000 MOV(P,A1) \ NOP
A4A 06900 49C00000 SUB(A6,A1) \ NOP
A4B 06902 088C0000 MOV(R,M4) \ NOP
A4C 06904 04000000 MUL(M0,M4) \ NOP
A4D 06906 08A00000 MOV(P,M0) \ NOP
A4E 06908 04400000 MUL(M0,M6) \ NOP
A4F 0690A 08B10000 MOV(P,A1) \ NOP
A50 0690C 49C00000 SUB(A6,A1) \ NOP
A51 0690E 088C0000 MOV(R,M4) \ NOP
A52 06900 04000000 MUL(M0,M4) \ NOP
A53 06902 08A00000 MOV(P,M0) \ NOP
A54 06904 04600000 MUL(M0,M7) \ NOP
A55 06906 08B00000 MOV(P,A0) \ NOP

      R=2
      A6=2
      R1=1/C+DEL(=F0)(= 1/C + 1)
      M0=F0
      P1=F0*C
      A1=F0*C
      R2=2-F0*C (= 1 - C1)
      M4=R2
      P2=F0*R2(=F1)(= 1/C - (1**2)C )
      M0=F1
      P3=F1*C
      A1=P3
      R3=2-F1*C
      M4=R3
      P4=F1*(2-F1*C)(=F2)(= 1/C - (1**4)C**3)) (=1/C)
      M0=F2
      P5=F2*M8 (=8/C)
      A0 = TAP \ ---

NOW DO QUANTIZE AND CODE OF TAP (TAP IS IN A0)
USE (2**15) AS COUNTER INCREMENT

MOV(ZERO,A7) \ NOP
MOV(IGA,A6) \ NOP
SUB(A7,A6) \ NOP
MOV(R,A7) \ NOP

MOV(IGA,A1) \ NOP
ADD(A6,A7) \ MOV(IGA,A0)

MOV(A7),SUB(A0,A1) \ R(A0)
MOV(R, NULL) \ NOP
JUMPS(PTUS11,FWI)
JUMPS(PTUS10,T1)
JUMP(PTUS9)

MOV(IGA, NULL) \ NOP
JUMPC(PTUS10,FWI)

ALIGN(A7) \ MOV(R,OQ)
MOV(R,OQ) \ NOP

      ; A6 <= 2**15 \ ---
      ; A7 <= -(2**15) (SO 1ST ADD =0)
      ; R <= -(2**15)
      ; A1 <= NEXT THRESH \ ---
      ; R<=INCR'D CODE \ A0<=CURRENT QNT
      ; A7<=CODE, R<=IAP-THRESH (T1<=SIGN)
      ; MAKE SURE PREV INSTR COMPLETED
      ; JUMP OUT IF NO MORE
      ; T1 SET IF IAP<THRESH (I.E. DONE)
      ; LOOP IF TAP .GT. CURR. THRESH.
      ; FLUSH REMAINING INPUT
      ; R<=ALIGNED CODE \ OQ<=QUANT VALUE
      ; OQ <= ALIGNED CODED TAP

```



PAGE 68: [BBN-TENEXID] MAP>BBN302.HSO.61, 30-Dec-79 16:14:24, Ed: KFIELD  
APS3-PTAP(Y,A,U,B,V,C,W)

```

(02958) * APS3-PTAP(Y,A,U,B,V,C,W)
(02959) *
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(03004) *
(03005) *
(03006) *
(03007) *
(03008) *
(03009) *
(03010) *

```

Y IS 'TBPR' (BASEBAND WITH PITCH REMOVED)  
A IS 'TPIC' (PITCH)  
U IS 'TBPCP' (AUTOCORR OF B.B. EXCIT. - PITCH CALC PART)  
B IS 'TQAP' (QUANTIZED TAP)  
V IS 'TBEO' (DOWNSAMPLED B.B. EXCITATION)  
C IS 'TCTAP' (LNG FXD CODED TAP - RGT JUSTIFIED IN LEFT HWORD)  
W IS 'TPAC' (AUTOCORR. OF B.B. EXCIT. - WHOLE THING)

```

DO SHAK(U) TO GET U(MAX) AND MAX(=PITCH-5)
ADD 5 TO GET PITCH
DO SDIV (U(MAX)/W(0)) TO GET TAP
DO QTZ TO GET QUANTIZED AND CODED TAP
DO VSMAD(V) TO ACCOMPLISH PITCH REMOVAL

```

INPUT STREAM: U(0),...,U(CBS-1),[FWI],(2\*\*15), 'MODIFIED INSTR',  
W(0), (2\*\*15), TAPQTH(0), TAPQVL(0), TAPQTH(1), TAPQVL(1), ...,  
TAPQTH(15), TAPQVL(15), [FWI],  
V(0-PITCH), V(0), V(1-PITCH), V(1), ...,  
V(VBS-1-PITCH), V(VBS-1), [FWI]

OUTPUT STREAM: A, INTEGER PITCH, 'MODIFIED INSTR',  
B,C,V(0),...,V(VBS-1),[EO]

WHERE:

'TAPQTH' ARE THE TAP QUANT THRESHOLDS (INTERNAL TO APS PGM)  
'TAPQVL' ARE THE TAP QUANT VALUES (INTERNAL TO APS PGM)  
'INTEGER PITCH' IS USED BY APS OUTPUT PGM TO MODIFY  
APS INPUT PGM, SO IT CAN GENERATE  
V(N-PITCH) ADDRESSES.  
'MODIFIED INSTR' IS APS INSTR TO BE MODIFIED.  
IT IS READ FROM BUS1, AND WRITTEN  
TO APS PSEUDO MEMORY.

```

EVEN PTSS1 ; CONSTR INSTR BLK
ADDR PTSS + 2*PTSS ; SCALAR BLK
DATA 3 ; NUMBER OF SCALARS
DATA PTSS2 ; MODULE SIZE
ADDR PTSSA ; PTR TO CHAIN ANCHOR
EVEN

```

BEGIN APS(PTS) ; START OF APS MODULE

INPUT PROGRAM  
REGISTER USAGE:  
BR0: BUFFER 'U', TAPQTH, BUFFER V(N-PITCH) ELEM ADDRS.

PAGE 69: [BBN-TELEXOJ<MAP>BBR300-MSD.61, 3N-Dec-79 16:14:24, Ed: KFIELD  
APS3-PTAP(Y,A,U,B,Y,C,W)

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(03011) *
(03012) *
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(03063) *

A00 06A30 00202A40 ; SET OUTPUT PC
A01 06A3A 02300030 ; RUN OUTPUT PGM

BR1: TAPQVL, BUFFER V(N) ELEM ADDRESSES
BR2: BUFFER SIZES - 1
BR3: SCRATCH

JSM(PTSS0,P2)
SET(R0)

FIRST GEN U(0)-U(UBS-1) ADDRESSES, THEN [FBI]
LOAD(BR0,[1]) ; LOAD U BASE ADDR
LOAD(BR2,MSS) ; LOAD UBS-1
SUB(BR0,MSS) ; SUBTRACT SPACING
ADD(BR0,[9],TF) ; GEN U-ELEM ADDR
SUBL(BR2,1),JUMPP(#1) ; DO UBS ELEMENTS
SET(WI) ; SET WI, WAIT TIL APU PGM CLEARS
NOP(0)

GEN (2*-15)
LOAD(BR3,SVTSUN1(1),TF) ; (2*-15)

WAIT FOR APU PGM TO SIGNAL THAT BUS1
COPY OF MODIFIED INSTR HAS BEEN
WRITTEN.

SET(WI)
NOP(0)
NOP(0)

GEN "MODIFIED INSTR" ADDRESS.

LOAD(BR3,PTSS1(1),TF) ; MODIFIED INSTR (BUS1 ADDR)
GEN W(0) ADDRESS

LOAD(BR3,[3],TF) ; W(0) ADDRESS
LOAD(BR2,MSS) ; DUMMY LOAD
LOAD(BR2,MSS) ; DUMMY LOAD

NOW GEN (2*-15) AND TAPQTR & TAPQVL ADDRESSES, THEN [FBI]
LOAD(BR3,SVTSUN1(1),TF) ; (2*-15)
LOAD(BR0,TAPQTR(1)) ; LOAD THRESH. TABLE BASE
LOAD(BR2,15) ; TABLE SIZE -1
SUB(BR0,2) ; SUBTR SPACING

```



PAGE 71: [88M-TENEXD]<MAP>88M300.NSO.61, 30-Dec-79 16:14:24, Ed: KFIELD  
APS3-PTAP(Y,A,U,B,V,C,W)

```

(03117) * FIRST, GET SCALAR A,B,C ADDRESSES, GEN A
(03118) *
(03119) * PTSSS
A2B 06A0E 56C20000 LOAD(BW0,MSS(1),TF) ; GEN SA ADDR
A2C 06A08 58520000 LOAD(BW1,MSS(1)) ; GET SB ADDR
A2D 06A92 5A620000 LOAD(BW2,MSS(1)) ; GET SC ADDR
(03122) *
(03123) *
(03124) *
(03125) *
(03126) *
A2E 06A94 5D726A79 LOAD(BW3,PTSS1+1(1),TE) ; SEND BUS1 ADDR OF R HW OF INSTR
(03128) *
(03129) *
(03130) *
(03131) *
(03132) *
(03133) *
(03134) *
(03135) *
(03136) *
(03137) *
A30 06A98 60910012 MOV(BW1,BW1,TF) ; GEN SB ADDR
A31 06A9A 63210014 MOV(BW2,BW2,TE) ; GEN SC ADDR (JUST L HW)
A32 06A9C 64400F1A LOAD(BW0,[0]) ; LOAD Y BASE ADDR
A33 06A9E 66600000 LOAD(BW2,MSS) ; LOAD YBS-1
A34 06AAE 68020000 SUB(BW0,MSS) ; SUBTR SPACING
A35 06AA2 6A8A0006 ADD(BW0,[0],TF) ; GEN Y-ELEM ADDR
A36 06AA4 6C2135B1 SUBL(BW2,1),JUMPP(#4) ; DO YBS ELEMS
(03147) *
(03148) *
A37 06AA6 6E200030 CLEAR(RO) ; HALT OUTPUT
A38 06AA8 70000020 NOP(0)
A39 06AAA 72000020 NOP(0)
(03152) *
(03153) * PTSSA=RC
(03154) *
(03155) * END #A-1
(03156) *
(03157) * PTSSI DATA 0F'0.0'-
...
(03158) * PTSSZ=NL-PTSS
(03159) *
(03160) * TAPQTR:
06ABC 0EABF9CF DATA 0.1146233E+00
06ABE 1CF680C0 DATA 0.2262736E+00
06ACB 2A801CC0 DATA 0.3322788E+00
06AC2 371ACDC0 DATA 0.4305055E+00
06AC4 427EC5C0 DATA 0.5194930E+00
06AC6 4C9AFE40 DATA 0.5984800E+00
06AC8 556AE6C0 DATA 0.6673249E+00
06ACA 5CFA3940 DATA 0.7263862E+00

```



**PAGE**

72:

188M-7ENEXD3CMAP>B1

BBN-TENEXDJ<MAP>BBN300.HS  
APS3-PTAP(Y,A,U,B,V,C,W)

61,

37-Dec-79 16:1

**Ed: KFIELD**

06ACC	63600AC0	(03169)	DATA	0.7763685E+00
06ACE	608A2E40	(03170)	DATA	0.0181818E+00
06AD0	60295DC0	(03171)	DATA	0.8528249E+00
06AD2	70C5E608	(03172)	DATA	0.8611901E+00
06AD4	73C82EC9	(03173)	DATA	0.9045466E+00
06AD6	70320D40	(03174)	DATA	0.9234270E+00
06AD8	70271840	(03175)	DATA	0.9386931E+00
06ADA	798A25C0	(03176)	DATA	0.9509933E+00
TAPQVL:				
06ADC	75C3258F	(03177)	DATA	0.5750112E-01
06ADE	15E33D40	(03179)	DATA	0.1709973E+00
06AE0	23084CC0	(03180)	DATA	0.2801340E+00
06AE2	30F40A40	(03181)	DATA	0.3824723E+00
06AE4	3CF486C0	(03182)	DATA	0.4762181E+00
06AE6	478692C0	(03183)	DATA	0.5682592E+00
06AE8	512C1440	(03184)	DATA	0.6341577E+00
06AEA	59596740	(03185)	DATA	0.6980409E+00
06AEC	65089040	(03186)	DATA	0.7524586E+00
06AEE	662C8DC0	(03187)	DATA	0.7982347E+00
06AF0	68001140	(03188)	DATA	0.8363363E+00
06AF2	6F132640	(03189)	DATA	0.8677719E+00
06AF4	725E0DC0	(03190)	DATA	0.8935196E+00
06AF6	75BD0C80	(03191)	DATA	0.9144832E+00
06AF8	773A5F40	(03192)	DATA	0.9314689E+00
06AFA	70F8B92C0	(03193)	DATA	0.9451774E+00

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PAGE 74: CBBN-TENEXD7<MAP>B3N308.NSO.61, 30-Dec-79 16:14:24, Ed: K7IELD  
APU-ENRG(A,B,C,W,D) COMPUTE, CODE & QUANTIZE ENERGY (GAIN)

NO	ADDRESS	INSTR	OPERANDS	COMMENT
0000	0000	NOP		
0001	0001	JUMPS	(ENUS3,FWI)	
0002	0002			
0003	0003			
0004	0004			
0005	0005			
0006	0006			
0007	0007			
0008	0008			
0009	0009			
0010	0010			
0011	0011			
0012	0012			
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0070	0070			
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0080	0080			
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0086	0086			
0087	0087			
0088	0088			
0089	0089			
0090	0090			
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0094	0094			
0095	0095			
0096	0096			
0097	0097			
0098	0098			
0099	0099			

PAGE 75: (80N-TENEXD)<MAP>88H300.MSO.61, 3P-Dec-79 16:14:24, Ed: KFIELD  
 APU-ENRG(A,B,C,N,D) COMPUTE, CODE & QUANTIZE ENERGY (GAIN)

A32 06862	20322032	(03301)	CLEAR(RA)
A33 06864	10000000	(03302)	JUMP(0)
A34 06866	00000000	(03303)	NOP
		(03304)	
	00000035	(03305)	ENUSZ = #A-ENUSZA
06868		(03306)	END ENUSZ
		(03307)	EVEN
		(03308)	
		(03309)	*

PAGE 76: [88M-TENEXD] <MAP> 88M300.MSD.61, 30-Dec-79 16:14:24, Ed: KFIELD  
APS3-ENRG(A,B,C,W,D)

```

(03310) * APS3-ENRG(A,B,C,W,D)
(03311) *
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06868 000068BE
0686A 00006870
0686C 0000
0686D 0052
0686E 00006888

A IS OUTPUT SCALAR: QUANTIZED GAIN
B IS OUTPUT SCALAR: INVERSE OF QUANT. GAIN
C IS OUTPUT SCALAR: CODED GAIN
W IS INPUT BUFFER: 8-B. WITH PITCH REMOVED
D IS INPUT SCALAR: INVERSE OF UNSAMPLED FRAMESIZE

INPUT STREAM: SD,W(0),...,W(WBS-1),IFW1J,
(2**--15),ENRQTH(0),ENRQVL(0),ENRQVI(0),...,
ENRQTH(63),ENRQVL(63),ENRQVI(63),IF1J

OUTPUT STREAM: SA,SB,SC,LE0J

WHERE: 'ENRQTH' ARE THE GAIN QUANT THRESHOLDS
'ENRQVL' ARE THE GAIN QUANT VALUES
'ENRQVI' ARE THE GAIN QUANT INVERSE VALUES
(ALL INTERNAL TO APS PGM.)

EVEN ENSSI ; CONSTR INSTR BLK
ADDR ENSS + 2*ENSSS ; SCALAR BLK
DATA 4 ; NUMBER OF SCALARS
DATA ENSZ ; MODULE SIZE
ADDR ENSA ; PTR TO CHAIN ANCHOR
EVEN

BEGIN APS(ENS) ; START OF APS MODULE

INPUT PROGRAM
REGISTER USAGE:
BR0: BUFFER 'W', ENRQTH ELEM ADDRS
BR1: ENRQVL ELEM ADDRS
BR2: ENRQVI ELEM ADDRS, W BUFFER SIZE-1
BR3: SCALAR D ADDR, SCRATCH

GET SCALAR ADDRESSES
LOAD(BR0,MS(1)) ; SA ADDR
LOAD(BR1,MS(1)) ; SB ADDR
LOAD(BR2,MS(1)) ; SC ADDR
LOAD(BR3,MS(1),TF) ; GEN SD ADDR

MOV8(BW0,BR0) ; PUT SA,SB,SC INTO OUTPUT REGS.
MOV8(BW1,BR1)
MOV8(BW2,BR2)
JSH(ENSS0,P2) ; SET OUTPUT PC
SET(R0) ; RUN OUTPUT PGM

```

PAGE 77: [BBH-TENEID]<MAP>BBN300.WSO.61, 30-Dec-79 16:14:24, Ed: KFIELD  
APS3-ENRG(A,B,C,W,D)

	(03363) *	GEN W ADDRESSES	
	(03364) *		
A09 06882 12403000	(03365)	LOAD(BR0,I3)	; LOAD W BASE ADDR
A0A 06884 14600000	(03366)	LOAD(BR2,M5)	; WBS-1
A0B 06886 16020000	(03367)	SUB(BR0,M5)	; SUBTRACT SPACING
	(03368) *		
A0C 06888 180A0006	(03369) #1	ADD(BR0,I1),TF	; GEN W-ELEM ADDR
A0D 0688A 1A290C81	(03370)	SUBL(BR2,1),JUMPP(#1)	; DO WBS ELEMENTS
	(03371) *		
A0E 0688C 1C300037	(03372)	SET(WI)	
A0F 0688E 1E000020	(03373)	NOP(0)	
A10 06890 20000020	(03374)	NOP(0)	
	(03375) *		
	(03376) *		
	(03377) *		
	(03378) *		
A11 06892 22F203CE	(03379)	LOAD(BR3,SVT\$UNI(1),TF)	; (2**--15)
	(03380) *		
A12 06894 244260C2	(03381)	LOAD(BR0,ENRQTH(1))	; LOAD THRESH TABLE BASE
A13 06896 26020002	(03382)	SUB(BR0,2)	; SUBTR SPACING
A14 06898 28526C42	(03383)	LOAD(BR1,ENRQVL(1))	; LOAD VALUE TABLE BASE
A15 0689A 2A120002	(03384)	SUB(BR1,2)	; SUBTR SPACING
A16 0689C 2C626C02	(03385)	LOAD(BR2,ENRQVI(1))	; LOAD IVAL TABLE BASE
A17 0689E 2E220002	(03386)	SUB(BR2,2)	; SUBTR SPACING
	(03387) *		
A18 068A0 3070003F	(03388)	LOAD(BR3,63)	; TABLE SIZE - 1
	(03389) *		
A19 068A2 328A0002	(03390) #2	ADD(BR0,2,TF)	; GEN THRESH ADDR
A1A 068A4 349A0002	(03391)	ADD(BR1,2,TF)	; GEN VAL ADDR
A1B 068A6 36AA0002	(03392)	ADD(BR2,2,TF)	; GEN IVAL ADDR
A1C 068A8 38391901	(03393)	SUBL(BR3,1),JUMPP(#2)	; DO 64 ELEMS
	(03394) *		
A1D 068AA 3A200031	(03395)	CLEAR(RI)	; HALT INPUT
A1E 068AC 3C000020	(03396)	NOP(0)	
A1F 068AE 3E000020	(03397)	NOP(0)	
	(03398) *		
	(03399) *		
	(03400) *	OUTPUT PROGRAM	
	(03401) *	REGISTER USAGE:	
	(03402) *	BW0: SA	
	(03403) *	BW1: SB	
	(03404) *	BW2: SC	
	(03405) *	BW3: UNUSED	
	(03406) *		
	(03407) *		
A20 068B0 40300032	(03408)	SET(RA)	; TURN ON APU
	(03409) *		
	(03410) *	GEN SA,SB,SC ADDRS	
	(03411) *		
A21 068B2 42810010	(03412)	MOV8(BW0,BW0,TF)	; SA
A22 068B4 44910012	(03413)	MOV8(BW1,BW1,TF)	; SB
A23 068B6 46A10014	(03414)	MOV8(BW2,BW2,TF)	; SC
	(03415) *		



PAGE 79: [BBN-TENEXD]C[NAP>BBN300.MSO.61, 30-Dec-79 16:14:24, ED: KFIELD  
APS3-ENRG(A,B,C,W,D)

06C10 16813F3E (03468)	DATA	0.6925163E-03
06C12 1880E03E (03469)	DATA	0.8402916E-03
06C14 2169663E (03470)	DATA	0.1019600E-02
06C16 288A268E (03471)	DATA	0.1237172E-02
06C18 3130898E (03472)	DATA	0.1501170E-02
06C1A 38AFDF0E (03473)	DATA	0.1821503E-02
06C1C 486C6F3E (03474)	DATA	0.2210192E-02
06C1E 57E0C08E (03475)	DATA	0.2681022E-02
06C20 6A114F3E (03476)	DATA	0.3254093E-02
06C22 0816243F (03477)	DATA	0.3948480E-02
06C24 09CFE2BF (03478)	DATA	0.4791041E-02
06C26 0BE7E4BF (03479)	DATA	0.5813396E-02
06C28 0E7247BF (03480)	DATA	0.7053909E-02
06C2A 118773BF (03481)	DATA	0.8559135E-02
06C2C 1945063F (03482)	DATA	0.1038556E-01
06C2E 19CEE3F (03483)	DATA	0.1260172E-01
06C30 1F50C73F (03484)	DATA	0.1529079E-01
06C32 25FF773F (03485)	DATA	0.1855367E-01
06C34 2E1833BF (03486)	DATA	0.2251282E-01
06C36 37F10E0F (03487)	DATA	0.2731680E-01
06C38 43E1FF3F (03488)	DATA	0.3314590E-01
06C3A 525E453F (03489)	DATA	0.4021087E-01
06C3C 63F1D73F (03490)	DATA	0.4880112E-01
06C3E 794593BF (03491)	DATA	0.5921474E-01
06C40 093265C0 (03492)	DATA	0.7185050E-01
ENRQVL:		
06C42 12E8A9BE (03494)	DATA	0.5770521E-03
06C44 14D42EBE (03495)	DATA	0.6356456E-03
06C46 16F198BE (03496)	DATA	0.7001006E-03
06C48 194602BE (03497)	DATA	0.7712053E-03
06C4A 1B06F83E (03498)	DATA	0.8496010E-03
06C4C 1EAA33E (03499)	DATA	0.9358689E-03
06C4E 21C7C83E (03500)	DATA	0.1030096E-02
06C50 2535E03E (03501)	DATA	0.1135573E-02
06C52 28FD203E (03502)	DATA	0.1250870E-02
06C54 2D26983E (03503)	DATA	0.1377092E-02
06C56 318C3EBE (03504)	DATA	0.1517002E-02
06C58 36C910BE (03505)	DATA	0.1671918E-02
06C5A 3C592A3E (03506)	DATA	0.1841684E-02
06C5C 4279DC3E (03507)	DATA	0.2028687E-02
06C5E 4939D63E (03508)	DATA	0.2234678E-02
06C60 50A947BE (03509)	DATA	0.2461506E-02
06C62 58D9FEBE (03510)	DATA	0.2711534E-02
06C64 61DF983E (03511)	DATA	0.298601E-02
06C66 68CF8C3E (03512)	DATA	0.3290145E-02
06C68 76C2318E (03513)	DATA	0.3624224E-02
06C6A 082D133F (03514)	DATA	0.3992225E-02
06C6C 09019ABF (03515)	DATA	0.4397593E-02
06C6E 09E8873F (03516)	DATA	0.484122E-02
06C70 0AED983F (03517)	DATA	0.5335990E-02
06C72 0C09A93F (03518)	DATA	0.5877003E-02
06C74 0D42923F (03519)	DATA	0.6474631E-02
06C76 0E98413F (03520)	DATA	0.7132061E-02



000-TEMEIDJ<4AP>88N300.450.61, 30-Dec-79 16:14:24, Ed: KFIELD  
 APS3-EMRG(A,B,C,W,D)

06C70 1016E3F (03521)	DATA	0.7856245E+02
06C71 1109283F (03522)	DATA	0.8653963E+02
06C72 13050E8F (03523)	DATA	0.9532681E+02
06C73 1501593F (03524)	DATA	0.1050062E+01
06C74 17005C3F (03525)	DATA	0.1156685E+01
06C75 1A10218F (03526)	DATA	0.1274134E+01
06C76 1C8E6C8F (03527)	DATA	0.1403508E+01
06C77 1FA9088F (03528)	DATA	0.1546820E+01
06C78 22E0A13F (03529)	DATA	0.1703001E+01
06C79 26683D3F (03530)	DATA	0.1875923E+01
06C80 2A51E73F (03531)	DATA	0.2066403E+01
06C81 2E9DF03F (03532)	DATA	0.2276224E+01
06C82 3359BC3F (03533)	DATA	0.2507350E+01
06C83 3890888F (03534)	DATA	0.2761945E+01
06C84 3E4EE43F (03535)	DATA	0.3042391E+01
06C85 44A2803F (03536)	DATA	0.3351313E+01
06C86 489AA18F (03537)	DATA	0.3691603E+01
06C87 5347E33F (03538)	DATA	0.4066446E+01
06C88 588CB18F (03539)	DATA	0.4479350E+01
06C89 650D503F (03540)	DATA	0.4934180E+01
06C90 6F50103F (03541)	DATA	0.5435193E+01
06C91 7A9D893F (03542)	DATA	0.5987079E+01
06C92 80710CC0 (03543)	DATA	0.6595082E+01
06C93 894C7B40 (03544)	DATA	0.7264654E+01
06C94 9A3E31C0 (03545)	DATA	0.8002302E+01
06C95 9B487340 (03546)	DATA	0.8814850E+01
06C96 EC608DC0 (03547)	DATA	0.9709904E+01
06C97 0D80D40 (03548)	DATA	0.1069508E+00
06C98 0F1480C0 (03549)	DATA	0.1170189E+00
06C99 109C8340 (03550)	DATA	0.1297821E+00
06C00 124C0440 (03551)	DATA	0.1429601E+00
06C01 1428E40 (03552)	DATA	0.1574762E+00
06C02 163423C0 (03553)	DATA	0.1734662E+00
06C03 18754E40 (03554)	DATA	0.1910799E+00
06C04 1AF112C0 (03555)	DATA	0.2104020E+00
06C05 1DAD65C0 (03556)	DATA	0.2310542E+00
06C06 20B0D540 (03557)	DATA	0.2553965E+00
ENRQVI:		
06CC2 36279143 (03558)	DATA	0.1732946E+04
06CC3 3129A1C3 (03559)	DATA	0.1573204E+04
06CC4 2CA17EC3 (03560)	DATA	0.1420187E+04
06CC5 28848C3 (03561)	DATA	0.1296537E+04
06CC6 24C82F43 (03562)	DATA	0.1177023E+04
06CC7 2163543 (03563)	DATA	0.1060526E+04
06CC8 1E503CC3 (03564)	DATA	0.9700296E+03
06CC9 1884E743 (03565)	DATA	0.8806128E+03
06CD0 18F8B1C3 (03566)	DATA	0.7994383E+03
06CD1 16ADF6C3 (03567)	DATA	0.7257465E+03
06CD2 1496C7C3 (03568)	DATA	0.6580476E+03
06CD3 12B0EC43 (03569)	DATA	0.5981154E+03
06CD4 10F7D9C3 (03570)	DATA	0.5429814E+03
06CD5 0F677043 (03571)	DATA	0.4929297E+03
06CD6 0DF8EF43 (03572)	DATA	0.4474917E+03

PAGE 81: [BBN-TEHEDJCNAP>8BN300.HSO.61, 30-Dec-79 16:14:24, Ed: KFIELD  
 APS3-ENRG(A,B,C,W,D)

06CE0	0CB1EFC3 (03574)	DATA	0.4062421E+03
06CE2	08865C43 (03575)	DATA	0.3687950E+03
06CE4	0A766SC3 (03576)	DATA	0.3347996E+03
06CE6	097F0143 (03577)	DATA	0.3039300E+03
06CE8	089F5E43 (03578)	DATA	0.2759211E+03
06CEA	703E52C2 (03579)	DATA	0.2504869E+03
06CEC	7182D442 (03580)	DATA	0.2273971E+03
06CEE	6737C942 (03581)	DATA	0.2064358E+03
06CF0	50840842 (03582)	DATA	0.1874066E+03
06CF2	5510D842 (03583)	DATA	0.1701316E+03
06CF4	403975C2 (03584)	DATA	0.1544489E+03
06CF6	46101FC2 (03585)	DATA	0.1402119E+03
06CF8	3FA4C642 (03586)	DATA	0.1272873E+03
06CFA	39C6E942 (03587)	DATA	0.1155540E+03
06CFC	34737EC2 (03588)	DATA	0.1049023E+03
06CFE	2F90C0C2 (03589)	DATA	0.9523245E+02
06D00	203A18C2 (03590)	DATA	0.8645397E+02
06D02	273E0A42 (03591)	DATA	0.7848469E+02
06D04	23A00C2 (03592)	DATA	0.7125002E+02
06D06	20575342 (03593)	DATA	0.6460223E+02
06D08	1D5C2442 (03594)	DATA	0.5871985E+02
06D0A	1AA74EC2 (03595)	DATA	0.5330709E+02
06D0C	183256C2 (03596)	DATA	0.4839327E+02
06D0E	15F75942 (03597)	DATA	0.4393241E+02
06D10	13F0FDC2 (03598)	DATA	0.3980274E+02
06D12	121A6A42 (03599)	DATA	0.3620637E+02
06D14	106F37C2 (03600)	DATA	0.3286889E+02
06D16	0EE865C2 (03601)	DATA	0.2903905E+02
06D18	0D8853C2 (03602)	DATA	0.2708850E+02
06D1A	0C488642 (03603)	DATA	0.2459150E+02
06D1C	0B298EC2 (03604)	DATA	0.2232467E+02
06D1E	0A222642 (03605)	DATA	0.2026679E+02
06D20	093305C2 (03606)	DATA	0.1839861E+02
06D22	0859F042 (03607)	DATA	0.1670264E+02
06D24	7940D2C1 (03608)	DATA	0.1516300E+02
06D26	6E1F4841 (03609)	DATA	0.1376528E+02
06D28	63F0A0C1 (03610)	DATA	0.1249640E+02
06D2A	5AC183C1 (03611)	DATA	0.1134449E+02
06D2C	52630C41 (03612)	DATA	0.1029876E+02
06D2E	4AC8A0C1 (03613)	DATA	0.9349428E+01
06D30	43E69C41 (03614)	DATA	0.8487603E+01
06D32	3DA44A41 (03615)	DATA	0.7705220E+01
06D34	37F5AC41 (03616)	DATA	0.6994957E+01
06D36	32CD23C1 (03617)	DATA	0.6350166E+01
06D38	2E1E5541 (03618)	DATA	0.5764811E+01
06D3A	29DE0841 (03619)	DATA	0.5233414E+01
06D3C	26020CC1 (03620)	DATA	0.471001E+01
06D3E	22812341 (03621)	DATA	0.4313056E+01
06D40	1F52E7C1 (03622)	DATA	0.3915481E+01

\* \*  
 (03623) \*  
 (03624) \*

PAGE 82: LBN-TEHETOJCHAP8BN300.MSD.61, 30-Dec-79 16:14:24, Ed: KFIELD  
 APU3-DCOR(V,U,V) DIRECT CONVOLUTION

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(03625) * APU3-DCOR(Y,U,V) DIRECT CONVOLUTION
(03626) *
(03627) *
(03628) * Y(M)=SUMCD(K)*V(K+M)J K=0,1,...R-1,M=0,1,...M-1
(03629) * WHERE R IS SIZE OF REFERENCE, M SIZE OF OUTPUT
(03630) *
(03631) * Y(M)=S0,Y(M+1)=S1,Y(M+2)=S2 AND Y(M+3)=S3 COMPUTED TOGETHER
(03632) * M=0,4,...
(03633) * REGISTER ASSIGNMENTS:
(03634) * M0 U(K)
(03635) * M1 U(K+1)
(03636) * M2 U(K+2)
(03637) * M3 U(K+3)
(03638) * M4 V(K+M) OR V(K+M+4)
(03639) * M5 V(K+M+1) OR V(K+M+5)
(03640) * M6 V(K+M+2) OR V(K+M+6)
(03641) * M7 V(K+M+3) OR V(K+M+7)
(03642) *
(03643) *
(03644) *
(03645) *
(03646) *
(03647) *
(03648) DCRUS BEGIN APU(DCRU)
(03649) * #A=0
(03650) *
(03651) DCRUSSA MOV(IQA,M4) V(0)
(03652) MOV(ZERO,A1)\MOV(ZERO,A0) CLEAR S0S1
(03653) MOV(IQA,M5) V(1)
(03654) MOV(ZERO,A3)\MOV(ZERO,A2) CLEAR S2S3
(03655) MOV(IQA,M6) V(2)
(03656) *
(03657) DCRUP: MOV(IQA,M0) U(0)
(03658) MOV(IQA,M7) V(M+3)
(03659) NOP WAIT FOR EO TO BE SET
(03660) NOP
(03661) NOP
(03662) NOP
(03663) NOP
(03664) NOP
(03665) NOP
(03666) NOP
(03667) NOP
(03668) NOP
(03669) NOP
(03670) NOP
(03671) NOP
(03672) NOP
(03673) NOP
(03674) NOP
(03675) NOP
(03676) JUMPS(DCREND,EO)
(03677) JUMPS(DCLUB,FWI)
  
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CHECK FOR ONLY ONE PROD NEEDED

PAGE 83: [88N-TENEXD]<MAP>88N380.M50.61, 38-Dec-79 16:14:24, Ed: KFIELD  
APU3-DCOR(Y,U,V) DIRECT CONVOLUTION

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A1A 06D78 84088420 (03678) DCRU0: MUL(M6,M4)\MUL(M6,M5) PS0(K);PS1(K)
A1B 06D7A 08E908E9 (03679) MOV(IQA,M1) U(K+1)
A1C 06D7C 84508471 (03680) MOV(A0),MUL(M0,M6)\MOV(A1),MUL(M0,M7) A0=PS0(K),PS2(K);
A1=PS1(K),PS3(K)
A1D 06D7E 08EC08EC (03681) MOV(IQA,M4) V(M+K+4)
A1E 06D80 41041100 (03682) ADD(A0,A1)\ADD(A0,A1) S0(K);S1(K)
A1F 06D82 91160848 (03683) JUMPS(DCLU1,FWI)
A20 06D84 84B284D3 (03684) *
A20 06D84 84B284D3 (03686) DCRU1: MOV(A2),MUL(M1,M5)\MOV(A3),MUL(M1,M6)
A21 06D86 08EA08EA (03687) * MOV(IQA,M2) U(K+2)
A22 06D88 43514350 (03688) MOV(A1),ADD(A2,A3)\MOV(A0),ADD(A2,A3)
A23 06D8A 84F08491 (03689) * MOV(A0),MUL(M1,M7)\MOV(A1),MUL(M1,M4)
A24 06D8C 41134112 (03690) * MOV(A3),ADD(A0,A1)\MOV(A2),ADD(A0,A1)
A25 06D8E 08ED08ED (03691) * MOV(IQA,M5) V(M+K+5)
A26 06D90 9116085A (03692) * JUMPS(DCLU2,FWI)
A27 06D92 85528573 (03693) * MOV(A2),MUL(M2,M6)\MOV(A3),MUL(M2,M7)
A28 06D94 08E808E8 (03694) * MOV(IQA,M3) U(K+3)
A29 06D96 43514350 (03695) * MOV(A1),ADD(A2,A3)\MOV(A0),ADD(A2,A3)
A2A 06D98 85108531 (03696) * MOV(A0),MUL(M2,M4)\MOV(A1),MUL(M2,M5)
A2B 06D9A 41134112 (03697) * MOV(A3),ADD(A0,A1)\MOV(A2),ADD(A0,A1)
A2C 06D9C 08EE08EE (03698) * MOV(IQA,M6) V(M+K+6)
A2D 06D9E 91160869 (03699) * JUMPS(DCLU3,FWI)
A2E 06DA0 85F28593 (03700) * MOV(A2),MUL(M3,M7)\MOV(A3),MUL(M3,M4)
A2F 06DA2 08E808E8 (03701) * MOV(IQA,M0) U(K+4)
A30 06DA4 43514350 (03702) * MOV(A1),ADD(A2,A3)\MOV(A0),ADD(A2,A3)
A31 06DA6 858085D1 (03703) * MOV(A0),MUL(M3,M5)\MOV(A1),MUL(M3,M6)
A32 06DA8 41134112 (03704) * MOV(A3),ADD(A0,A1)\MOV(A2),ADD(A0,A1)
A33 06DAA 08EF08EF (03705) * MOV(IQA,M7) V(M+K+7)
A34 06DAC 91160878 (03706) * JUMPS(DCLU4,FWI)
A35 06DAE 84128433 (03707) * MOV(A2),MUL(M0,M4)\MOV(A3),MUL(M0,M5)
A36 06D80 88E908E9 (03708) * MOV(IQA,M1) U(K+5)
A37 06D82 43514350 (03709) * MOV(A1),ADD(A2,A3)\MOV(A0),ADD(A2,A3)
A38 06D84 84508471 (03710) * MOV(A0),MUL(M0,M6)\MOV(A1),MUL(M0,M7)
A39 06D86 41134112 (03711) * MOV(A3),ADD(A0,A1)\MOV(A2),ADD(A0,A1)
A2=PS2(K),PS0(K+1);
A3=PS3(K),PS1(K+1)
A1=S0(K),S2(K);
A0=PS1(K),S3(K)
A0=PS0(K+1),PS2(K+1);
A1=PS1(K+1),PS3(K+1)
A3=S2(K),S0(K+1);
A2=S3(K),S1(K+1)
A2=PS2(K+1),PS0(K+2);
A3=PS3(K+1),PS1(K+2)
A1=S0(K+1),S2(K+1);
A0=PS1(K+1),S3(K+1)
A0=PS0(K+2),PS2(K+2);
A1=PS1(K+2),PS3(K+2)
A3=S2(K+1),S0(K+2);
A2=S3(K+1),S1(K+2)
A2=PS2(K+2),PS0(K+3);
A3=PS3(K+2),PS1(K+3)
A1=S0(K+2),S2(K+2);
A0=PS1(K+2),S3(K+2)
A0=PS0(K+3),PS2(K+3);
A1=PS1(K+3),PS3(K+3)
A3=S2(K+2),S0(K+3);
A2=S3(K+2),S1(K+3)
A2=PS2(K+3),PS0(K+4);
A3=PS3(K+3),PS1(K+4)
A1=S0(K+3),S2(K+3);
A0=PS1(K+3),S3(K+3)
A0=PS0(K+4),PS2(K+4);
A1=PS1(K+4),PS3(K+4)
A3=S2(K+3),S0(K+4);
A2=S3(K+3),S1(K+4)

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PAGE 84: [BBN-TENEXDJ<MAP>88M300.HS0.61, 3e-Dec-79 16:14:24, Ed: KFIELD  
AP03-DCOR(Y,U,V) DIRECT CONVOLUTION

A3A 06000 08EC08EC (03731)	MOV(IA0,M4)	Y(M+K+0)
A3B 0600A 9116004B (03732)	JUMPS(DCLU1,FWI)	
A3C 0600C 10000020 (03733)	JUMP(DCRU1)	
(03734) *		
(03735) *		
(03736) *		
A3D 0600E 20372037 (03737) DCLU0:	CLEAR(WI)	
A3E 0600B 04000420 (03738)	MUL(M0,M4)\MUL(M0,M5)	PS0(K);PS1(K)
A3F 0600C 84500471 (03739)	MOV(M0,M4)\MUL(M0,M6)\MOV(A1)\MUL(M0,M7)	A0=PS0(K),PS2(K); A1=PS1(K),PS3(K)
(03740) *		
A40 260C4 41004100 (03741)	ADD(A0,A1)\ADD(A0,A1)	S0(K);S1(K)
A41 060C6 089C0810 (03742)	MOV(R,0Q)\MOV(ZERO,A0)	OUT=S0(M);CLEAR S1
A42 060C8 0811089C (03743)	MOV(ZERO,A1)\MOV(R,0Q)	CLEAR S0;OUT=S1(M)
A43 060CA 08B20883 (03744)	MOV(P,A2)\MOV(P,A3)	PS2;PS3
A44 060CC 43004340 (03745)	ADD(A2,A3)	S2;S3
A45 060CE 08EC08EC (03746)	MOV(IA0,M4)	V(M+4)
A46 060D0 08ED08ED (03747)	MOV(IA0,M5)	V(M+5)
A47 060D2 089C0812 (03748)	MOV(R,0Q)\MOV(ZERO,A2)	OUT=S2(M);CLEAR S3
A48 060D4 0813089C (03749)	MOV(ZERO,A3)\MOV(R,0Q)	CLEAR S2;OUT=S3(M)
A49 060D6 08EE08EE (03750)	MOV(IA0,M6)	V(M+6)
A4A 060D8 10000005 (03751)	JUMP(DCRUP)	
(03752) *		
A4B 06DDA 20372037 (03753) DCLU1:	CLEAR(WI)	
A4C 06DDC 04B204D3 (03754)	MOV(A2)\MUL(M1,M5)\MOV(A3)\MUL(M1,M6)	A2=PS2(K),PS0(K+1); A3=PS3(K),PS1(K+1)
(03755) *		
A4D 06DDE 43514350 (03756)	MOV(A1)\ADD(A2,A3)\MOV(A0)\ADD(A2,A3)	A1=S0(K),S2(K); A0=S1(K),S3(K)
(03757) *		
A4E 06DE0 04F00491 (03758)	MOV(A0)\MUL(M1,M7)\MOV(A1)\MUL(M1,M4)	A0=PS0(K+1),PS2(K+1); A1=PS1(K+1),PS3(K+1)
(03759) *		
A4F 06DE2 41134112 (03760)	MOV(A3)\ADD(A0,A1)\MOV(A2)\ADD(A0,A1)	A2=PS2(K),PS0(K+1); A3=PS3(K),PS1(K+1)
(03761) *		
A50 06DE4 089C0810 (03762)	MOV(R,0Q)\MOV(ZERO,A0)	OUT=S0(M);CLEAR S1
A51 06DE6 0811089C (03763)	MOV(ZERO,A1)\MOV(R,0Q)	CLEAR S0;OUT=S1(M)
A52 06DE8 08B20883 (03764)	MOV(P,A2)\MOV(P,A3)	PS2;PS3
A53 06DEA 43004340 (03765)	ADD(A2,A3)	S2;S3
A54 06DEC 08EC08EC (03766)	MOV(IA0,M4)	V(M+4)
A55 06DEE 08ED08ED (03767)	MOV(IA0,M5)	V(M+5)
A56 06DF0 089C0812 (03768)	MOV(R,0Q)\MOV(ZERO,A2)	OUT=S2(M);CLEAR S3
A57 06DF2 0813089C (03769)	MOV(ZERO,A3)\MOV(R,0Q)	CLEAR S2;OUT=S3(M)
A58 06DF4 08EE08EE (03770)	MOV(IA0,M6)	V(M+6)
A59 06DF6 10000005 (03771)	JUMP(DCRDP)	
(03772) *		
(03773) *		
A5A 06DF8 20372037 (03774) DCLU2:	CLEAR(WI)	
A5B 06DFA 05520573 (03775)	MOV(A2)\MUL(M2,M6)\MOV(A3)\MUL(M2,M7)	A2=PS2(K+1),PS0(K+2); A3=PS3(K+1),PS1(K+2)
(03776) *		
A5C 06DFC 43514350 (03777)	MOV(A1)\ADD(A2,A3)\MOV(A0)\ADD(A2,A3)	A1=S0(K+1),S2(K+1); A0=S1(K+1),S3(K+1)
(03778) *		
A5D 06DFE 05100531 (03779)	MOV(A0)\MUL(M2,M4)\MOV(A1)\MUL(M2,M5)	A0=PS0(K+2),PS2(K+2); A1=PS1(K+2),PS3(K+2)
(03780) *		
A5E 06E00 41134112 (03781)	MOV(A3)\ADD(A0,A1)\MOV(A2)\ADD(A0,A1)	A3=S2(K+1),S0(K+2); A2=S3(K+1),S1(K+2)
(03782) *		
A5F 06E02 089C0810 (03783)	MOV(R,0Q)\MOV(ZERO,A0)	OUT=S0(M);CLEAR S1

PAGE 05: CBBN-TEWEIDJCHAP>88N300.MSD.61, 38-Dec-79 16:14:24, Ed: KFIELD  
AP03-DCOR(Y,U,V) DIRECT CONVOLUTION

A60 06E04 0811089C (03784)	MOV(ZERO,A1)\MOV(R,OQ)	CLEAR S0;OUT=S1(M)	
A61 06E06 08220883 (03785)	MOV(P,A2)\MOV(P,A3)	PS2;PS3	
A62 06E08 43484340 (03786)	ADD(A2,A3)	S2;S3	
A63 06E0A 08EC08EC (03787)	MOV(IQA,M4)	V(M+4)	
A64 06E0C 08CD08ED (03788)	MOV(IQA,M5)	V(M+5)	
A65 06E0E 089C0812 (03789)	MOV(R,OQ)\MOV(ZERO,A2)	OUT=S2(M);CLEAR S3	
A66 06E10 0813089C (03790)	MOV(ZERO,A3)\MOV(R,OQ)	CLEAR S2;OUT=S3(M)	
A67 06E12 08EE08EE (03791)	MOV(IQA,M6)	V(M+6)	
A68 06E14 10000805 (03792)	JUMP(DCRUP)		
A69 06E16 28372837 (03795)	CLEAR(WI)		
A6A 06E18 85F28593 (03796)	MOV(A2),MUL(M3,M7)\MOV(A3),MUL(M3,M4)	A2=PS2(K+2),PS0(K+3);	
		A3=PS3(K+2),PS1(K+3)	
A68 06E1A 43514350 (03797)	MOV(A1),ADD(A2,A3)\MOV(A0),ADD(A2,A3)	A1=S0(K+2),S2(K+2);	
		A0=S1(K+2),S3(K+2)	
A6C 06E1C 858085D1 (03800)	MOV(A0),MUL(M3,M5)\MOV(A1),MUL(M3,M6)	A0=PS0(K+3),PS2(K+3);	
		A1=PS1(K+3),PS3(K+3)	
A6D 06E1E 41134112 (03802)	MOV(A3),ADD(A0,A1)\MOV(A2),ADD(A0,A1)	A3=S2(K+2),S0(K+3);	
		A2=S3(K+2),S1(K+3)	
A6E 06E20 089C0810 (03804)	MOV(R,OQ)\MOV(ZERO,A0)	OUT=S0(M);CLEAR S1	
A6F 06E22 0811089C (03805)	MOV(ZERO,A1)\MOV(R,OQ)	CLEAR S0;OUT=S1(M)	
A70 06E24 08220883 (03806)	MOV(P,A2)\MOV(P,A3)	PS2;PS3	
A71 06E26 43404340 (03807)	ADD(A2,A3)	S2;S3	
A72 06E28 08EC08EC (03808)	MOV(IQA,M4)	V(M+4)	
A73 06E2A 08ED08ED (03809)	MOV(IQA,M5)	V(M+5)	
A74 06E2C 089C0812 (03810)	MOV(R,OQ)\MOV(ZERO,A2)	OUT=S2(M);CLEAR S3	
A75 06E2E 0813089C (03811)	MOV(ZERO,A3)\MOV(R,OQ)	CLEAR S2;OUT=S3(M)	
A76 06E30 08EE08EE (03812)	MOV(IQA,M6)	V(M+6)	
A77 06E32 10000805 (03813)	JUMP(DCRUP)		
A78 06E34 28372837 (03815)	CLEAR(WI)		
A79 06E36 84128433 (03816)	MOV(A2),MUL(M0,M4)\MOV(A3),MUL(M0,M5)	A2=PS2(K+3),PS0(K+4);	
		A3=PS3(K+3),PS1(K+4)	
A7A 06E38 43514350 (03817)	MOV(A1),ADD(A2,A3)\MOV(A0),ADD(A2,A3)	A1=S0(K+3),S2(K+3);	
		A0=S1(K+3),S3(K+3)	
A7B 06E3A 84508471 (03818)	MOV(A0),MUL(M0,M6)\MOV(A1),MUL(M0,M7)	A0=PS0(K+4),PS2(K+4);	
		A1=PS1(K+4),PS3(K+4)	
A7C 06E3C 41134112 (03819)	MOV(A3),ADD(A0,A1)\MOV(A2),ADD(A0,A1)	A3=S2(K+3),S0(K+4);	
		A2=S3(K+3),S1(K+4)	
A7D 06E3E 089C0810 (03820)	MOV(R,OQ)\MOV(ZERO,A0)	OUT=S0(M);CLEAR S1	
A7E 06E40 0811089C (03821)	MOV(ZERO,A1)\MOV(R,OQ)	CLEAR S0;OUT=S1(M)	
A7F 06E42 08220883 (03822)	MOV(P,A2)\MOV(P,A3)	PS2;PS3	
A80 06E44 43404340 (03823)	ADD(A2,A3)	S2;S3	
A81 06E46 08EC08EC (03824)	MOV(IQA,M4)	V(M+4)	
A82 06E48 08ED08ED (03825)	MOV(IQA,M5)	V(M+5)	
A83 06E4A 089C0812 (03826)	MOV(R,OQ)\MOV(ZERO,A2)	OUT=S2(M);CLEAR S3	
A84 06E4C 0813089C (03827)	MOV(ZERO,A3)\MOV(R,OQ)	CLEAR S2;OUT=S3(M)	
A85 06E4E 08EE08EE (03828)	MOV(IQA,M6)	V(M+6)	
A86 06E50 10000805 (03829)	JUMP(DCRUP)		

PAGE 06: C08M-TENEXD3<MAP>8BN306.MSD.61, 3e-Dec-79 16:14:24, Ed: KFIELD  
 APU3-DCOR(Y,U,V) DIRECT CONVOLUTION

A87 06E52 00000000	(03037) *	
A88 06E54 20322032	(03038) *	
A89 06E56 00000000	(03039) DCREND:	NDP
A8A 06E58 10000000	(03040)	CLEAR(RA)
	(03041)	NDP
	(03042)	JUMP(0)
	(03043) *	
	(03044) *	
06E5A 00000000	(03045)	DCRUSSZ=8A-DCRUSSA
	(03046)	END
	(03047)	EVEN





PAGE 88: [BBN-TENEXD]<MAP>8BN300.MSO.61, 30-Dec-79 16:14:24, Ed: KFIELD  
APS3-DCOR(Y,U,V)

A18 06E98 365000E (03901)	LOAD(BW1,[0])	Y ST ADDR
A1C 06E9A 3860000 (03902)	LOAD(BW2,MSS)	Y SIZE-1
A1D 06E9C 3A120000 (03903)	SUB(BW1,MSS)	Y ST ADDR - SPACING
(03904) *		
(03905) *	OUTPUT LOOP	
A1E 06E9E 3C9A0006 (03906)	DCRS3: ADD(BW1,[0],1P)	Y(N)
A1F 06EA0 3E211E81 (03907)	SUBL(BW2,1),JUMPP(DCRS3)	NO
A20 06EA2 40200030 (03908)	CLEAR(RO)	
A21 06EA4 42000020 (03909)	NOP(0)	
00006E9E (03910)	DCRSSA=RC	
06EA6 (03911)	END	
06EA6 00000000 (03912)	DCRSI DATA 11P-0.0"	
...		
0000005A (03913)	DCRSSZ=HL-DCRSS	
(03914) *		
(03915) *		
(03916) *		



PAGE 90: [BBB-TENEXD]<MAP>BBN300.HSD.61, 30-Dec-79 16:14:24, Ed: KFIELD  
DEFINE TOP OF MODULE

(03969) \* DEFINE TOP OF MODULE  
(03970) \*  
(03971) \*  
00006EE2 TOESCOR = #L  
(03972) \*  
(03973) \*  
(03974) \*  
(03975)  
06EE2  
END

AAPCS:	0674C (00131) (02278) (02283) (02415)
AAPCS3:	00032 (02289) (02368)
AAPCSA:	067C8 (02291) (02418)
AAPCS1:	067F2 (02277) (02414)
AAPCSM:	0678E (02314) (02342) (02373)
AAPCS:	00004 (02278) (02296)
AAPCSZ:	00000 (02200) (02415)
AFDTSORC:	000E0 (00011) (00109) (00114) (00119) (00124) (00134) (00139) (00144) (00149) (00154)
APSASS:	00245 (00012)
APSNDR:	00EFA (00013)
APSNDR0:	00F63 (00014)
APSNDR1:	00F2D (00015)
APSNDR:	00240 (00016)
APSCSC:	00248 (00017) (02027)
APSDONE:	00F81 (00018)
APSDOMER:	00F8C (00019)
APSG0:	0000C (00020)
APSG1:	00000 (00021)
APSPBFF:	00010 (00022)
APSSAID:	0000A (00023)
APSSSCLR:	0000E (00024)
APSSSS:	00009 (00025)
APCIS5:	068CC (00141) (02610) (02615) (02717)
APCIS53:	00028 (02621) (02687)
APCIS5A:	06934 (02613) (02712)
APCIS5M:	068FE (02643) (02667) (02692)
APCIS5S:	00004 (02610) (02628)
APCIS5Z:	0007E (02612) (02717)
APCIUS:	06876 (00140) (02522)
APCIUSSA:	00000 (02519) (02529) (02588)
APCIUSSZ:	00027 (02520) (02588)
APCLP:	00027 (02196) (02250)
APCS51:	0693C (02609) (02716)
APSSBMO:	00004 (00026)
APSSLA:	1FFC0 (00027) (02032) (02377) (02696) (03133)
APSR:	1FFC0 (00028)
08TAB:	05D00 (00103)
BCISAD:	00604 (00030) (03955)
BCISAT:	00686 (00031) (03947)
BCISBA:	00582 (00032) (03941) (03950)
BITSG0:	00000 (00033)
BITSRC:	00000 (00034) (03947)
CK408S:	01A06 (00036) (03940)
CLP5G0G1:	00792 (00037)
C0SS:	03190 (00038)
CSF0\$M0S:	021FC (00039) (00112) (00117) (00122) (00132) (00137) (00142) (00147) (00152) (00157)
DCLU0:	0003D (03677) (03737)
DCLU1:	00048 (03694) (03732) (03753)
DCLU2:	0005A (03696) (03774)
DCLU3:	00069 (03708) (03795)
DCLU4:	00078 (03720) (03816)
DCREND:	00087 (03676) (03839)

PAGE 92: [BBM-TENEXDJ<MAP>8BN300.MSD.61, 30-Dec-79 16:14:24, Ed: KFIELD  
 DEFINE TOP OF MODULE

DCRS:	06E62 (00156)	(03866)	(03913)
DCRS\$A:	06E9E (03864)	(03910)	
DCRS\$I:	06EA6 (03868)	(03912)	
DCRS\$D:	0601A (03868)	(03900)	
DCRS\$S:	06E62 (03861)	(03867)	
DCRS\$Z:	0605A (03863)	(03913)	
DCRS1:	00009 (03879)	(03895)	
DCRS2:	00000 (03884)	(03886)	
DCRS3:	0001E (03906)	(03907)	
DCRS:	06D44 (00155)	(03648)	
DCRS\$A:	00000 (03645)	(03651)	(03845)
DCRS\$Z:	00008 (03646)	(03845)	
DCRU0:	0001A (03678)		
DCRU1:	00029 (03686)	(03733)	
DCRU2:	00027 (03698)		
DCRU3:	0002E (03710)		
DCRU4:	00035 (03722)		
DCRUP:	00005 (03657)		
DEALS\$:	06038 (00136)	(03751)	(03771) (03792) (03813) (03834)
DEALS\$3:	00009 (02472)	(02482)	
DEALS\$A:	06060 (02469)	(02490)	
DEALS\$I:	06060 (02464)	(02502)	
DEALS\$S:	0000A (02465)	(02484)	
DEALS\$Z:	0003C (02467)	(02503)	
DEALU\$:	067FE (00135)	(02425)	
DEALU\$A:	00000 (02422)	(02427)	(02452)
DEALU\$Z:	00019 (02423)	(02452)	
DKTAB1:	05D10 (00191)		
DKTAB2:	05D50 (00224)		
DKTAB3:	05D90 (00257)		
DKTAB4:	05D00 (00290)		
DKTAB5:	05DF0 (00307)		
DKTAB6:	05E10 (00324)		
DKTAB7:	05E30 (00341)		
DKTAB8:	05E40 (00350)		
DMV:	00794 (00041)	(01601)	
DOQ:	00070 (01454)	(01456)	(01458) (01462) (01464) (01466) (01469) (01471) (01477)
DTQAB:	05ED0 (00440)		
EDTAB:	05E50 (00359)		
ENR0TH:	06C2 (03381)	(03420)	
ENRQVI:	06CC2 (03305)	(03550)	
ENRQVL:	06C42 (03383)	(03493)	
ENSS:	06070 (00151)	(03332)	(03339) (03425)
ENSSA:	06080 (03335)	(03420)	
ENSS1:	0608E (03331)	(03424)	
ENSSD:	00020 (03360)	(03400)	
ENSSS:	00000 (03332)	(03351)	
ENSSZ:	00052 (03334)	(03425)	
ENUS:	06AFE (00150)	(03225)	
ENUS2:	00017 (03244)	(03264)	
ENUS3:	00019 (03249)	(03267)	
ENUS4:	00010 (03254)	(03270)	
ENUS5:	00020 (03291)	(03297)	

PAGE 93: [BBN-TEHEND]<MAP>BBN300.MS0.61, 30-Dec-79 16:14:24, Ed: KFIELD  
 DEFINE TOP OF MODULE

ENUS6:	0002F (03295)	(03298)
ENUS8:	00000 (03222)	(03235)
ENUSZ:	00035 (03223)	(03305) (03306)
ERR0R:	01AFA (00043)	
F11A:	03110 (00045)	
F0T:	007E8 (00046)	(00162) (00165)
F0D:	03040 (00047)	
FFTSBZ:	0079A (00048)	
FLCSCL:	00000 (00049)	
FLCSG0:	00004 (00050)	
FLCSG1:	00005 (00051)	
FLCSG2:	00006 (00052)	
FLCSG3:	00007 (00053)	
FLCSRI:	00011 (00054)	
FLCSSET:	00020 (00055)	
GATHER:	0183C (00057)	(03964)
HS:	00001 (00059)	(02068) (03956)
IFPS1:	0608A (02072)	(02076)
IN:	00000 (00548)	
INST:	0607A (02030)	(02036)
ISVTS:	00502 (00061)	(02071) (02076)
K123:	00058 (01653)	(01658)
K1LP:	0005A (01655)	(01657)
K456:	0005F (01661)	(01666)
F4LP:	00061 (01663)	(01665)
K70:	00066 (01670)	(01675)
K7LP:	00068 (01672)	(01674)
KQ31:	0004E (01524)	(01642)
KQSD:	0006E (01642)	(01680)
KQLP:	00074 (01686)	(01688)
KQIAB:	003EC (01650)	(01703)
KQAN:	00055 (01443)	(01450)
LCASE1:	0002C (01102)	(01130)
LCASE2:	00031 (01106)	(01136)
LCASE3:	0001A (01108)	
LDONE:	0004F (00616)	(00630)
LEFT:	00014 (01099)	
LOADSAP:	0187E (00063)	
LOADSAP1:	F1B0F (00064)	
LOOP:	0001D (00577)	(00627)
LP01:	00073 (01480)	(01486)
MSS:	00000 (00069)	(00609)
	(00756)	(00764)
	(01202)	(01205)
	(01534)	(01535)
	(01682)	(01683)
	(02355)	(02381)
	(02408)	(02493)
	(02700)	(02701)
	(03120)	(03121)
	(03872)	(03873)
MP1FFS:	0667E (00163)	(02067)
MPMBSS:	066BC (00166)	(03937)
	(00690)	(00706)
	(00765)	(00991)
	(01206)	(01225)
	(01581)	(01592)
	(02296)	(02297)
	(02382)	(02391)
	(02494)	(02629)
	(03023)	(03024)
	(03142)	(03143)
	(03875)	(03876)
	(00724)	(00723)
	(00749)	(00750)
	(01011)	(01017)
	(01230)	(01235)
	(01631)	(01647)
	(02299)	(02343)
	(02475)	(02476)
	(02485)	(02487)
	(02672)	(02673)
	(03081)	(03082)
	(03351)	(03352)
	(03353)	(03354)
	(03894)	(03895)
	(01198)	(01199)
	(01529)	(01530)
	(01647)	(01648)
	(02347)	(02348)
	(02484)	(02485)
	(02677)	(02678)
	(03089)	(03090)
	(03366)	(03367)

AD-A083 238

**BOLT BERANEK AND NEWMAN INC CAMBRIDGE MA**

F/6 17/2

DESIGN AND REAL-TIME IMPLEMENTATION OF A BASEBAND LPC CODER FOR--ETC(U)

FEB 80 R VISWANATHAN, J WOLF, L COSELL

DCA100-79-C-0003

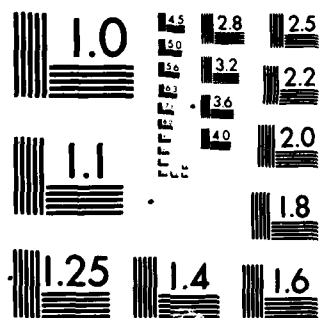
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**UNCLASSIFIED**

2-3

$$\Delta_{\lambda} = \Delta_{\lambda}(\mathcal{A})$$

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MICROCOPY RESOLUTION TEST CHART

NATIONAL BUREAU OF STANDARDS-1963-A



PAGE 94: [BDB-TEHEND]<MAP>BDB300-MS0-61, 30-Dec-79 16:14:24, Ed: KFIELD  
 DEFINE TOP OF MODULE

MSK\$BMB:	00006 (00066) (03942)	
MSK\$BYT:	00007 (00067) (03957)	
MSK\$RBYT:	00008 (00068) (02067)	(03937) (03961)
MSDOME:	00001 (01481) (01489)	(01496)
MSLAE:	00010 (01572) (01578)	
MSLFA:	003CA (01512) (01693)	
MSLFSAPS:	062EC (00126) (01509)	(01521) (01701)
MSLFSAPU:	06108 (00125) (01298)	
MSLFSI:	063DE (01508) (01699)	
MSLFS:	00002 (01509) (01523)	(01525)
MSLFS\$A:	00000 (01294) (01301)	(01501)
MSLFS\$Z:	00006 (01295) (01501)	
MSLFSZ:	00100 (01511) (01701)	
MSLFAE:	00039 (01402) (01448)	
MSLFAW:	00053 (01386) (01447)	
MSLFD:	00040 (01427) (01441)	
MSLFRE:	00017 (01337) (01446)	
MSLFRW:	00051 (01316) (01445)	(01445)
MSLFSE:	00010 (01318) (01327)	
MSLLOC:	00048 (01632) (01638)	
MSLLOD:	0004C (01620) (01640)	
MSLOE:	00032 (01603) (01608)	
MSLOT:	0002A (01578) (01598)	
MSLO\$SM:	0666C (00127) (02025)	
MSLSC:	00015 (01546) (01554)	(01556)
MSLSE:	00012 (01547) (01551)	
MSLT:	00020 (01580)	
MS\$S\$1:	06671 (02025) (02030)	
MSWDOME:	0007C (01498) (01494)	
DQE:	00010 (00071) (02151)	(02166) (02547) (02841) (02856)
P2120\$:	0510C (00121) (01177)	(01184) (01251)
P2120\$2:	0000F (01194) (01223)	
P2120\$A:	061C0 (01180) (01244)	
P2120\$1:	061C8 (01176) (01250)	
P2120\$S:	00002 (01177) (01197)	
P2120\$Z:	0004A (01179) (01251)	
PROOME:	00043 (01129) (01149)	(01158)
PRTRB\$:	060F8 (00120) (01066)	
PRTRB\$A:	00000 (01063) (01069)	(01162)
PRTRB\$Z:	00046 (01064) (01162)	(01163)
PT\$:	06A30 (00146) (02998)	(03005) (03158)
PT\$S1:	06A78 (03047) (03008)	(03127)
PT\$SA:	06AA2 (03001) (03153)	
PT\$SI:	06AAC (02997) (03157)	
PT\$SO:	0002A (03016) (03115)	
PT\$SS:	00028 (02998) (03119)	
PT\$SZ:	00004 (03000) (03158)	
PTU\$:	0694C (00145) (02763)	
PTU\$0:	00007 (02778) (02786)	
PTU\$1:	00010 (02776) (02709)	
PTU\$10:	00061 (02919) (02922)	(02923)
PTU\$11:	00063 (02918) (02925)	
PTU\$12:	00068 (02938) (02944)	

PAGE 95: [BBM-TEMEXD]<MAP>8BN300.MSD.61, 30-Dec-79 16:14:24, Ed: KFIELD  
 DEFINE TOP OF MODULE

PTUS2:	00015 (02782) (02796)	
PTUS3:	0003E (02784) (02798) (02870)	
PTUS4:	00041 (02780) (02791) (02874)	
PTUS5:	00019 (02793) (02802)	
PTUS6:	0001F (02804) (02808)	
PTUS7:	00021 (02807) (02813)	
PTUS8:	00044 (02866) (02804)	
PTUS9:	0005A (02913) (02920)	
PTUS10:	00008 (02760) (02770) (02955)	
PTUS11:	00072 (02761) (02955) (02956)	
PTUS12:	0002F (02040) (02041)	
PTUS13:	00039 (02055) (02056)	
PTUS14:	00042 (02215) (02220) (02226) (02229)	
RCASE1:	00036 (01117) (01142)	
RCASE2:	0003E (01120) (01151)	
RCASE3:	00024 (01122)	
RIGHT:	0001E (01115) (01134) (01140)	
S1011:	035EA (00073)	
SHFTLSRS:	007AE (00074)	
SIMS:	03192 (00076)	
SINCOSST:	023FA (00075)	
SVTS:	00302 (00077) (01645) (03960)	
SVTSUN1:	0030E (00078) (02294) (02626) (03034) (03058) (03379)	
SYSEFLGS:	1PCE (00079) (02033)	
TAPQTH:	06ABC (03060) (03160)	
TAPQVL:	06ADC (03064) (03177)	
TEM50:	007B4 (00081)	
TOES:	021FE (00082) (03972)	
TOESCUR:	06EE2 (00080) (00096)	
TOESPTR:	00208 (00083) (00974) (01036)	
V1100K5:	060C6 (00116) (00974) (01000)	
V1100K52:	0000A (00079) (01000)	
V1100K5A:	060E6 (00070) (01026)	
V1100K5I:	060EE (00066) (01033)	
V1100K5Z:	00030 (00069) (01036)	
V32005:	05F86 (00111) (00671) (00780)	
V320056:	00010 (00677) (00744)	
V32005A:	06F12 (00667) (00782)	
V32005I:	06F1A (00663) (00787)	
V32005Z:	00000 (00666) (00780)	
VALBUFS:	01C5A (00085)	
VAPCS:	06692 (00130) (02126)	
VAPCSA:	00000 (02124) (02133) (02257)	
VAPCS52:	00059 (02125) (02257)	
VAPCSW1:	00006 (02150) (02151)	
VAPCSW2:	00010 (02165) (02166)	
VAPCI\$W1:	00006 (02546) (02547)	
VAPCI\$W2:	00010 (02562) (02563)	
VKT0A5:	06030 (00115) (00841)	
VKT0A5SA:	00000 (00038) (00843) (00940)	
VKT0A5S2:	00043 (00039) (00940) (00941)	
VLTSY5:	05EF2 (00110) (00545)	
VLTSY5SA:	00000 (00542) (00547) (00646)	

PAGE 96: [BBM-TEMEKD]<MAP>BBH300.MSO.61, 30-Dec-79 16:14:24, Ed: KFIELD  
 DEFINE TOP OF MODULE

VLTSYSSZ: 0005E (00543) (00646) (00647)  
 VSHA2S: 02070 (00006)  
 WS: 00002 (00000) (00109) (00114) (00119) (00124) (00129) (00134) (00139) (00144) (00149)  
 (00154) (00162) (00165) (02069)  
 X23RD: 00032 (00854) (00855) (00913)  
 X4TH: 00020 (00856) (00906)  
 X5TH: 0002E (00857) (00905) (00907)  
 X6TH: 00027 (00859) (00899)  
 X7TH: 00029 (00860) (00890) (00901)  
 X8TH: 00023 (00862) (00894)  
 XFL\$01: 0152C (00090) (02077)  
 ZERO: 0078A (00092)

LINES WITH ERRORS: 0 (MAP VERSION 00101.10) E- 0

PAGE 1: [BBN-TENEXD]<MAP>BBN10S.MSO.96, 31-Dec-79 13:53:20, Ed: WOLF

(00001) [BBN-TENEXD]<MAP>BBN10S.MSO.96, 31-Dec-79 13:53:20, Ed: WOLF

# T A B L E O F C O N T E N T S

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ADAM SINGLE-CHANNEL DOUBLE-BUFFERED D/A OUT	PAGE	16
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ADAMINT: ADAM INTERRUPT SERVICE ROUTINE	PAGE	19
TMODEMINT: TMODEM INTERRUPT SERVICE ROUTINE	PAGE	21
RMODEMINT: RMODEM INTERRUPT SERVICE ROUTINE	PAGE	23
FRAME SYNCHRONIZATION ROUTINES	PAGE	26
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PROTECT(AB) -- PROTECT AND BITSTREAM THE CODED TRANSMITTER FRAME.	PAGE	31
CORRECT(AB): UNBITSTREAM, ERROR-CORRECT, AND DECODE	PAGE	37
MPGSC -- G-FLAG SET/CLEAR	PAGE	45

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(00003) *SYMBOL DEFINITIONS
(00004) ? THE IOS CODE FITS IN THE HOLE AT $4000 - $49FF. THE CSPU CODE
(00005) ; GOES IN THE HOLE AT $3000 - $3FFF.
(00006) ;
(00007) H$ = 1 ;HALF-WORD ADDRESS INCREMENT
(00008) W$ = 2 ;FULL-WORD ADDRESS INCREMENT
(00009) ;
(00010) ACQTHR = 10 ;SYNC-SEARCH ACQUISITION THRESHOLD: NUMBER OF
(00011) ; FRAMES OF CONSECUTIVE GOOD SYNC BITS NEEDED
(00012) ;
(00013) BITS15 = 0'77777' ; TO ACQUIRE SYNC.
(00014) ;
(00015) ;BIT-MASKS SHIFTED 1 PLACE LEFT FOR USE IN THE CORRECT ROUTINE
(00016) C0 = 2
(00017) C1 = 4
(00018) C2 = 8
(00019) C3 = 16
(00020) C210 = C2+C1+C0
(00021) C32 = C3+C2
(00022) C321 = C3+C2+C1
(00023) ;
(00024) ;DECODING TABLE ADDRESSES: THESE ARE AT THE BEGINNING OF 88N300.M50
(00025) B0TAB = $5000 ;TABLE FOR BASEBAND RESIDUAL SAMPLES
(00026) D0TAB = $5000 ;TABLES FOR KI-K8
(00027) D0TAB1 = D0TAB + 0*W$
(00028) D0TAB2 = D0TAB1 + 32*W$
(00029) D0TAB3 = D0TAB2 + 32*W$
(00030) D0TAB4 = D0TAB3 + 32*W$
(00031) D0TAB5 = D0TAB4 + 16*W$
(00032) D0TAB6 = D0TAB5 + 16*W$
(00033) D0TAB7 = D0TAB6 + 16*W$
(00034) D0TAB8 = D0TAB7 + 8*W$
(00035) D0TAB9 = D0TAB8 + 8*W$
(00036) D0TAB10 = D0TAB9 + 8*W$
(00037) D0TAB11 = D0TAB10 + 8*W$
(00038) D0TAB12 = D0TAB11 + 8*W$
(00039) D0TAB13 = D0TAB12 + 8*W$
(00040) D0TAB14 = D0TAB13 + 8*W$
(00041) D0TAB15 = D0TAB14 + 8*W$
(00042) ;
(00043) F0T$ = $7E8 ;START OF MONARRAY FUNCTION DISPATCH TABLE
(00044) ;GFLAG SET/CLEAR CONSTANTS
(00045) SET = 0'40'
(00046) CLR = 0
(00047) C0 = 4
(00048) C1 = 5
(00049) C2 = 6
(00050) C3 = 7
(00051) ;
(00052) ;HISTOGRAM TABLES (304. HALFWORDS ON BUS 1 ABOVE RDAB8)
(00053) PHIST = 48206 ;PITCH
(00054) THIST = PHIST + 64*H$ ;TAP
(00055) CHIST = THIST + 16*H$ ;GAIN

```

PAGE 4: CBBN-TENEXD\<MAP>8BMIDS.MSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
SYMBOL DEFINITIONS

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00000D2E (00056) K1HIST = GHIST + 64*HS
00000D4E (00057) K2HIST = K1HIST + 32*HS
00000D6E (00058) K3HIST = K2HIST + 32*HS
00000D8E (00059) K4HIST = K3HIST + 32*HS
00000DAE (00060) K5HIST = K4HIST + 16*HS
00000DCE (00061) K6HIST = K5HIST + 16*HS
00000DEE (00062) K7HIST = K6HIST + 16*HS
00000DFE (00063) K8HIST = K7HIST + 8*HS
00000E0E (00064) ;
00000E1E (00065) ;MODEM-SCROLL INPUT DATA WORD BITS
00000E2E (00066) ;LOCAL HANDSET HOOKSWITCH STATE
00000E3E (00067) ;SIGNAL RATE INDICATOR
00000E4E (00068) ;INCOMING CALL
00000E5E (00069) ;CLEAR TO SEND
00000E6E (00070) ;DATA MODE
00000E7E (00071) ;RECEIVER READY
00000E8E (00072) ;SIGNAL QUALITY
00000E9E (00073) ;RECEIVE DATA
00000EAE (00074) ;
00000EBE (00075) ;START OF INTEGER SCALAR TABLE IN SNAP EXEC
00000ECE (00076) ;SYNC-SEARCH LOSE-SYNC THRESHOLD: NUMBER OF
00000EDE (00077) ; SYNC ERRORS WITHOUT 2 CONSECUTIVE GOOD-SYNC
00000EEA (00078) ; FRAMES NEEDED TO LOSE SYNC.
00000EEB (00079) ;
00000EEC (00080) ;MODEM-SCROLL OUTPUT DATA WORD BITS
00000EED (00081) ;TERMINAL READY
00000EEF (00082) ;REQUEST TO SEND
00000EF0 (00083) ;SIGNALING RATE
00000EF1 (00084) ;SEND DATA
00000EF2 (00085) ;
00000EF3 (00086) ;PITCH DECODER LOWER LIMIT (LEFT SHIFTED 1)
00000EF4 (00087) ;PITCH DECODER UPPER LIMIT (LIKEWISE)
00000EF5 (00088) ;LOWER AND UPPER LIMITS FOR ERRPTR
00000EF6 (00089) ; IN RMISIM
00000EF7 (00090) ;MAP SYSTEM FLAGS REGISTER IN PSEUDO-MEMORY
00000EF8 (00091) ;CONTROL BITS USED FOR TRANSMIT MODEM
00000EF9 (00092) ;
00000EFA (00093) ;SYNCSOP IS A "REGISTER" IN THE ADAM AND ADM USED FOR CSPO-SCROLL
00000EFB (00094) ; COMMUNICATION.
00000EFC (00095) ; OPADD SYNCSOP,(16 .LS. 10) + (26 .LS. 5)+4
00000EFD (00096) ;
00000EFE (00097) ;THE SAF (SET ADDRESS FIELD) INSTRUCTION IS ONE OF THE NEW ONES ADDED
00000EFF (00098) ; TO THE CSPO BY THE "REV. 19" MICROCODE REVISION.
00000F00 (00099) ; OPADD SAF,(1 .LS. 14) + (29 .LS. 8) + $EF
00000F01 (00100) ;

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PAGE 5: [BBN-TELEXD]<MAP>BBN105.MSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
 PATCHES TO NON-ARRAY FUNCTION DISPATCH TABLE

```

    (00101) *PATCHES TO NON-ARRAY FUNCTION DISPATCH TABLE
    0000080A (00102) $L = FDIS + (W$ + 121)
    0000A 00003CC4 (00103) ADDR PROTECT JFCB 121 (SNAP FUNCTION PROTECT)
    0000C 00003E08 (00104) ADDR CORRECT JFCB 122 (CORRECT)
    0000E 00003F8A (00105) ADDR WP$C JFCB 123 (WP$C)
    00010 0000495C (00106) ADDR ADAMINT JFCB 124 (IADINT)
    00012 00003B08 (00107) ADDR RMODEMINT JFCB 125 (IYHINT)
    00014 00003B5A (00108) ADDR RMODEMINT JFCB 126 (IRHINT)
    00016 00003C00 (00109) ADDR ADAMINT JFCB 127 (IDAMINT)
    (00110) ;
    (00111) ;
  
```

PAGE 6: [BBN-TELEXD]<MAP>BBN105.MSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
 PATCHES TO INTERRUPT SERVICE ROUTINES

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    (00112) *PATCHES TO INTERRUPT SERVICE ROUTINES
    (00113) ; THESE PATCHES MAKE SCROLL INTERRUPTS GO TO OUR OWN ROUTINES.
    (00114) ; NOTE THAT LOADING THIS FILE WILL MAKE THE NORMAL ADAM/ADM/IOS-2 SNAP
    (00115) ; FUNCTIONS INOPERATIVE!
    00004022 (00116) $L = D16$INT1
    04022 06003B08 (00117) $1: CALL R0, TMODEMINT
    04024 0F70 (00118) RET
    04025 0000 (00119) EVEN
    04026 00004022 (00120) JMP #1
    (00121) ;
    00004030 (00122) $L = D16$INT2
    04030 06003B5A (00123) $1: CALL R0, RMODEMINT
    04032 0F70 (00124) RET
    04033 0000 (00125) EVEN
    04034 00004030 (00126) JMP #1
    (00127) ;
    000040FA (00128) $L = D22$INT1
    040FA 06003C00 (00129) $1: CALL R0, ADAMINT
    040FC 0F70 (00130) RET
    040FD 0000 (00131) EVEN
    040FE 000040FA (00132) JMP #1
    (00133) ;
    0000411E (00134) $L = D23$INT1
    0411E 0600495C (00135) $1: CALL R0, ADAMINT
    04120 0F70 (00136) RET
    04121 0000 (00137) EVEN
    04122 0000411E (00138) JMP #1
    (00139) ;
  
```

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(00140) ; I/O BUFFER DEFINITIONS
(00141) ; EXCEPT AS NOTED, ALL BUFFERS ARE ON BUS 1, ARE FIXED-POINT, 16 BITS,
(00142) ; AND HAVE SPACING OF 1 HALF-WORD. THE SOURCE/SINK BUFFERS HAVE
(00143) ; ASSIGNED BID NUMBERS, BUT THE OTHERS DON'T. THEREFORE, IN THESE
(00144) ; I/O ROUTINES, WE USE THE ABSOLUTE ADDRESSES GIVEN BELOW (WHICH ARE
(00145) ; TAKEN FROM THE VOCODER INITIALIZATION TYPEOUT).
(00146) ; *** THEREFORE, IF THE BUFFER CONFIGURATIONS SHOULD CHANGE, THESE
(00147) ; SYMBOLS WILL NEED TO BE MODIFIED. ***
(00148) ;
(00149) ; SBLNGTH = 180
(00150) ; SL6=SBLNGTH/6
(00151) ; SBLNGTH = 261
(00152) ; SL6 = 258/6
(00153) ;
(00154) ; NEW BUFFERS:
(00155) ; T8TA = 42842
(00156) ; T8TB = 43104
(00157) ; T8TC = 43366
(00158) ; R8TA = 43628
(00159) ; R8TB = 43890
(00160) ;
(00161) ; TAD8A = 44152
(00162) ; TAD8B = 44332
(00163) ; TAD8C = 44512
(00164) ; TSRA = 44692
(00165) ; TSRA = 44872
(00166) ; TSNA = 45052
(00167) ; TSNB = 45124
(00168) ; TSNK = 45196
(00169) ; TMDA = 45268
(00170) ; TMDB = 45530
(00171) ;
(00172) ; RMDA = 45792
(00173) ; RMDB = 46054
(00174) ; RMDC = 46316
(00175) ; RSSP = 46578
(00176) ; RSSS = 46840
(00177) ; RSRA = 47102
(00178) ; RSRB = 47244
(00179) ; RSKA = 47386
(00180) ; RSKB = 47566
(00181) ; RSKC = 47746
(00182) ; RDAB = 47926
(00183) ; RDAB = 48106
(00184) ;
(00185) ; INITIALIZE ALTERNATING SYNC BITS IN TMODEM BUFFERS
(00186) ; FL = TMDA
(00187) ; DATA 0 + TMBITS
(00188) ; FL = TMDB
(00189) ; DATA 1 + TMBITS
(00190) ;
(00191) ; INITIALIZE FINAL (UNUSED) BITS IN TMODEM BUFFERS. IF THEY WERE LEFT
(00192) ; ALONE AND HAPPENED TO ALTERNATE, WE COULDN'T GET SYNC.

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PAGE 8: [88N-TENERD]<MAP>BNIDS.MSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
I/O BUFFER DEFINITIONS

88B1D8 88EC (88193) #L = TMDBA + MBLNCTH - 1  
88B1D8 88EC (88194) DATA 8 + TMDBITS  
88B2DE (88195) #L = TMDBA + MBLNCTH - 1  
88B2DE 88EC (88196) DATA 8 + TMDBITS  
(88197)

```

PAGE 9: [BBN-TENEIDJ<MAP>BNTOS.MSO.96, 31-Dec-79 13:53:28, Ed: WOLF
INTEGER SCALAR TABLE DEFINITIONS FOR I/O ROUTINES

(00198) * INTEGER SCALAR TABLE DEFINITIONS FOR I/O ROUTINES
(00199) ;
(00200) ; T/R SOURCE/SINK BUFFER FLAGS. ALL ARE INITED TO 0 AND USE THE
(00201) ; CONVENTION THAT 0 = NON-FULL AND NZ = NON-EMPTY.
(00202) ;
(00203) ; TSRA = ISVT$+50 ;TSOURCE FLAGS A & B
(00204) ; TSRA = ISVT$+51 ;TSOURCE FLAGS A & B
(00205) ; TRFA = ISVT$+52 ;TBITS FLAGS A & B
(00206) ; TRFB = ISVT$+53 ;TBITS FLAGS A & B
(00207) ; RTFA = ISVT$+57 ;RBITS FLAGS A & B
(00208) ; RTFB = ISVT$+58 ;RBITS FLAGS A & B
(00209) ; RSFA = ISVT$+59 ;RSINK FLAGS A & B
(00210) ; RSFB = ISVT$+60 ;RSINK FLAGS A & B
(00211) ; RL=TSRA
(00212) ; DATA 4D'0'
00534 0000
...
00538 0000
...

(00215) ;
(00216) ; I/O ROUTINE BUFFER/FLAG POINTER OFFSETS (AND INIT VALUES)
(00217) ; THESE ARE USED BY THE 4 INTERRUPT ROUTINES TO KEEP TRACK OF
(00218) ; THE PROPER BUFFERS AND FLAGS.
(00219) ;
(00220) ; USED IN ADAMINT:
0000542 (00221) ADPO = ISVT$+64 ;A/D OFFSET (0)
0000543 (00222) TSRO = ISVT$+65 ;TSOURCE OFFSET (-2)
(00223) ; USED IN TMODEHINT:
0000544 (00224) TRPO = ISVT$+66 ;TBITS OFFSET (0)
0000545 (00225) TRPO = ISVT$+67 ;TMODEM OFFSET (0)
(00226) ; USED IN RMODEHINT:
0000546 (00227) RMPO = ISVT$+68 ;RMODEM OFFSET (0)
0000547 (00228) RBTPD = ISVT$+69 ;RBITS OFFSET (-2)
(00229) ; USED IN ADMINT:
0000548 (00230) RSNPD = ISVT$+70 ;RSINK OFFSET (0)
0000549 (00231) DAPO = ISVT$+71 ;D/A OFFSET (0)
0000542 (00232) PL=ADPO
(00233) ; DATA 8,-2,0,0,0,-2,0,0
00542 0000
00543 FFFE
00544 0000
00545 0000
00546 0000
00547 FFFE
00548 0000
00549 0000

(00234) ;
(00235) ; I/O BUFFER ERROR COUNTERS. ALL ARE INITED TO 0.
(00236) ;
000054A (00237) TADFC = ISVT$+72 ;A/D FRAME DISCARD COUNTER
000054B (00238) TMFFC = ISVT$+73 ;TMODEM FRAME DISCARD COUNTER
000054C (00239) RMFFC = ISVT$+74 ;RMODEM FRAME DISCARD COUNTER
000054D (00240) RSNMR = ISVT$+75 ;RSINK DATA NOT READY COUNTER
(00241) ;

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PAGE 10: [BBN-TELEXD]<MAP>88MIDS-MSO-96, 31-Dec-79 13:53:28, Ed: WOLF  
 INTEGER SCALAR TABLE DEFINITIONS FOR I/O ROUTINES

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    (00242) ; MISC. INTEGER MEMORIES (AND INIT VALUES)
    (00243) ;
00000540 (00244) RUN = ISVTS+62          ;VOCODER RUN FLAG
00000540 (00245) ; (63 FREE FOR MPITM USE)
00000540 (00246) #L=RUN
00000540 (00247) DATA 1
    (00248) ;
00000540 (00249) IFRCR = ISVTS+76
00000540 (00250) RFRCTR = ISVTS+77
00000550 (00251) RLSCR = ISVTS+78
00000551 (00252) RORHK = ISVTS+79
00000552 (00253) RSYNC = ISVTS+80
00000553 (00254) RBOFO = ISVTS+81
    (00255) ; (82 FREE FOR MPITM)
0000054A (00256) #L=IADFOC
00000540 (00257) DATA 90*8
    ...
    (00258) ;
    (00259) ; DEBUGGING/DEMONSTRATION AIDS
    (00260) ;
0000057D (00261) RNCOR = ISVTS+123
0000057F (00262) ; (124 FREE FOR MPITM)
0000057F (00263) RERSIM = ISVTS+125
    (00264) ; (126 FREE FOR MPITM)
00000581 (00265) VSTATE = ISVTS+127
0000057D (00266) #L=RNCOR
0000057D (00267) DATA 50*8
    ...
    (00268) ;
    ;SET TO NZ TO TELL CORRECT NOT TO
    ; CORRECT CHANNEL ERRORS (0)
    ;SET TO N>0 TO TELL RMODEXINT TO
    ; SIMULATE N CHANNEL ERRORS PER FRAME(0)
    ;VOCODER STATE, IS DISPLAYED BY DAC0 (0)

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PAGE 11: [BBN-TENEXD] <MAP> BBNIOS.MSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
MODEM INTERFACE AND SYSTEM CLOCKS PROGRAMS,

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(00269) *MODEM INTERFACE AND SYSTEM CLOCKS PROGRAMS
(00270) ; JJW, 18-SEP-79
(00271) ;
(00272) ; THERE ARE TWO ENTRY POINTS:
(00273) ; CLKSET: CLOCK RATE SETTING (START ADDRESS = 0)
(00274) ; THIS ENTRY SETS THE DATA AND SAMPLE CLOCKS AND STARTS THEM.
(00275) ; THE 16-BIT CLOCK RATE VALUE IS A LITERAL IN THE INSTRUCTION AT
(00276) ; CLKG0. (TRIED BINDING AN INTEGER TABLE VALUE HERE, BUT BECAUSE
(00277) ; WE USE NO BIDS, LOSS DOES NO BINDING AT ALL!)
(00278) ;
(00279) ; RUNMOD: SET CLOCKS AND RUN MODEM TRANSMIT/RECEIVE (START ADDRESS 1)
(00280) ; THIS ENTRY SETS AND STARTS THE CLOCKS AND THEN RUNS THE MODEM
(00281) ; INTERFACE PROGRAM.
(00282) ;
(00283) ; THESE ENDING ADDRESSES ARE TRUNCATED TO 15 BITS BECAUSE THE LLIT
(00284) ; FIELD OF THE JNE INSTRUCTION IS ONLY THAT BIG.
(00285) ; RMASEND = (RMDMA+MBLNGTH-1) .AND. BITS15
(00286) ; RMCSEND = (RMDMB+MBLNGTH-1) .AND. BITS15
(00287) ; RMCSEND = (RMDMC+MBLNGTH-1) .AND. BITS15
(00288) ; TMASEND = (TMDMA+MBLNGTH-1) .AND. BITS15
(00289) ; TMCSEND = (TMDMB+MBLNGTH-1) .AND. BITS15
(00290) ;
(00291) ; CONSTANT FOR SETTING CLOCK RATES TO MODEM=9600 BPS, SIGNAL
(00292) ; SAMPLE RATE=6621 SAMPLES/SEC.
(00293) ; CLKRATES = SECC6
(00294) ;
(00295) ; REGISTER AND FLAG USAGE:
(00296) ; R0 - RCVR BUFFER SWITCH, SET TO MOD$RA, MOD$RB, OR MOD$RC
(00297) ; R1 - RCVR ADDRESS POINTER
(00298) ; R2 - XMR ADDRESS POINTER
(00299) ; P1 SET SIGNIFIES RCVR DATUM READY
(00300) ; P2 SET SIGNIFIES XMR READY FOR NEXT DATUM
(00301) ; F1 IS USED AS XMR BUFFER SWITCH: CLEAR=>TMODEMA, SET=>TMODEMB
(00302) ; F2 IS USED TO REMEMBER WHICH START ADDRESS WAS USED.
(00303) ; INT1 IS USED TO SIGNAL END OF TRANSMITTER BUFFER
(00304) ; INT2 IS USED TO SIGNAL END OF RECEIVER BUFFER
(00305) ;
(00306) ; L = $4000
(00307) ; A = 0
(00308) ;
(00309) ;
(00310) ;
(00311) ; MOD$BGN: BEGIN IOSZ(RTMODEM)
(00312) ;
(00313) ; CLKSET: SF2, JUMP(CLKGO)
(00314) ; RUNMOD: CF2
(00315) ; CLKG0: LOAD(R0, CLKRATES)
(00316) ; ADDL(R0, R, TP), JIFC(MOD$INIT, F2)
(00317) ; STOP, CF2
(00318) ;
(00319) ; MOD$INIT: LOAD(R0, MOD$RA)
(00320) ; LOAD(R1, RMDMA-1(1), L)
(00321) ;
(00322) ;
(00323) ;
(00324) ;
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A09 04814 0A020003 (00322)      LOAD(R2,TMDMA-1(1),L)
A0A 04816 0C340C00 (00323)      MODSLOOP: JIFS(MOD$RCVR,P1)
A0B 04818 00000000
A0C 0481C 0C341C00 (00324)      JIFS(MOD$XNTR,P2)
A0D 04820 0A300000 (00325)      JUMP(MOD$LOOP)
A0E 04822 00326) ;
A0F 04824 00327) ; MODEM RECEIVER
A10 04826 00328) ;
A11 04828 11300600 (00329)      MOD$RCVR: NW
A12 04830 122C001C (00330)      JEQ(MOD$RB,R0,MOD$RB)
A13 04832 142C0022 (00331)      JEQ(MOD$RC,R0,MOD$RC)
A14 04834 165A0001 (00332)      MOD$RA: ADD(R1,1,TM)
A15 04836 186833E4 (00333)      JNE(MOD$LOOP,R1,RH$SEND)
A16 04838 00000000
A17 04840 1A00001C (00334)      LOAD(R0,MOD$RB)
A18 04842 1B283E5 (00335)      LOAD(R1,RMDMB-1(1),L)
A19 04844 0A304000 (00336)      INT2, JUMP(MOD$LOOP)
A20 04846 1D5A0001 (00337) ;
A21 04848 1E6834EA (00338)      MOD$RB: ADD(R1,1,TM)
A22 04850 20000022 (00339)      JNE(MOD$LOOP,R1,RH$SEND)
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A25 04856 24282D0F (00342)      INT2, JUMP(MOD$LOOP)
A26 04858 255A0001 (00343) ;
A27 04860 26835F0 (00344)      MOD$RC: ADD(R1,1,TM)
A28 04862 28000015 (00345)      JNE(MOD$LOOP,R1,RMC$SEND)
A29 04864 29A00001 (00346)      LOAD(R0,MOD$RA)
A30 04866 2A03108 (00347)      LOAD(R1,RMDMA-1(1),L)
A31 04868 2B02100 (00348)      INT2, JUMP(MOD$LOOP)
A32 04870 2C9A0001 (00349) ;
A33 04872 2E1A03108 (00350) ; MODEM TRANSMITTER
A34 04874 2F02100 (00351) ;
A35 04876 3002100 (00352)      MOD$XNTR: NR, JIFS(MOD$TB,F1)
A36 04878 3102100 (00353) ;
A37 04880 329A0001 (00354)      MOD$TA: ADD(R2,1,TM)
A38 04882 34A832DE (00355)      JNE(MOD$LOOP,R2,TH$SEND)
A39 04884 36820003 (00356)      LOAD(R2,TMDMB-1(1),L)
A40 04886 3802100 (00357)      SF1, INT1, JUMP(MOD$LOOP)
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A42 04890 3A032DE (00359)      MOD$TB: ADD(R2,1,TM)
A43 04892 3B02100 (00360)      JNE(MOD$LOOP,R2,TH$SEND)
A44 04894 3C02100 (00361)      LOAD(R2,TMDMA-1(1),L)
A45 04896 3D02100 (00362)      CF1, INT1, JUMP(MOD$LOOP)
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A47 04900 3F02100 (00364) ;
A48 04902 4002100 (00365) ;
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A54 05314 0E02100 (00571) ;
A55 05316 0F02100 (00572) ;
A56 05318 1002100 (00573) ;
A57 05320 1102100 (00574) ;
A58 05322 1202100 (00575) ;
A59 05324 1302100 (00576) ;
A60 05326 1402100 (00577) ;
A61 05328 1502100 (00578) ;
A62 05330 1602100 (00579) ;
A63 05332 1702100 (00580) ;
A64 05334 1802100 (00581) ;
A65 05336 1902100 (00582) ;
A66 05338 1A02100 (00583) ;
A67 05340 1B02100 (00584) ;
A68 05342 1C02100 (00585) ;
A69 05344 1D02100 (00586) ;
A70 05346 1E02100 (00587) ;
A71 05348 1F02100 (00588) ;
A72 05350 2002100 (00589) ;
A73 05352 2102100 (00590) ;
A74 05354 2202100 (00591) ;
A75 05356 2302100 (00592) ;
A76 05358 2402100 (00593) ;
A77 05360 2502100 (00594) ;
A78 05362 2602100 (00595) ;
A79 05364 2702100 (00596) ;
A80 05366 2802100 (00597) ;
A81 05368 2902100 (00598) ;
A82 05370 2A02100 (00599) ;
A83 05372 2B02100 (00600) ;
A84 05374 2C02100 (00601) ;
A85 05376 2D02100 (00602) ;
A86 05378 2E02100 (00603) ;
A87 05380 2F02100 (00604) ;
A88 05382 3002100 (00605) ;
A89 05384 3102100 (00606) ;
A90 05386 3202100 (00607) ;
A91 05388 3302100 (00608) ;
A92 05390 3402100 (00609) ;
A93 05392 3502100 (00610) ;
A94 05394 3602100 (00611) ;
A95 05396 3702100 (00612) ;
A96 05398 3802100 (00613) ;
A97 05400 3902100 (00614) ;
A98 05402 3A02100 (00615) ;
A99 05404 3B02100 (00616) ;
A00 05406 3C02100 (00617) ;
A01 05408 3D02100 (00618) ;
A02 05410 3E02100 (00619) ;
A03 05412 3F02100 (00620) ;
A04 05414 4002100 (00621) ;
A05 05416 4102100 (00622) ;
A06 05418 4202100 (00623) ;
A07 05420 4302100 (00624) ;
A08 05422 4402100 (00625) ;
A09 05424 4502100 (00626) ;
A10 05426 4602100 (00627) ;
A11 05428 4702100 (00628) ;
A12 05430 4802100 (00629) ;
A13 05432 4902100 (00630) ;
A14 05434 4A02100 (00631) ;
A15 05436 4B02100 (00632) ;
A16 05438 4C02100 (00633) ;
A17 05440 4D02100 (00634) ;
A18 05442 4E02100 (00635) ;
A19 05444 4F02100 (00636) ;
A20 05446 5002100 (00637) ;
A21 05448 5102100 (00638) ;
A22 05450 5202100 (00639) ;
A23 05452 5302100 (00640) ;
A24 05454 5402100 (00641) ;
A25 05456 5502100 (00642) ;
A26 05458 5602100 (00643) ;
A27 05460 5702100 (00644) ;
A28 05462 5802100 (00645) ;
A29 05464 5902100 (00646) ;
A30 05466 5A02100 (00647) ;
A31 05468 5B02100 (00648) ;
A32 05470 5C02100 (00649) ;
A33 05472 5D02100 (00650) ;
A34 05474 5E02100 (00651) ;
A35 05476 5F02100 (00652) ;
A36 05478 6002100 (00653) ;
A37 05480 6102100 (00654) ;
A38 05482 6202100 (00655) ;
A39 05484 6302100 (00656) ;
A40 05486 6402100 (00657) ;
A41 05488 6502100 (00658) ;
A42 05490 6602100 (00659) ;
A43 05492 6702100 (00660) ;
A44 05494 6802100 (00661) ;
A45 05496 6902100 (00662) ;
A46 05498 6A02100 (00663) ;
A47 05500 6B02100 (00664) ;
A48 05502 6C02100 (00665) ;
A49 05504 6D02100 (00666) ;
A50 05506 6E02100 (00667) ;
A51 05508 6F02100 (00668) ;
A52 05510 7002100 (00669) ;
A53 05512 7102100 (00670) ;
A54 05514 7202100 (00671) ;
A55 05516 7302100 (00672) ;
A56 05518 7402100 (00673) ;
A57 05520 7502100 (00674) ;
A58 05522 7602100 (00675) ;
A59 05524 7702100 (00676) ;
A60 05526 7802100 (00677) ;
A61 05528 7902100 (00678) ;
A62 05530 7A02100 (00679) ;
A63 05532 7B02100 (00680) ;
A64 05534 7C02100 (00681) ;
A65 05536 7D02100 (00682) ;
A66 05538 7E02100 (00683) ;
A67 05540 7F02100 (00684) ;
A68 05542 8002100 (00685) ;
A69 05544 8102100 (00686) ;
A70 05546 8202100 (00687) ;
A71 05548 8302100 (00688) ;
A72 05550 8402100 (00689) ;
A73 05552 8502100 (00690) ;
A74 05554 8602100 (00691) ;
A75 05556 8702100 (00692) ;
A76 05558 8802100 (00693) ;
A77 05560 8902100 (00694) ;
A78 05562 8A02100 (00695) ;
A79 05564 8B02100 (00696) ;
A80 05566 8C02100 (00697) ;
A81 05568 8D02100 (00698) ;
A82 05570 8E02100 (00699) ;
A83 05572 8F02100 (00700) ;
A84 05574 9002100 (00701) ;
A85 05576 9102100 (00702) ;
A86 05578 9202100 (00703) ;
A87 05580 9302100 (00704) ;
A88 05582 9402100 (00705) ;
A89 05584 9502100 (00706) ;
A90 05586 9602100 (00707) ;
A91 05588 9702100 (00708) ;
A92 05590 9802100 (00709) ;
A93 05592 9902100 (00710) ;
A94 05594 9A02100 (00711) ;
A95 05596 9B02100 (00712) ;
A96 05598 9C02100 (00713) ;
A97 05600 9D02100 (00714) ;
A98 05602 9E02100 (00715) ;
A99 05604 9F02100 (00716) ;
A00 05606 A002100 (00717) ;
A01 05608 A102100 (00718) ;
A02 05610 A202100 (00719) ;
A03 05612 A302100 (00720) ;
A04 05614 A402100 (00721) ;
A05 05616 A502100 (00722) ;
A06 05618 A602100 (00723) ;
A07 05620 A702100 (00724) ;
A08 05622 A802100 (00725) ;
A09 05624 A902100 (00726) ;
A10 05626 AA02100 (00727) ;
A11 05628 AB02100 (00728) ;
A12 05630 AC02100 (00729) ;
A13 05632 AD02100 (00730) ;
A14 05634 AE02100 (00731) ;
A15 05636 AF02100 (00732) ;
A16 05638 B002100 (00733) ;
A17 05640 B102100 (00734) ;
A18 05642 B202100 (00735) ;
A19 05644 B302100 (00736) ;
A20 05646 B402100 (00737) ;
A21 05648 B502100 (00738) ;
A22 05650 B602100 (00739) ;
A23 05652 B702100 (00740) ;
A24 05654 B802100 (00741) ;
A25 05656 B902100 (00742) ;
A26 05658 BA02100 (00743) ;
A27 05660 BB02100 (00744) ;
A28 05662 BC02100 (00745) ;
A29 05664 BD02100 (00746) ;
A30 05666 BE02100 (00747) ;
A31 05668 BF02100 (00748) ;
A32 05670 C002100 (00749) ;
A33 05672 C102100 (00750) ;
A34 05674 C202100 (00751) ;
A35 05676 C302100 (00752) ;
A36 05678 C402100 (00753) ;
A37 05680 C502100 (00754) ;
A38 05682 C602100 (00755) ;
A39 05684 C702100 (00756) ;
A40 05686 C802100 (00757) ;
A41 05688 C902100 (00758) ;
A42 05690 CA02100 (00759) ;
A43 05692 CB02100 (00760) ;
A44 05694 CC02100 (00761) ;
A45 05696 CD02100 (00762) ;
A46 05698 CE02100 (00763) ;
A47 05700 CF02100 (00764) ;
A48 05702 D002100 (00765) ;
A49 05704 D102100 (00766) ;
A50 05706 D202100 (00767) ;
A51 05708 D302100 (00768) ;
A52 05710 D402100 (00769) ;
A53 05712 D502100 (00770) ;
A54 05714 D602100 (00771) ;
A55 05716 D702100 (00772) ;
A56 05718 D802100 (00773) ;
A57 05720 D902100 (00774) ;
A58 05722 DA02100 (00775) ;
A59 05724 DB02100 (00776) ;
A60 05726 DC02100 (00777) ;
A61 05728 DD02100 (00778) ;
A62 05730 DE02100 (00779) ;
A63 05732 DF02100 (00780) ;
A64 05734 E002100 (00781) ;
A65 05736 E102100 (00782) ;
A66 05738 E202100 (00783) ;
A67 057
```

PAGE 13: CBBN-TENEXD<4AP>BBNIDS.MSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
 MODEN INTERFACE AND SYSTEM CLOCKS PROGRAMS

04872 0000	(00369)	DATA 0	;BID3 : BID2
04873 0000	(00370)	DATA 0,0	;NO CONTROL REGISTERS
04874 0000			
04875 FFFF	(00371)	DATA -1,-1,-1,-1,-1	;NULL OFFSET, BUFFER CHAIN ANCHORS
04876 FFFF			
04877 FFFF			
04878 FFFF			
04879 FFFF	(00372)		



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PAGE 15: [BBN-TENEXD]<MAP>BBNIDS.MSD.96, 31-Dec-79 13:53:28, Ed: WOLF
          ADAM SINGLE-CHANNEL DOUBLE-BUFFERED SAMPLING

048D2 0000          DATA 0          ;REG. 0: SAMPLE RATE 1 (FOR EXT CLK)
048D3 0000          DATA 0          ;REG. 1: SAMPLE RATE 2 NOT USED
048D4 0000          DATA 0          ;REG. 2: ADAM CONTROL BITS
048D5 0002          DATA -1,-1,-1,-1 ;NULL OFFSET, BUFFER CHAIN ANCHORS
048D6 FFFF
048D7 FFFF
048D8 FFFF
048D9 FFFF
048DA FFFF
          (00424)
          (00425)
          (00426)
          (00427)
          (00428)

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PAGE 17: [BDM-TEHEND]<MAP>88NIOS.MSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
ADM SINGLE-CHANNEL DOUBLE-BUFFERED D/A OUT

04913 0000	(00478)	DATA 0	;REG. 0: SAMPLE RATE
04914 0000	(00479)	DATA 0	;REG. 1 NOT USED
04915 0002	(00480)	DATA DACNTRL	;REG. 2: AOM CONTROL BITS
04916 FFFF	(00481)	DATA -1,-1,-1,-1,-1	;NULL OFFSET, BUFFER CHAIN ANCHORS
04917 FFFF			
04918 FFFF			
04919 FFFF			
0491A FFFF	(00482)		

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(00483) 'AOM VSTATE-ONLY D/A PROGRAM
(00484) ; JJW, 7 OCT 79
(00485) ;
(00486) ; SPECIAL DIAGNOSTIC PROGRAM FOR DEBUGGING THE NOT-YET-REAL-TIME
(00487) ; VOCODER. D/A CHANNELS 0 AND 1 OUTPUT THE CONTENTS OF THE VOCODER
(00488) ; STATE WORD. RUNS ON AOM INTERNAL CLOCK AT A 50 USEC PERIOD ON
(00489) ; EACH CHANNEL
(00490) ;
(00491) ; #L = $4920
(00492) #L=0
(00493) AVCNTRL = 1*0+0
(00494) EVEN
(00495) DATA AV$SZ,0
(00496) ;
(00497) AV$BGN: BEGIN IOS2(AVPROG)
(00498) PW
(00499) LOAD(R0,VSTATE(1),L)
(00500) #1:
(00501) ADDR(R0,0,"H)
(00502) JIFC(#1,SYNCSOP)
(00503) STOP
(00504) END
(00505) ;
(00506) AV$SZ = #L-AV$BGN
(00507) DATA 0
(00508) DATA $0001
(00509) DATA 0
(00510) DATA 3,4
(00511) DATA (0000/20)/2-1
(00512) DATA 0
(00513) DATA AVCNTRL
(00514) DATA -1,-1,-1,-1,-1
(00515) ;
A00 04922 01300600 ;SET DIRECTION MAP-TO-AOM
A01 04924 02020581 ;VSTATE WORD ADR ON BUS 1
A02 04926 03100000 ;OUTPUT IT ON CH0
A03 04928 04100000 ;WE HAVE TO DO CB1 ALSO
A04 0492A 06301000 ;LOOP UNLESS CSDP SAYS TO STOP
A05 0492C 00000000
A06 04930 00306000
A07 04932 00000010 ;DIR=FROM MAP
04932 0000 ;#CHNLS=NULL ; AQ MODE = DOUBLE
04933 0001 ;ID3 : BID3
04934 0000 ;LOAD 3 CONTROL REGS STARTING WITH 0
04935 0003 ;REG. 0: SAMPLE RATE, 50 USEC
04937 0077- ;REG. 1: NOT USED
04938 0000 ;REG. 2: AOM CONTROL BITS
04939 0001 ;NULL OFFSET, BUFFER CHAIN ANCHORS
0493A FFFF
0493B FFFF
0493C FFFF
0493D FFFF
0493E FFFF

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PAGE 20: [BON-TENEXD] <MAP> BONIOS.MSD.96, 31-Dec-79 13:53:28, Ed: WOLF  
ADAMINT: ADAM INTERRUPT SERVICE ROUTINE

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(00569) ; DATA. JUST COUNT THIS FRAME AS DISCARDED.
(00570) EVEN
04985 0800 ADSDISC: INCN TADFOC
04986 E50054A HOP AD$RTM
04988 2107 ;
(00573) ;
(00574) ; ROUTINES TO COPY FROM THE A/D BUFFER POINTED TO BY (R2)+1
(00575) ; TO A TSOURCE BUFFER. A SINGLE BMOVE WOULD SUFFICE, BUT THAT WOULD
(00576) ; HOLD OFF INTERRUPTS FOR ABOUT 250 USEC, SO WE BREAK IT UP INTO A FEW,
(00577) ; WHICH IS ALMOST AS FAST, BUT MAKES FOR BETTER INTERRUPT LATENCY.
(00578) ; THE REV-19 CPU MICROCODE REVISION MODIFIED THE BMOVE R,X, WHEN SO THAT
(00579) ; (1) R IS LEFT POINTING TO THE LAST SOURCE-WORD MOVED, AND
(00580) ; (2) X IS LEFT WITH ITS ORIGINAL CONTENTS. THIS LETS US USE
(00581) ; THE BMOVES AS DONE BELOW WITHOUT RESETTNG R AND X BEFORE EACH ONE.
(00582) EVEN
04989 0800 ADSCPYA: MOVIR R4,SL6-1
0498A 9040001D BMOVE R2,R4,TSRA
0498C D720AE94 BMOVE R2,R4,TSRA+2*SL6
0498E D720AE08 BMOVE R2,R4,TSRA+4*SL6
04990 D720AF0C RETURN
04992 0E70 ;
(00588) EVEN
04993 0800 ADSCPYB: MOVIR R4,SL6-1
04994 9040001D BMOVE R2,R4,TSRB
04996 D720AF48 BMOVE R2,R4,TSRB+2*SL6
04998 D720AF84 BMOVE R2,R4,TSRB+4*SL6
0499A D720AFC0 RETURN
0499C 0E70 ;
(00595)

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PAGE 22: [88N-TEHEDJ<MAP>88N10S.WSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
TMODEMINT: TMODEM INTERRUPT SERVICE ROUTINE

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(00649) ;
(00650) ;
(00651) ; ROUTINES FOR COPYING FROM THE TBIT BUFFER TO THE TMODEM BUFFER.
(00652) ; R1 COMES POINTING TO THE 1ST WORD OF THE TBIT BUFFER (THE STREAM
(00653) ; STARTS IN THE 2ND WORD, BUT WE WANT THE 3RD WORD-2 FOR THE STREAM
(00654) ; WE COPY THE 2ND THRU THE 26TH HALFWORD, OMITTING THE (INITIAL)
(00655) ; SYNC-BIT WORD AND THE (FINAL) UNUSED WORD.
(00656) ; THE TBIT BUFFER HAS THESE DATA IN EXACTLY THE SAME POSITIONS (RELATIVE
(00657) ; TO THE START OF THE BUFFER) AS IN THE TMODEM BUFFER.
(00658)
(00659) TMSCPYA: MOVNR R4,1(R1) ;MOVE 2ND HALFWORD
(00660) MOVNR R4,TMDMA+1 ;INTO HALFWORD AFTER SYNC BIT
(00661) MOVIR R4,ML6-1
(00662) BMOVEL R1,R4,TMDMA+2 ;MOVE THE REST IN 3 HUNKS OF 43 FW
(00663) BMOVEL R1,R4,TMDMA+2+2*ML6
(00664) BMOVEL R1,R4,TMDMA+2+4*ML6
(00665) RETURN
(00666) ;
(00667)
(00668) TMSCPYB: MOVNR R4,1(R1)
(00669) MOVNR R4,TMDMB+1
(00670) MOVIR R4,ML6-1
(00671) BMOVEL R1,R4,TMDMB+2
(00672) BMOVEL R1,R4,TMDMB+2+2*ML6
(00673) BMOVEL R1,R4,TMDMB+2+4*ML6
(00674) RETURN
(00675)
03831 0000
03832 F0420001
03833 E0408005
03834 9040802A
03835 D7188006
03836 D718812C
03837 D7188182
03838 0E70
0383F 0000
03840 F0420001
03841 E0408108
03842 9040802A
03843 D71881DC
03844 D7188232
03845 D7188288
03846 0E70

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PAGE 23:

[88M-TENEXD]<MAP>8MIOS.MSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
RMODEMINT: RMODEM INTERRUPT SERVICE MODULE

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(00676) RMODEMINT: RMODEM INTERRUPT SERVICE MODULE
(00677) ; JMW, 25 SEPT 79
(00678) ;
(00679) ; RMODEMINT CHECKS SYNC ON THE BUFFER JUST INPUT BY THE RMODEM SCROLL
(00680) ; PROGRAM, AND IF SYNC IS GOOD, COPIES THE LATEST "FRAME" OF DATA
(00681) ; FROM THE RMODEM BUFFERS TO A RBITS BUFFER. A "FRAME" OF DATA IS
(00682) ; MBLNGTH WORDS STARTING WITH A SYNC-BIT WORD; IN GENERAL, IT DOES
(00683) ; NOT START AND END ON RMODEM BUFFER BOUNDARIES.
(00684) ; THE STATE OF FRAME SYNC IS GIVEN BY RSYNC:
(00685) ; RSYNC=0 MEANS WE KNOW NOTHING ABOUT SYNC, SO WE MUST CALL SYNCINIT
(00686) ; TO INITIALIZE SYNC-SEARCHING.
(00687) ; RSYNC=-1 MEANS WE ARE STILL IN THE PROCESS ON SCANNING INCOMING
(00688) ; BUFFERS FOR FRAME SYNC (SYNCRCH).
(00689) ; RSYNC=+1 MEANS WE HAVE SYNC, SO WE CALL SYNCUPDATE TO CHECK THE NEW
(00690) ; FRAME FOR CONTINUED SYNC. IF SYNCUPDATE SETS RSYNC TO 0, THEN WE
(00691) ; HAVE LOST SYNC AND WE MUST REINITIALIZE WITH SYNCINIT.
(00692) ; RMODEMINT ALSO:
(00693) ; COPIES THE ON-HOOK BIT OF THE 1ST DATUM IN EACH RMODEM
(00694) ; BUFFER INTO ROMHK FOR USE BY THE ADVANT MODULE,
(00695) ; SIMULATES CHANNEL ERRORS IF RERSIM IS NONZERO, AND
(00696) ; CHECKS TO BE SURE THAT THE RBITS BUFFER IS AVAILABLE; IF NOT, THEN
(00697) ; THE NEW FRAME OF RMODEM DATA IS SIMPLY DISCARDED.
(00698) ;
(00699) ; POINTER OFFSETS:
(00700) ; RBTP0 IS FOR THE RBITS BUFFERS AND FLAGS (-2,0; INIT TO -2
(00701) ; SO THAT IT POINTS TO RBTPA THE FIRST TIME)
(00702) ; RMP0 IS FOR RMODEM BUFFERS (-4,-2,0; INIT TO 0 SO IT GETS SWITCHED
(00703) ; TO RMDMA THE FIRST TIME)
(00704) ;
(00705) ; POINTERS TO RMODEM BUFFERS AND RBITS FLAGS. (POINTERS TO
(00706) ; RBITS BUFFERS ARE IN THE CORRECT MODULE.)
(00707) ; USE RBTPTR(RBTP0) TO POINT TO THE CURRENT RBITS BUFFER AND
(00708) ; RBTFPTR(RBTP0) TO POINT TO THE CURRENT FLAG.
(00709) ;
(00710) ;
(00711) ; RBTPTR: ADDR RBTPA
(00712) ;
(00713) ;
(00714) ; ADDR RMDMC ;USE RMPTR(RMP0) TO POINT TO THE
(00715) ; ADDR RMDMA ; CURRENT RMODEM BUFFER, OR RMPTR-2(RMP0)
(00716) ; ADDR RMDMB ; TO POINT TO THE PREVIOUS ONE
(00717) ; RMPTR: ADDR RMDMC
(00718) ;
(00719) ; NOTE THAT RMODEMINT IS TO BE ENTERED BY CALL R0,RMODEMINT,
(00720) ; SO IT SIMPLY RETURNS. THERE WILL BE ANOTHER LEVEL THAT
(00721) ; IS DISPATCHED TO BY THE INTERRUPT, SUCH AS:
(00722) ; CALL R0,RMODEMINT
(00723) ; RET
(00724) ;
(00725) ; RMODEMINT:
(00726) ; MOVLW SET+G2,SYSSFLCS ;MARK START OF RMODEMINT
(00727) ; MOVNR R2,RMP0 ;R2 GETS RMODEM BUFFER PTR OFFSET
(00728) ; INCR R2,2 ;STEP TO NEXT RMODEM BUFFER (A,B,C,A,...)

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0385F 182F (00729) SKPL LEZ
03860 9021FFFC (00730) MOVIR R2,-4
03862 E02E0546 (00731) MOVIR R2,RMPO
03864 E2000540 (00732) CPMZ R0W
03866 8010308A (00733) JMP RMISSTN,EQZ
03868 90943058 (00734) MOVIR R1,RMPTIR(R2)
0386A 70320080 (00735) MOVIR R3,R1,IDWSONRK
0386C E0300551 (00736) MOVIR R3,R0NHR
0386E F040057F (00737) MOVIR R4,RERSIM
03870 1A10 (00738) SKPL EQZ
03871 861038C2 (00739) CALL R1,RMISSIM
03873 E2000552 (00740) CPMZ RSYNC
03875 0000 (00741) EVEN
03876 801030AE (00742) RMISINIT,EQZ
03878 80303088 (00743) JMP RMISRCH,LIZ
0387A 86103C4C (00744) CALL R1,SYNCUPDATE
0387C E2000552 (00745) CPMZ RSYNC
0387E 80103082 (00746) JMP RMISLOST,EQZ
03880 F0300547 (00747) MOVIR R3,R0TPO
03882 E2063050 (00748) CPMZ R0TPTIR(R3)
03884 8110308E (00749) JMP RMISDISC,NEZ
;0 MEANS NIL, SO INIT
;-1 MEANS WE LACK IT, SO SEARCH
;+1 MEANS WE HAVE IT, SO UPDATE
;DO WE STILL HAVE IT AFTER THE UPDATE?
;NO, SO REINIT FOR SYNC-SEARCHING
;R3 GETS RBITS POINTER OFFSET
;IS RBITS BUFFER AVAILABLE (EMPTY)?
;NO, SO DISCARD THE FRAME OF DATA
;COPY FRAME OF BITS (INCLUDING SYNC BIT) FROM PREVIOUS AND CURRENT
;MODEM BUFFERS TO RBITS BUFFER. WE COPY (MBLNGTH-RBOFO) BITS
;FROM (PREV. RMODEM + RBOFO) TO RBITS, AND THEN (RBOFO) BITS FROM
;(CURRENT RMODEM) TO (RBITS + MBLNGTH-RBOFO).
;NOTE THAT WE JAM ADDRESSES INTO TWO MOVE INSTRUCTIONS BELOW
03886 90943056 (00750) MOVIR R1,RMPTIR-2(R2)
03888 FC100553 (00751) ADDMR R1,RBOFO
0388A 2711 (00752) DECR R1,1
0388B 0000 (00753) EVEN
0388C 90C630FC (00754) MOVIR R4,R0T8PTR(R3)
0388E EF403098 (00755) SAF R4,RMISBMD1
03890 90500104 (00756) MOVIR R5,MBLNGTH-1
03892 FE500553 (00757) SUBMR R5,RBOFO
03894 9C4A0001 (00758) ADDIR R4,1(R5)
03896 EF4030A2 (00759) SAF R4,RMISBMD2
03898 D51A0000 (00760) RMISBMD1: MOVE R1,R5,0
0389A F0500553 (00761) MOVIR R5,RBOFO
0389C 801030A4 (00762) JMP RMISSTF,EQZ
0389E 90943058 (00763) MOVIR R1,RMPTIR(R2)
038A0 2711 (00764) DECR R1,1
038A1 2751 (00765) DECR R5,1
038A2 D51A0000 (00766) RMISBMD2: MOVE R1,R5,0
038A4 E5063050 (00767) RMISSTF: INCM R0T8PTR(R3)
038A6 9431FFFE (00768) XORIR R3,-2
038A8 E0300547 (00769) MOVIR R3,R0TPO
038AA E500054F (00770) INCM RFRCTR
038AC 2000 (00771) HOP RMISSTN
;
038AD 0000 (00772) EVEN
038AE 861030E2 (00773) RMISINIT: CALL R1,SYNCINIT
038B0 2009 (00774) ROP RMISSTN
;
;DO NIL IF VOCODER NOT RUNNING
;R1 NOW POINTS TO LATEST RMODEM BUFFER
;GET ON-HOOK BIT FROM 1ST RMODEM DATUM
;AND SAVE IT FOR USE BY ADAMINT ROUTINE
;RERSIM=# OF CHANNEL ERRORS PER BUFFER
;TO BE SIMULATED
;WHAT DO WE KNOW ABOUT SYNC?
;0 MEANS NIL, SO INIT
;-1 MEANS WE LACK IT, SO SEARCH
;+1 MEANS WE HAVE IT, SO UPDATE
;DO WE STILL HAVE IT AFTER THE UPDATE?
;NO, SO REINIT FOR SYNC-SEARCHING
;R3 GETS RBITS POINTER OFFSET
;IS RBITS BUFFER AVAILABLE (EMPTY)?
;NO, SO DISCARD THE FRAME OF DATA
;COPY (MBLNGTH-RBOFO) BITS
;FROM (PREV. RMODEM + RBOFO) TO RBITS, AND THEN (RBOFO) BITS FROM
;(CURRENT RMODEM) TO (RBITS + MBLNGTH-RBOFO).
;NOTE THAT WE JAM ADDRESSES INTO TWO MOVE INSTRUCTIONS BELOW
;R1 = PTR TO PREVIOUS RMODEM BUFFER
;R1 NOW POINTS TO THE SYNC BIT
;MAKE R1 BE PTR-1 FOR MOVE
;R4 GETS PTR TO RBITS BUFFER
;SET UP 1ST MOVE WITH 17-BIT ADR
;R5 GETS NUMBER-1 OF BITS TO BE MOVED
;FROM PREVIOUS RMODEM BUFFER
;R4 NOW POINTS TO RBITS FOR 2ND COPY
;SET UP 2ND MOVE WITH 17-BIT ADR
;(ADR GETS SET ABOVE)
;DO WE NEED ANY BITS FROM 2ND BUFFER?
;JUMP IF NO
;R1=PTR TO CURRENT RMODEM BUFFER
;PTR-1 FOR MOVE
;NUMBER-1 FOR MOVE
;(ADR GETS SET ABOVE)
;SET R0TFLAG TO MARK RBITS BUFFER FULL
;SWITCH RBITS BUFFERS (-2,0,-2,...)
;COUNT FRAME RECEIVED
;INIT THE SYNC-SEARCH

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PAGE 25: [88N-TENEXD]MAP>88NLOS.MSD.96, 31-Dec-79 13:53:28, Ed: WOLF  
RMODEMINT: RMODEM INTERRUPT SERVICE MODULE

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03801 0000      EVEN
03802 90943856  RMISLST: MOVIR R1,9RMPTR-2(R2) ;WE HAVE LOST SYNC:
03804 861038E2  CALL R1,SYNCINIT ; CALL SYNCINIT ON THE OLDER RMODEM
03806 90943858  MOVIR R1,9RMPTR(R2) ; BUFFER, THEN SYNCRCH ON THE NEWER ONE
03808 86103C0C  RMISRCH: CALL R1,SYNCSRCH
0380A F80DFCE  RMISRTM: MOVLM CLR+G2,SYSSFLGS ;MARK END OF RMODEMINT
0380C 0E7F      RETURN
(00782)
(00783) RMISLST: MOVIR R1,9RMPTR-2(R2) ;WE HAVE LOST SYNC:
(00784) CALL R1,SYNCINIT ; CALL SYNCINIT ON THE OLDER RMODEM
(00785) MOVIR R1,9RMPTR(R2) ; BUFFER, THEN SYNCRCH ON THE NEWER ONE
(00786) RMISRCH: CALL R1,SYNCSRCH
(00787) RMISRTM: MOVLM CLR+G2,SYSSFLGS ;MARK END OF RMODEMINT
(00788) RETURN
(00789) ;
(00790) ;THE R0ITS BUFFER ISN'T READY (EMPTY) YET, SO WE HAVE NO PLACE TO
(00791) ; PUT THE RMODEM DATA, AND WE HAVE TO DISCARD IT.
(00792) EVEN
(00793) RMISDISC: INCH RMYDC ;COUNT RMODEM FRAME DISCARD
(00794) WOP
(00795) ;
(00796) ;ROUTINE TO SIMULATE CHANNEL ERRORS. WE USE THE EXEC CODE AS A "RANDOM"
(00797) ; NUMBER GENERATOR. WE MAINTAIN A POINTER TO THAT PART OF BUS 1. FOR
(00798) ; EACH ERROR, WE PULL IN 2 WORDS, XOR, AND OFF TO 8 BITS, AND USE THAT
(00799) ; AS AN OFFSET INTO THE CURRENT RMODEM BUFFER, WHERE WE FLIP THE DATA
(00800) ; BIT. THUS WE CAN ERROR ANY OF THE FIRST 256 BITS OF THE FRAME.
(00801) ; DOING IT TWICE PER FRAME GIVES 0.77% ERRORS.
(00802) ;UPON ENTRY, R1 POINTS TO THE START OF THE RMODEM BUFFER, AND R4 HOLDS
(00803) ; THE NUMBER OF ERRORS TO BE MADE.
(00804) EVEN
(00805) RMISIM: DECR R4,1 ;NUMBER-1 OF ERRORS
(00806) EVEN
(00807) MOVIR R5,ERRPTR ;PICK UP POINTER INTO EXEC CODE
(00808) MOVIR R7,10MSRD ;WILL BE USED TO FLIP DATA BIT
(00809) #1: MOVIR R6,0(R5) ;GET "RANDOM" NUMBER
(00810) XORMR R6,1(R5) ;MAKE IT MORE SO
(00811) INCR R6,3 ;AVOID BIAS FOR 0
(00812) INCR R5,3 ;STEP RANDOM NUMBER PTR
(00813) ANDIR R6,$FF ;MAKE THE RANDOM NUMBER 8 BITS
(00814) ADDR R6,R1 ;MAKE PTR INTO RMODEM BUFFER
(00815) EVEN
(00816) XORRM R7,0(R6) ;FLIP DATA BIT FOR THE ERROR
(00817) DJP R4,R1 ;GO BACK IF NOT ENOUGH ERRORS YET
(00818) CMPIR R5,RANDSU ;CHECK IF ERRPTR NEEDS RESETING
(00819) SKPL LE ;
(00820) MOVIR R5,RANDSL ;RESET ERRPTR TO LOWER VALUE
(00821) WOPRM R5,ERRPTR
(00822) RETURN
(00823) ERRPTR: DATA RANDSL
(00824)

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(00825) * FRAME SYNCHRONIZATION ROUTINES
(00826) ;
(00827) ; MISC. INTERNAL VARIABLES
(00828) ERRSYNC: DATA 0
(00829) LASTERR: DATA 0
(00830) OLDSYNC: DATA 0
(00831) ;RBOFO: (1ST 01)
(00832) ;
(00833) ;
(00834) ;
(00835) ; SYNCINIT: INITIALIZE BUFFERS, ETC. FOR SYNC SEARCHING. CALL WITH
(00836) ; R1 POINTING TO RMODEM BUFFER.
(00837) ; ALSO ZEROS THE GAIN WORD OF BOTH RESOURCE BUFFERS AND ZEROS THE
(00838) ; ENTIRE "LAST" RBITS BUFFER, SINCE IF WE JUST LOST SYNC, THOSE
(00839) ; BUFFERS CONTAIN GARBAGE, AND WE DON'T WANT TO RESUME SYNTHESIZING
(00840) ; WITH THEM.
(00841) ;
(00842) SYNCINIT: MOVIR R2,MBLNCTH-1 ;BUFFER OFFSET = LAST WORD IN BUFFER
(00843) ;R1 NOW POINTS TO END OF RMODEM BUFFER
(00844) ;PICK UP DATA BIT (BIT 0)
(00845) ;AND STASH IT IN RSSPF(1)
(00846) ;
(00847) DJP R2,RSLOOP
(00848) ;SET RSYNC=-1 TO MEAN SEARCH FOR SYNC
(00849) ;ZERO THE 3RD FULLWORD (=GAIN) OF BOTH
(00850) ;RESOURCE BUFFERS
(00851) ;PICK UP PTR OFFSET TO "THIS" RBITS
(00852) ;SWITCH IT TO THE "OTHER" ONE
(00853) ;R4 NOW POINTS TO "OTHER" RBITS BUFFER
(00854) ;CLEAR THE 1ST FULLWORD
(00855) ;RBITS-2 FOR BMOVEL DEST. ADR
(00856) ;SET UP ADR IN BMOVEL BELOW
(00857) ;RBITS-2 FOR BMOVEL SOURCE ADR
(00858) ;SET UP TO CLEAR MBLNCTH-1 HALFWORDS
(00859) ;(ADR IS SET UP ABOVE)
(00860) ;CLEAR THE RSSSS BUFFER TOO
(00861) ;
(00862) ;R2 IS STILL SET FROM PREVIOUS BMOVEL)
(00863) ;CLEAR THE LAST HALFWORD OF RSSSS
(00864) ;
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03C47 0800      EVEN
03C48 E0200552  SSSNO: MOVBM  R2,RSYNC      ;NO SYNC YET: SET TO -1 (=SEARCH)
03C4A 0E70      RETURN                      ; FOR GOOD MEASURE

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;SYNCUPDATE: CHECK RMODEM BUFFER TO SEE IF THE DATA BIT AT THE
;EXPECTED SYNC POSITION (START OF BUFFER+RBOFO) HAS THE EXPECTED
;VALUE. IF THERE ARE LOSETHR ERRORS WITHOUT 2 CONSECUTIVE CORRECT
;COMPARISONS, CLEAR RSYNC TO SIGNIFY LOSS OF FRAME SYNCHRONIZATION.
;ENTER WITH R1 POINTING TO THE LATEST RMODEM BUFFER. ALSO, THE
;WORD RBOFO HOLDS THE OFFSET IN THE FRAME THAT POINTS TO THE
;SYNC WORD TO BE VERIFIED.
;VARIABLES:
;RBOFO - BEGINNING OF FRAME OFFSET
;OLDSYNC - SYNC BIT FROM PREVIOUS FRAME
;LASTERR - NZ IF SYNC ERROR ON PREVIOUS FRAME
;ERRSYNC - COUNTS SYNC ERRORS (FROM LOSETHR DOWN TO 0)
;R1 NOW POINTS TO NEW SYNC WORD
;PICK UP THE NEW SYNC WORD
;XOR WITH LAST SYNC BIT
;SKIP IF DATA BIT SET (GOOD SYNC)
;WAS THERE AN ERROR LAST FRAME (NEZ)?
;YES
;NO: THAT'S (AT LEAST) 2 CONSECUTIVE
;CORRECT FRAMES, SO RESET ERROR COUNT
;REMEMBER NOWERROR FOR NEXT FRAME

;COME HERE ON SYNC BIT ERROR
;REMEMBER ERROR FOR NEXT FRAME
;HAVE WE COUNTED LOSETHR ERRORS?
;YES, THEREFORE WE'VE LOST SYNC
;COMPLEMENT OLDSYNC IN PREPARATION
;FOR USE NEXT FRAME

;ZERO RSYNC TO SAY WE LOST SYNC
;COUNT LOSS OF SYNC

```

## PRICE

PAGE 30: [BBN-TENEXD]<MAP>BBN105.MSD.96, 31-Dec-79 13:53:28, Ed: WOLF  
 ADMIN: ADM INTERRUPT SERVICE ROUTINE

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03CB1 0000 (01029) ;
03CB2 9040010 (01030) EVEN
03CB4 072000EA (01031) DASCPTB: MOVIR R4,SL6-1
03CB6 072000C26 (01032) BMOVEL R2,R4,RDAB8
03CB8 072000C62 (01033) BMOVEL R2,R4,RDAB8+2*SL6
03CBA 0E70 (01034) BMOVEL R2,R4,RDAB8+4*SL6
03CBB (01035) RETURN
03CBC (01036)

```

PAGE 31:

CBBN-TEMEXD)<MAP>BBNIDS.MSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
PROTECT(AB) -- PROTECT AND BITSTREAM THE CODED TRANSMITTER FRAME.

```
(01037) ;PROTECT(AB) -- PROTECT AND BITSTREAM THE CODED TRANSMITTER FRAME.
(01038) ;
(01039) ;PROTECT TAKES QUANTIZED AND CODED ANALYSIS PARAMETERS AND RESIDUAL
(01040) ; SAMPLES FROM A TSINK BUFFER, AND FORMS A BITSTREAM, INCLUDING
(01041) ; ERROR-PROTECTING CODES FOR SOME BITS OF CERTAIN PARAMETERS, IN
(01042) ; A TBITTS BUFFER. PROTECT NO LONGER RUNS AT INTERRUPT LEVEL, BUT IS
(01043) ; INVOKED VIA FCB.
(01044) ;PROTECT ALSO ACCUMULATES HISTOGRAM DATA FOR P, I, G, K1-K8.
(01045) ;
(01046) ;THIS ROUTINE IS ENTERED WITH R1 POINTING TO THE FCB%. I(R1)
(01047) ; IS THE BUFFER/FLAG/ROUTINE POINTER OFFSET, -2 FOR "A" OR 0 FOR "B".
(01048) ;
(01049) ;FORMAT OF INPUT (TSINK) BUFFER IS:
(01050) ; PITCH(5/6)
(01051) ; TAP(4/4)
(01052) ; GAIN(5/6)
(01053) ; K1(5/5)
(01054) ; K2(5/5)
(01055) ; K3(5/5)
(01056) ; K4(3/4)
(01057) ; K5(3/4)
(01058) ; K6(3/4)
(01059) ; K7(1/3)
(01060) ; K8(1/3)
(01061) ; BB1 - BB6(8/3)
(01062) ;WHERE (N/M) MEANS N HIGH-ORDER BITS PROTECTED OUT OF M TOTAL FOR PARAM.
(01063) ;
(01064) ;
(01065) ;FORMAT OF OUTPUT (TBITTS) BUFFER IS:
(01066) ; POSITION FOR THE SYNC BIT (IN THE TMODEM BUFFER)
(01067) ; HIGH 4 BITS PITCH (7/4)
(01068) ; 4 BITS TAP(7/4)
(01069) ; HIGH 4 BITS GAIN (7/4)
(01070) ; LOWEST PITCH BIT (1)
(01071) ; LOWEST GAIN BIT (1)
(01072) ; HIGH 4 BITS K1 (7/4)
(01073) ; HIGH 4 BITS K2 (7/4)
(01074) ; LOW PROT BIT PITCH, LOW PROT BIT GAIN, LOW K1, LOW K2 (7/4)
(01075) ; HIGH 4 K3 (7/4)
(01076) ; HIGH 3 BITS K4, LOW BIT K3 (7/4)
(01077) ; 3 HIGH BITS K5, HIGH BIT K6 (7/4)
(01078) ; LOWEST K4, LOWEST K5 (2)
(01079) ; LOWEST K6, LOW 2 K7 (3)
(01080) ; LOW 2 PROT K6, HIGH K7, HIGH K8 (7/4)
(01081) ; LOW 2 K8 (2)
(01082) ; BB1 - BB6(3)
(01083) ;... FOR A TOTAL OF 260 BITS, WHICH LEAVES THE LAST BIT POSITION UNUSED.
(01084) ;THERE'S ONE EXTRA BIT POSITION IN THE FRAME.
(01085) ;
(01086) ;REGISTER USAGE:
(01087) ; R1 = PTR-1 TO INPUT (TSINK) BUFFER
(01088) ; R2 = PTR TO OUTPUT (TBITTS) BUFFER
(01089) ; R3 = COUNTER IN PRBBLP
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PAGE 32:

[88M-TENEXD]<MAP>88MIOS.MSO.96, 31-Dec-79 13:53:20, Ed: WOLF  
PROTECT(AB) -- PROTECT AND BITSTREAM THE CODED TRANSMITTER FRAME.

```

(01090) ; R4-R6 ARE USED FOR HOLDING PIECES OF PARAMETERS. R6 IS
(01091) ; ALSO USED IN OUT7, BUT THE RETURN RESTORES IT.
(01092) ; R7 = HOLDS DATUM FOR INPUT TO OUT7, OUT3, OUT2 ROUTINES.
(01093) ;
(01094) ; POINTERS TO BUFFERS AND FLAGS. SOME OF THESE ARE ALSO USED BY
(01095) ; TMODEMINT.
(01096) ;
(01097) ; ADDR TSXKA-1
(01098) ; ADDR TSXKB-1
(01099) ;
(01100) ; ADDR TBYA+1
(01101) ; ADDR TBYB+1
(01102) ;
(01103) ; PROTECT: NOVLM SET+CL,SYSFLCS ; PICK UP POINTER OFFSET
(01104) ; MOVHR R3,1(R1)
(01105) ; MOVHR R1,0(TSNBPTR(R3)) ; R1 GETS PTR-1 TO TSINK BUFFER
(01106) ; MOVHR R2,0(TBTBPTR(R3)) ; R2 GETS OUTPUT PTR (TO TBT + 1)
(01107) ;
(01108) ; POPXI R1,R7 ; PITCH(5/6)
(01109) ; EVEN
(01110) ; INCM PHIST(R7) ; ADD INTO HISTOGRAM DATA
(01111) ; MOVHR R4,R7,3 ; LOW 2 BITS TO R4
(01112) ; ARS R7,2 ; HIGH 4 BITS ONLY
(01113) ; EVEN
(01114) ; CALL R6,OUT7 ; PUT 7 BITS OUT
(01115) ;
(01116) ; POPXI R1,R7 ; TAP(4/4)
(01117) ; EVEN
(01118) ; INCM THIST(R7)
(01119) ; CALL R6,OUT7 ; PUT 7 TAP BITS OUT
(01120) ;
(01121) ; POPXI R1,R7 ; GAIN(5/6)
(01122) ; EVEN
(01123) ; INCM CHIST(R7)
(01124) ; MOVHR R5,R7,3 ; LOW 2 BITS TO R5
(01125) ; ARS R7,2 ; HIGH 4 BITS ONLY
(01126) ; EVEN
(01127) ; CALL R6,OUT7 ; HIGH GAIN OUT
(01128) ;
(01129) ; MOVHR R7,R4,1 ; LOWEST PITCH BIT
(01130) ; IORIR R7,TMBITS ; ADD MODEM BITS
(01131) ; PUSHXI R2,R7 ; PUT OUT 1 BIT
(01132) ; EVEN
(01133) ;
(01134) ; MOVHR R7,R5,1 ; GET LOWEST GAIN BIT
(01135) ; IORIR R7,TMBITS
(01136) ; PUSHXI R2,R7 ; PUT OUT 1 BIT
(01137) ; EVEN
(01138) ;
(01139) ; MOVHR R6,R4,2 ; LOWEST PROT PITCH BIT TO R6
(01140) ; ADDR R6,R6 ; SHIFT LEFT (FASTER THAN LLS) TO MAKE ROOM
(01141) ; EVEN
(01142) ; TORR R6,R5,2 ; OR IN LOWEST PROT GAIN BIT

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PAGE 33: [88N-TENEXD] <MAP> 88NIOS.MSO.96, 31-Dec-79 13:53:28, EQ: WOLF  
PROTECT(AB) -- PROTECT AND BITSTREAM THE CODED TRANSMITTER FRAME.

03CF8 301E (01143)	POPXI R1,R7	;K1(5/5)
03CF9 0000 (01144)	EVEN	
03CFA E50ED2E (01145)	INCM KIHIST(R7)	
03CFC 566E0001 (01146)	IORKR R6,R7,1	;FOR IN LOWEST K1 BIT
03CFE 4C6C (01147)	ADDRR R6,R6	;MAKE ROOM
03CFF 3871 (01148)	ARS R7,1	;HIGH 4 BITS K1 ONLY
03D00 86603D70 (01149)	CALL R6,OUT7	;PUT OUT PROTECTED HIGH 4 BITS K1
03D02 301E (01150)	POPXI R1,R7	;K2(5/5)
03D03 0000 (01151)	EVEN	
03D04 E50ED4E (01152)	INCM K2HIST(R7)	
03D06 566E0001 (01153)	IORKR R6,R7,1	;FOR IN LOWEST BIT K2
03D08 3871 (01154)	ARS R7,1	;HIGH 4 BITS K2 ONLY
03D09 0000 (01155)	EVEN	
03D0A 86603D70 (01156)	CALL R6,OUT7	;OUTPUT PROTECTED HIGH 4 BITS K2
03D0C 407C (01157)	MOVRR R7,R6	;PICK UP BITS STASHED IN R6
03D0D 0000 (01158)	EVEN	
03D0E 86603D70 (01159)	CALL R6,OUT7	;OUTPUT LOWEST BITS PROTECTED OF P,C,K1,K2
03D10 301E (01160)	POPXI R1,R7	;K3(5/5)
03D11 0000 (01161)	EVEN	
03D12 E50ED06E (01162)	INCM K3HIST(R7)	
03D14 504E0001 (01163)	MOVRR R4,R7,1	;SAVE LOWEST BIT OF K3 IN R4
03D16 3871 (01164)	ARS R7,1	;HIGH 4 BITS ONLY
03D17 0000 (01165)	EVEN	
03D18 86603D70 (01166)	CALL R6,OUT7	;OUTPUT HIGH 4 BITS K3 PROTECTED
03D1A 301E (01167)	POPXI R1,R7	;K4(3/4)
03D1B 0000 (01168)	EVEN	
03D1C E50ED08E (01169)	INCM K4HIST(R7)	
03D1E 505E0001 (01170)	MOVRR R5,R7,1	;LOW BIT K4 TO R5
03D20 9A7E000E (01171)	ANDIR R7,0'16'	;CLEAR SLOT FOR LO K3
03D22 4678 (01172)	IORRR R7,R4	;FOR IN LOW BIT K3 TO HIGH 3 BITS K4
03D23 0000 (01173)	EVEN	
03D24 86603D70 (01174)	CALL R6,OUT7	;OUTPUT PROTECTED 3 HI K4, LOW K3
03D26 301E (01175)	POPXI R1,R7	;K5(3/4)
03D27 4C5A (01176)	ADDRR R5,R5	;LOW K4, SHIFT TO MAKE ROOM
03D28 E50ED09E (01177)	INCM K5HIST(R7)	
03D2A 565E0001 (01178)	IORKR R5,R7,1	;FOR IN LOWEST K5 BIT
03D2C 3018 (01179)	POPXI R1,R4	;K6(3/4)
03D2D 0000 (01180)	EVEN	
03D2E E50ED0AE (01181)	INCM K6HIST(R7)	
03D30 56600007 (01182)	MOVRR R6,R4,7	;LOW 3 BITS K6
03D32 3843 (01183)	ARS R4,3	;HIGH BIT K6
03D33 0000 (01184)	EVEN	
03D34 9A7E000E (01185)	ANDIR R7,0'16'	;SAVE 3 HIGH BITS K5
03D36 4678 (01186)	IORRR R7,R4	;FOR IN 1 HIGH BIT K6
03D37 0000 (01187)	EVEN	
03D38 86603D70 (01188)	CALL R6,OUT7	;OUTPUT 3 HIGH K5, 1 HIGH K6, PROTECTED
03D3A 407A (01189)	MOVRR R7,R5	;LOWEST K4, LOWEST K5

[illegible]

PAGE 35: [B8K-TENEXD]<MAP>B8N10S.MS0.96, 31-Dec-79 13:53:28, Ed: WOLF  
PROTECT(AB) -- PROTECT AND BITSTREAM THE CODED TRANSMITTER FRAME.

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0307B 0000 (01249) EVEN
0307C 4C7E (01250) ADDR R7,R7 ;CONVERT R7 TO FULLWORD OFFSET
0307D 2623 (01251) INCR R2,3 ;UPDATE OUTPUT POINTER
0307E 90FE3082 (01252) MOVIR R7,0PTR3(R7) ;R7 GETS PTR-1
0308 0000 (01253) POPHI R7,-3(R2) ;MOVE 3 WORDS
0308C C875FFFF (01254) POPHI R7,-2(R2)
0308E C875FFFF (01255) POPHI R7,-1(R2)
0309 0000 (01256) RETURN
03096 0E70 (01257) ;
03098 0000 (01258) ;OUT2 OUTPUTS 2 BITS GIVEN IN R7
0309C 4C7E (01259) EVEN
0309E 2622 (01260) ADDR R7,R7 ;CONVERT R7 TO FULLWORD OFFSET
030A 0000 (01261) INCR R2,2 ;UPDATE OUTPUT POINTER
030A8 90FE30C2 (01262) MOVIR R7,0PTR2(R7) ;R7 GETS PTR-1
030AC C875FFFF (01263) POPHI R7,-2(R2)
030AE C875FFFF (01264) POPHI R7,-1(R2)
030B 0000 (01265) RETURN
030B6 0E70 (01266)

```

TABLE OF HANNING CODE SEQUENCES. INDEX WITH 2\*(4-BIT VALUE)  
; TO BE PROTECTED; TABLE ENTRY IS PTR-1 TO THE 7-BIT HANNING  
; CODEWORD.

```

03091 0000 (01270) EVEN
03092 000030F1 (01271) HPTR: ADDR H000-1
03094 000030C9 (01272) ADDR H151-1
03096 000030E3 (01273) ADDR H052-1
03098 000030E8 (01274) ADDR H103-1
0309A 000030D2 (01275) ADDR H114-1
0309C 000030CB (01276) ADDR H045-1
0309E 000030D1 (01277) ADDR H145-1
030A 000030EA (01278) ADDR H017-1
030A2 000030EE (01279) ADDR H160-1
030A4 000030D3 (01280) ADDR H031-1
030A6 000030DF (01281) ADDR H132-1
030A8 000030D4 (01282) ADDR H063-1
030AA 000030EC (01283) ADDR H074-1
030AC 000030E2 (01284) ADDR H125-1
030AE 000030CD (01285) ADDR H026-1
030B 000030D9 (01286) ADDR H177-1
030B6 000030D0 (01287)

```

TABLE OF PTR-1 TO 3-BIT SEQUENCES  
PTR3:

```

03082 000030F2 (01288) ADDR P0-1
03084 000030D7 (01289) ADDR P1-1
03086 000030CE (01291) ADDR P2-1
03088 000030E0 (01292) ADDR P3-1
0308A 000030F0 (01293) ADDR P4-1
0308C 000030CA (01294) ADDR P5-1
0308E 000030D5 (01295) ADDR P6-1
0309 000030DA (01296) ADDR P7-1
03096 000030D0 (01297)

```

TABLE OF PTR-1 TO 2-BIT SEQUENCES  
PTR2:

```

030C2 000030F3 (01298) ADDR Q0-1
030C4 000030D8 (01299) ADDR Q1-1
030C6 000030CF (01301) ADDR Q2-1

```

PAGE 36: [BBN-TENEXD]<MAP>BBNLOS.MSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
 PROTECT(AB) -- PROTECT AND BITSTREAM THE CODED TRANSMITTER FRAME.

	ADDR Q3-1
030C8 000030E1 (01302)	
	(01303)
	(01304) ;TABLE OF CODE SEQUENCES.
030CA 0000	(01305) H151: DATA 1 + TMBITS
030CB 0000	(01306) P5: DATA 1 + TMBITS
030CC 000C	(01307) H045: DATA 0 + TMBITS
030CD 0000	(01308) DATA 1 + TMBITS
030CE 000C	(01309) H026: DATA 0 + TMBITS
030CF 000C	(01310) P2: DATA 0 + TMBITS
030D0 0000	(01311) Q2: DATA 1 + TMBITS
030D1 000C	(01312) DATA 0 + TMBITS
030D2 0000	(01313) H146: DATA 1 + TMBITS
030D3 0000	(01314) H114: DATA 1 + TMBITS
030D4 000C	(01315) H031: DATA 0 + TMBITS
030D5 000C	(01316) H063: DATA 0 + TMBITS
030D6 0000	(01317) P6: DATA 1 + TMBITS
030D7 0000	(01318) DATA 1 + TMBITS
030D8 000C	(01319) P1: DATA 0 + TMBITS
030D9 000C	(01320) Q1: DATA 0 + TMBITS
030DA 0000	(01321) H177: DATA 1 + TMBITS
030DB 0000	(01322) P7: DATA 1 + TMBITS
030DC 0000	(01323) DATA 1 + TMBITS
030DD 0000	(01324) DATA 1 + TMBITS
030DE 0000	(01325) DATA 1 + TMBITS
030DF 0000	(01326) DATA 1 + TMBITS
030E0 0000	(01327) H132: DATA 1 + TMBITS
030E1 000C	(01328) P3: DATA 0 + TMBITS
030E2 0000	(01329) Q3: DATA 1 + TMBITS
030E3 0000	(01330) H125: DATA 0 + TMBITS
030E4 000C	(01331) H052: DATA 1 + TMBITS
030E5 0000	(01332) DATA 1 + TMBITS
030E6 000C	(01333) DATA 0 + TMBITS
030E7 0000	(01334) DATA 1 + TMBITS
030E8 000C	(01335) DATA 0 + TMBITS
030E9 0000	(01336) H103: DATA 1 + TMBITS
030EA 000C	(01337) DATA 0 + TMBITS
030EB 000C	(01338) H017: DATA 0 + TMBITS
030EC 000C	(01339) DATA 0 + TMBITS
030ED 000C	(01340) H074: DATA 0 + TMBITS
030EE 0000	(01341) DATA 1 + TMBITS
030EF 0000	(01342) H160: DATA 1 + TMBITS
030F0 0000	(01343) DATA 1 + TMBITS
030F1 0000	(01344) P4: DATA 1 + TMBITS
030F2 000C	(01345) H000: DATA 0 + TMBITS
030F3 000C	(01346) P0: DATA 0 + TMBITS
030F4 000C	(01347) Q0: DATA 0 + TMBITS
030F5 000C	(01348) DATA 0 + TMBITS
030F6 000C	(01349) DATA 0 + TMBITS
030F7 000C	(01350) DATA 0 + TMBITS
030F8 000C	(01351) DATA 0 + TMBITS
	(01352)

PAGE 37: [BBM-TENEXD]<HAP>BBNIDS.MSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
 CORRECT(AB): UNBITSTREAM, ERROR-CORRECT, AND DECODE

```

(01353) ;CORRECT(AB): UNBITSTREAM, ERROR-CORRECT, AND DECODE
(01354) ;
(01355) ;CORRECT TAKES A FRAME OF BITSTREAM (RBITS BUFFER) FROM THE
(01356) ; RECEIVER MODEM AND FORMS IT INTO PARAMETERS AND RESIDUAL SAMPLES,
(01357) ; DOING ERROR-CORRECTING DECODING FOR THE PROTECTED DATA BITS. EACH
(01358) ; UNBITSTREAMED AND CORRECTED VALUE IS THEN LOOKED UP IN THE
(01359) ; APPROPRIATE DECODING TABLE, SO THAT THE OUTPUT (TSOURCE) BUFFER IS
(01360) ; FULL-WORD FLOATING POINT VALUES READY FOR THE SYNTHESIS PROCESS.
(01361) ;CORRECT NO LONGER RUNS AT INTERRUPT LEVEL, BUT IS INVOKED VIA FCB.
(01362) ;
(01363) ;THIS ROUTINE IS ENTERED (VIA FCB) WITH R1 POINTING TO THE
(01364) ; FCB#. 1(R1) IS THE BUFFER/FLAG POINTER OFFSET, EQUAL TO -2 FOR "A"
(01365) ; OR 0 FOR "B".
(01366) ;REGISTER USAGE:
(01367) ; R1 = PTR-1 TO THE INPUT (RBITS) BUFFER (USED BY POPX'S)
(01368) ; R2 = PTR TO THE OUTPUT (RSOURCE) BUFFER (USED IN PUSHMIL'S)
(01369) ; R6 - SCRATCH IN PARAM DECODING AND IN GET74, ETC.
(01370) ; R7 - GET74, ETC. RETURN VALUES IN HERE
(01371) ;POINTERS TO BUFFERS. THE RBITS POINTERS ARE ALSO USED BY RMODEMINT.
(01372) ;EVEN
(01373) ;ADDR RBIA ;RBITS BUFFERS
(01374) ;RBTBPTR: ADDR RBTB
(01375) ;
(01376) ;ADDR RSRA ;RSOURCE BUFFERS
(01377) ;RSRBPTR: ADDR RSRB
(01378) ;
(01379) ;TEMPS FOR PARTIAL, UNBITSTREAMED, CODED PARAM VALUES
(01380) ;PITCH: DATA 0 ;ALSO K3, K4, K7
(01381) ;TAP: DATA 0 ;ALSO K5, K8
(01382) ;GAIN: DATA 0 ;ALSO K6
(01383) ;K1: DATA 0
(01384) ;K2: DATA 0
(01385) ;K3 = PITCH
(01386) ;K4 = PITCH
(01387) ;K5 = TAP
(01388) ;K6 = GAIN
(01389) ;K7 = PITCH
(01390) ;K8 = TAP
(01391) ;
(01392) ;EVEN
(01393) ;CORRECT: MOVLM SET+G1,SYSSFLGS
(01394) ;MOVLM R2,1(R1) ;PICK UP POINTER OFFSET
(01395) ;MOVIR R1,0RBTBPTR(R2) ;R1 GETS PTR TO RBITS BUFFER (POINTS TO
(01396) ; ;SYNC BIT, SO IS PTR-1 TO THE DATA)
(01397) ;MOVIR R2,0RSRBPTR(R2) ;R2 GETS PTR TO RSOURCE BUFFER
(01398) ;
(01399) ;CALL R0,GET74 ;GET AND DECODE HI 4 OF PITCH
(01400) ;LLS R7,2 ;SHIFT BITS TO FINAL RESING PLACE
(01401) ;EVEN
(01402) ;MOVLM R7,PITCH ;AND SAVE THEM
(01403) ;
(01404) ;CALL R0,GET74 ;GSD TAP (4 BITS)
(01405) ;MOVLM R7,TAP ;SAVE IT

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PAGE 38: [BBN-TENEXD]<MAP>89NIOS.MSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
CORRECT(RB): UNBITSTREAM, ERROR-CORRECT, AND DECODE

```

03E1A 8603ED2 (01406) ;
03E1C 3A72 (01407) ;
03E1D 0000 (01408) ;
03E1E E0703E04 (01409) ;
03E20 8603F08 (01410) ;
03E22 F7703E02 (01411) ;
03E24 8603F08 (01412) ;
03E26 F7703E04 (01413) ;
03E28 8603ED2 (01414) ;
03E2A 4C7E (01415) ;
03E2B 0000 (01416) ;
03E2C E0703E05 (01417) ;
03E2E 8603ED2 (01418) ;
03E30 4C7E (01419) ;
03E31 0000 (01420) ;
03E32 E0703E06 (01421) ;
03E34 8603ED2 (01422) ;
03E36 506E0010 (01423) ;
03E38 3862 (01424) ;
03E39 0000 (01425) ;
03E3A F6603E02 (01426) ;
03E3C 9260000A (01427) ;
03E3E 1B30 (01428) ;
03E3F 9060000A (01429) ;
03E41 9260004C (01430) ;
03E43 1B20 (01431) ;
03E44 9060004C (01432) ;
03E46 340E (01433) ;
03E47 3A66 (01434) ;
03E48 90700042 (01435) ;
03E4A 84640000 (01436) ;
03E4C 2622 (01437) ;
03E4D 0000 (01438) ;
03E4E F6603E03 (01439) ;
03E50 C62C5E00 (01440) ;
03E52 310E (01441) ;
03E53 0000 (01442) ;
03E54 506E0000 (01443) ;
03E56 3861 (01444) ;
03E57 0000 (01445) ;
03E58 F6603E04 (01446) ;
03E5A C62C5E00 (01447) ;
03E5C 506E0004 (01448) ;
03E5E 3861 (01449) ;
03E5F 0000 (01450) ;
03E60 F6603E05 (01451) ;

CALL R0,GET74 ;
LLS R7,2 ;
EVEN ;
MOVNR R7,GAIN ;
;AND SAVE THEM
;GET LOWEST PITCH BIT
;AND OR IT INTO PITCH
;SET LOWEST GAIN BIT
;LIKEWISE
;AND HI 4 OF K1
;SHIFT TO POSITION (FASTER THAN LLS)
;AND STORE
;AND HI 4 OF K2
;LIKEWISE
;AND B1 PITCH, B1 GAIN, B0 K1, B0 K2
;EXTRACT B1 PITCH
;SHIFT IT INTO POSITION
;R6 NOW HAS THE CODED PITCH (LSHFTD 1)
;MAKE SURE PITCH VALUE IS WITHIN BOUNDS
;VALUE WAS TOO SMALL
;VALUE WAS TOO LARGE
;TEMP. SAVE R7 ON THE STACK
;MAKE INTO UNNORM FLTNG NUMBER
;RIGHT EXP FOR FLT INTEGER
;PUT OUT PITCH AS UNNORM FLT PT INTCR
;STEP OUTPUT PTR TO TAP
;SET CODED TAP
;DECODE AND STORE TAP
;RESTORE R7 FOR THE OTHER 3 BITS
;EXTRACT B1 GAIN
;SHIFT TO RIGHT SPOT
;OR IN THE REST OF THE GAIN BITS
;DECODE AND STORE GAIN
;EXTRACT B0 K1

```

PAGE 39: IBBM-TENEXD3<MAP>88MIOS.MSD.96, 31-Dec-79 13:53:28, Ed: WOLF  
CORRECT(AB): UNBITSTREAM, ERROR-CORRECT, AND DECODE

```

03E62 C62C5D10 (01459)      PUSHMIL R2,DKTAB1(R6)
03E64 9A700002 (01460)      ANDIR R7,C0
03E66 F6703E06 (01461)      IOR4R R7,K2
03E68 C62E5D50 (01462)      PUSHMIL R2,DKTAB2(R7)
                                ;
03E6A 06003ED2 (01463)      CALL R0,GET74
03E6C 4C7E (01464)          ADDR R7,R7
03E6D 0000 (01465)          EVEN
03E6E E0703E2 (01466)      MOVPM R7,K3
                                ;
03E70 06003ED2 (01467)      CALL R0,GET74
03E72 506E0002 (01468)      MOVKR R6,R7,C0
03E74 F6603E2 (01469)      IOR4R R6,K3
03E76 C62C5D90 (01470)      PUSHMIL R2,DKTAB3(R6)
03E78 506E001C (01471)      MOVKR R6,R7,C321
03E7A E0603E2 (01472)      MOVPM R6,K4
                                ;
03E7C 06003ED2 (01473)      CALL R0,GET74
03E7E 506E001C (01474)      MOVKR R6,R7,C321
03E80 E0603E03 (01475)      MOVPM R6,K5
03E82 9A700002 (01476)      ANDIR R7,C0
03E84 3A73 (01477)          LLS R7,3
03E85 0000 (01478)          EVEN
03E86 E0703E04 (01479)      MOVPM R7,K6
                                ;
03E88 06003F08 (01480)      CALL R0,GET1
03E8A F6703E2 (01481)      IOR4R R7,K4
03E8C C62E5D08 (01482)      PUSHMIL R2,DKTAB4(R7)
                                ;
03E8E 06003F08 (01483)      CALL R0,GET1
03E90 F6703E03 (01484)      IOR4R R7,K5
03E92 C62E5D0F (01485)      PUSHMIL R2,DKTAB5(R7)
                                ;
03E94 06003F08 (01486)      CALL R0,GET1
03E96 F7703E04 (01487)      IOR4R R7,K6
                                ;
03E98 06003FE (01488)      CALL R0,GET2
03E9A E0703E2 (01489)      MOVPM R7,K7
                                ;
03E9C 06003ED2 (01490)      CALL R0,GET74
03E9E 506E0010 (01491)      MOVKR R6,R7,C32
03EA0 3061 (01492)          ARS R6,1
03EA1 0000 (01493)          EVEN
03EA2 F6603E04 (01494)      IOR4R R6,K6
03EA4 C62C5E10 (01495)      PUSHMIL R2,DKTAB6(R6)
03EA6 4C7E (01496)          ADDR R7,R7
03EA7 0000 (01497)          EVEN
03EA8 506E0008 (01498)      MOVKR R6,R7,C2
03EAA F6603E2 (01499)      IOR4R R6,K7
03EAC C62C5E30 (01500)      PUSHMIL R2,DKTAB7(R6)
03EAE 4C7E (01501)          ADDR R7,R7
03EAF 0000 (01502)          EVEN
03EB0 9A700008 (01503)      ANDIR R7,C2
                                ;
03EB2 C62C5D10 (01504)      PUSHMIL R2,DKTAB1(R6)
03EB4 9A700002 (01505)      ANDIR R7,C0
03EB6 F6703E06 (01506)      IOR4R R7,K2
03EB8 C62E5D50 (01507)      PUSHMIL R2,DKTAB2(R7)
03EBA 06003ED2 (01508)      CALL R0,GET74
03EBC 4C7E (01509)          ADDR R7,R7
03EBD 0000 (01510)          EVEN
03EBE E0703E2 (01511)      MOVPM R7,K3
                                ;
03EC0 06003ED2 (01512)      CALL R0,GET74
03EC2 506E0002 (01513)      MOVKR R6,R7,C0
03EC4 F6603E2 (01514)      IOR4R R6,K3
03EC6 C62C5D90 (01515)      PUSHMIL R2,DKTAB3(R6)
03EC8 506E001C (01516)      MOVKR R6,R7,C321
03ECA E0603E2 (01517)      MOVPM R6,K4
                                ;
03ECB 06003ED2 (01518)      CALL R0,GET74
03ECD 506E001C (01519)      MOVKR R6,R7,C321
03ECE E0603E03 (01520)      MOVPM R6,K5
03ECF 9A700002 (01521)      ANDIR R7,C0
03ED1 3A73 (01522)          LLS R7,3
03ED2 0000 (01523)          EVEN
03ED3 E0703E04 (01524)      MOVPM R7,K6
                                ;
03ED5 06003F08 (01525)      CALL R0,GET1
03ED7 F6703E2 (01526)      IOR4R R7,K4
03ED9 C62E5D08 (01527)      PUSHMIL R2,DKTAB4(R7)
                                ;
03EDB 06003F08 (01528)      CALL R0,GET1
03EDD F6703E03 (01529)      IOR4R R7,K5
03EDF C62E5D0F (01530)      PUSHMIL R2,DKTAB5(R7)
                                ;
03EE1 06003F08 (01531)      CALL R0,GET1
03EE3 F7703E04 (01532)      IOR4R R7,K6
                                ;
03EE5 06003FE (01533)      CALL R0,GET2
03EE7 E0703E2 (01534)      MOVPM R7,K7
                                ;
03EE9 06003ED2 (01535)      CALL R0,GET74
03EEB 506E0010 (01536)      MOVKR R6,R7,C32
03EED 3061 (01537)          ARS R6,1
03EEF 0000 (01538)          EVEN
03EF1 F6603E04 (01539)      IOR4R R6,K6
03EF3 C62C5E10 (01540)      PUSHMIL R2,DKTAB6(R6)
03EF5 4C7E (01541)          ADDR R7,R7
03EF7 0000 (01542)          EVEN
03EF9 506E0008 (01543)      MOVKR R6,R7,C2
03EFA F6603E2 (01544)      IOR4R R6,K7
03EFC C62C5E30 (01545)      PUSHMIL R2,DKTAB7(R6)
03EFE 4C7E (01546)          ADDR R7,R7
03EFF 0000 (01547)          EVEN
03F01 9A700008 (01548)      ANDIR R7,C2
                                ;
03F03 C62C5D10 (01549)      PUSHMIL R2,DKTAB1(R6)
03F05 9A700002 (01550)      ANDIR R7,C0
03F07 F6703E06 (01551)      IOR4R R7,K2
03F09 C62E5D50 (01552)      PUSHMIL R2,DKTAB2(R7)
03F0B 06003ED2 (01553)      CALL R0,GET74
03F0D 4C7E (01554)          ADDR R7,R7
03F0F 0000 (01555)          EVEN
03F11 E0703E2 (01556)      MOVPM R7,K3
                                ;
03F13 06003ED2 (01557)      CALL R0,GET74
03F15 506E0002 (01558)      MOVKR R6,R7,C0
03F17 F6603E2 (01559)      IOR4R R6,K3
03F19 C62C5D90 (01560)      PUSHMIL R2,DKTAB3(R6)
03F1B 506E001C (01561)      MOVKR R6,R7,C321
03F1D E0603E2 (01562)      MOVPM R6,K4
                                ;
03F1F 06003ED2 (01563)      CALL R0,GET74
03F21 506E001C (01564)      MOVKR R6,R7,C321
03F23 E0603E03 (01565)      MOVPM R6,K5
03F25 9A700002 (01566)      ANDIR R7,C0
03F27 3A73 (01567)          LLS R7,3
03F28 0000 (01568)          EVEN
03F29 E0703E04 (01569)      MOVPM R7,K6
                                ;
03F2B 06003F08 (01570)      CALL R0,GET1
03F2D F6703E2 (01571)      IOR4R R7,K4
03F2F C62E5D08 (01572)      PUSHMIL R2,DKTAB4(R7)
                                ;
03F31 06003F08 (01573)      CALL R0,GET1
03F33 F6703E03 (01574)      IOR4R R7,K5
03F35 C62E5D0F (01575)      PUSHMIL R2,DKTAB5(R7)
                                ;
03F37 06003F08 (01576)      CALL R0,GET1
03F39 F7703E04 (01577)      IOR4R R7,K6
                                ;
03F3B 06003FE (01578)      CALL R0,GET2
03F3D E0703E2 (01579)      MOVPM R7,K7
                                ;
03F3F 06003ED2 (01580)      CALL R0,GET74
03F41 506E0010 (01581)      MOVKR R6,R7,C32
03F43 3061 (01582)          ARS R6,1
03F45 0000 (01583)          EVEN
03F47 F6603E04 (01584)      IOR4R R6,K6
03F49 C62C5E10 (01585)      PUSHMIL R2,DKTAB6(R6)
03F4B 4C7E (01586)          ADDR R7,R7
03F4D 0000 (01587)          EVEN
03F4F 506E0008 (01588)      MOVKR R6,R7,C2
03F51 F6603E2 (01589)      IOR4R R6,K7
03F53 C62C5E30 (01590)      PUSHMIL R2,DKTAB7(R6)
03F55 4C7E (01591)          ADDR R7,R7
03F57 0000 (01592)          EVEN
03F59 9A700008 (01593)      ANDIR R7,C2
                                ;
03F5B C62C5D10 (01594)      PUSHMIL R2,DKTAB1(R6)
03F5D 9A700002 (01595)      ANDIR R7,C0
03F5F F6703E06 (01596)      IOR4R R7,K2
03F61 C62E5D50 (01597)      PUSHMIL R2,DKTAB2(R7)
03F63 06003ED2 (01598)      CALL R0,GET74
03F65 4C7E (01599)          ADDR R7,R7
03F67 0000 (01600)          EVEN
03F69 E0703E2 (01601)      MOVPM R7,K3
                                ;
03F6B 06003ED2 (01602)      CALL R0,GET74
03F6D 506E0002 (01603)      MOVKR R6,R7,C0
03F6F F6603E2 (01604)      IOR4R R6,K3
03F71 C62C5D90 (01605)      PUSHMIL R2,DKTAB3(R6)
03F73 506E001C (01606)      MOVKR R6,R7,C321
03F75 E0603E2 (01607)      MOVPM R6,K4
                                ;
03F77 06003ED2 (01608)      CALL R0,GET74
03F79 506E001C (01609)      MOVKR R6,R7,C321
03F7B E0603E03 (01610)      MOVPM R6,K5
03F7D 9A700002 (01611)      ANDIR R7,C0
03F7F 3A73 (01612)          LLS R7,3
03F80 0000 (01613)          EVEN
03F81 E0703E04 (01614)      MOVPM R7,K6
                                ;
03F83 06003F08 (01615)      CALL R0,GET1
03F85 F6703E2 (01616)      IOR4R R7,K4
03F87 C62E5D08 (01617)      PUSHMIL R2,DKTAB4(R7)
                                ;
03F89 06003F08 (01618)      CALL R0,GET1
03F8B F6703E03 (01619)      IOR4R R7,K5
03F8D C62E5D0F (01620)      PUSHMIL R2,DKTAB5(R7)
                                ;
03F8F 06003F08 (01621)      CALL R0,GET1
03F91 F7703E04 (01622)      IOR4R R7,K6
                                ;
03F93 06003FE (01623)      CALL R0,GET2
03F95 E0703E2 (01624)      MOVPM R7,K7
                                ;
03F97 06003ED2 (01625)      CALL R0,GET74
03F99 506E0010 (01626)      MOVKR R6,R7,C32
03F9B 3061 (01627)          ARS R6,1
03F9D 0000 (01628)          EVEN
03F9F F6603E04 (01629)      IOR4R R6,K6
03FA1 C62C5E10 (01630)      PUSHMIL R2,DKTAB6(R6)
03FA3 4C7E (01631)          ADDR R7,R7
03FA5 0000 (01632)          EVEN
03FA7 506E0008 (01633)      MOVKR R6,R7,C2
03FA9 F6603E2 (01634)      IOR4R R6,K7
03FAB C62C5E30 (01635)      PUSHMIL R2,DKTAB7(R6)
03FAD 4C7E (01636)          ADDR R7,R7
03FAF 0000 (01637)          EVEN
03FB1 9A700008 (01638)      ANDIR R7,C2
                                ;
03FB3 C62C5D10 (01639)      PUSHMIL R2,DKTAB1(R6)
03FB5 9A700002 (01640)      ANDIR R7,C0
03FB7 F6703E06 (01641)      IOR4R R7,K2
03FB9 C62E5D50 (01642)      PUSHMIL R2,DKTAB2(R7)
03FBB 06003ED2 (01643)      CALL R0,GET74
03FBD 4C7E (01644)          ADDR R7,R7
03FBE 0000 (01645)          EVEN
03FBC E0703E2 (01646)      MOVPM R7,K3
                                ;
03FBD 06003ED2 (01647)      CALL R0,GET74
03FDF 506E0002 (01648)      MOVKR R6,R7,C0
03FDF F6603E2 (01649)      IOR4R R6,K3
03FE1 C62C5D90 (01650)      PUSHMIL R2,DKTAB3(R6)
03FE3 506E001C (01651)      MOVKR R6,R7,C321
03FE5 E0603E2 (01652)      MOVPM R6,K4
                                ;
03FE7 06003ED2 (01653)      CALL R0,GET74
03FE9 506E001C (01654)      MOVKR R6,R7,C321
03FEB E0603E03 (01655)      MOVPM R6,K5
03FED 9A700002 (01656)      ANDIR R7,C0
03FEF 3A73 (01657)          LLS R7,3
03F00 0000 (01658)          EVEN
03F01 E0703E04 (01659)      MOVPM R7,K6
                                ;
03F03 06003F08 (01660)      CALL R0,GET1
03F05 F6703E2 (01661)      IOR4R R7,K4
03F07 C62E5D08 (01662)      PUSHMIL R2,DKTAB4(R7)
                                ;
03F09 06003F08 (01663)      CALL R0,GET1
03F0B F6703E03 (01664)      IOR4R R7,K5
03F0D C62E5D0F (01665)      PUSHMIL R2,DKTAB5(R7)
                                ;
03F0F 06003F08 (01666)      CALL R0,GET1
03F11 F7703E04 (01667)      IOR4R R7,K6
                                ;
03F13 06003FE (01668)      CALL R0,GET2
03F15 E0703E2 (01669)      MOVPM R7,K7
                                ;
03F17 06003ED2 (01670)      CALL R0,GET74
03F19 506E0010 (01671)      MOVKR R6,R7,C32
03F1B 3061 (01672)          ARS R6,1
03F1D 0000 (01673)          EVEN
03F1F F6603E04 (01674)      IOR4R R6,K6
03F21 C62C5E10 (01675)      PUSHMIL R2,DKTAB6(R6)
03F23 4C7E (01676)          ADDR R7,R7
03F25 0000 (01677)          EVEN
03F27 506E0008 (01678)      MOVKR R6,R7,C2
03F29 F6603E2 (01679)      IOR4R R6,K7
03F2B C62C5E30 (01680)      PUSHMIL R2,DKTAB7(R6)
03F2D 4C7E (01681)          ADDR R7,R7
03F2F 0000 (01682)          EVEN
03F31 9A700008 (01683)      ANDIR R7,C2
                                ;
03F33 C62C5D10 (01684)      PUSHMIL R2,DKTAB1(R6)
03F35 9A700002 (01685)      ANDIR R7,C0
03F37 F6703E06 (01686)      IOR4R R7,K2
03F39 C62E5D50 (01687)      PUSHMIL R2,DKTAB2(R7)
03F3B 06003ED2 (01688)      CALL R0,GET74
03F3D 4C7E (01689)          ADDR R7,R7
03F3F 0000 (01690)          EVEN
03F41 E0703E2 (01691)      MOVPM R7,K3
                                ;
03F43 06003ED2 (01692)      CALL R0,GET74
03F45 506E0002 (01693)      MOVKR R6,R7,C0
03F47 F6603E2 (01694)      IOR4R R6,K3
03F49 C62C5D90 (01695)      PUSHMIL R2,DKTAB3(R6)
03F4B 506E001C (01696)      MOVKR R6,R7,C321
03F4D E0603E2 (01697)      MOVPM R6,K4
                                ;
03F4F 06003ED2 (01698)      CALL R0,GET74
03F51 506E001C (01699)      MOVKR R6,R7,C321
03F53 E0603E03 (01700)      MOVPM R6,K5
03F55 9A700002 (01701)      ANDIR R7,C0
03F57 3A73 (01702)          LLS R7,3
03F58 0000 (01703)          EVEN
03F59 E0703E04 (01704)      MOVPM R7,K6
                                ;
03F5B 06003F08 (01705)      CALL R0,GET1
03F5D F6703E2 (01706)      IOR4R R7,K4
03F5F C62E5D08 (01707)      PUSHMIL R2,DKTAB4(R7)
                                ;
03F61 06003F08 (01708)      CALL R0,GET1
03F63 F6703E03 (01709)      IOR4R R7,K5
03F65 C62E5D0F (01710)      PUSHMIL R2,DKTAB5(R7)
                                ;
03F67 06003F08 (01711)      CALL R0,GET1
03F69 F7703E04 (01712)      IOR4R R7,K6
                                ;
03F6B 06003FE (01713)      CALL R0,GET2
03F6D E0703E2 (01714)      MOVPM R7,K7
                                ;
03F6F 06003ED2 (01715)      CALL R0,GET74
03F71 506E0010 (01716)      MOVKR R6,R7,C32
03F73 3061 (01717)          ARS R6,1
03F75 0000 (01718)          EVEN
03F77 F6603E04 (01719)      IOR4R R6,K6
03F79 C62C5E10 (01720)      PUSHMIL R2,DKTAB6(R6)
03F7B 4C7E (01721)          ADDR R7,R7
03F7D 0000 (01722)          EVEN
03F7F 506E0008 (01723)      MOVKR R6,R7,C2
03F81 F6603E2 (01724)      IOR4R R6,K7
03F83 C62C5E30 (01725)      PUSHMIL R2,DKTAB7(R6)
03F85 4C7E (01726)          ADDR R7,R7
03F87 0000 (01727)          EVEN
03F89 9A700008 (01728)      ANDIR R7,C2
                                ;
03F8B C62C5D10 (01729)      PUSHMIL R2,DKTAB1(R6)
03F8D 9A700002 (01730)      ANDIR R7,C0
03F8F F6703E06 (01731)      IOR4R R7,K2
03F91 C62E5D50 (01732)      PUSHMIL R2,DKTAB2(R7)
03F93 06003ED2 (01733)      CALL R0,GET74
03F95 4C7E (01734)          ADDR R7,R7
03F97 0000 (01735)          EVEN
03F99 E0703E2 (01736)      MOVPM R7,K3
                                ;
03F9B 06003ED2 (01737)      CALL R0,GET74
03F9D 506E0002 (01738)      MOVKR R6,R7,C0
03F9F F6603E2 (01739)      IOR4R R6,K3
03FA1 C62C5D90 (01740)      PUSHMIL R2,DKTAB3(R6)
03FA3 506E001C (01741)      MOVKR R6,R7,C321
03FA5 E0603E2 (01742)      MOVPM R6,K4
                                ;
03FA7 06003ED2 (01743)      CALL R0,GET74
03FA9 506E001C (01744)      MOVKR R6,R7,C321
03FAB E0603E03 (01745)      MOVPM R6,K5
03FAD 9A700002 (01746)      ANDIR R7,C0
03FAF 3A73 (01747)          LLS R7,3
03FB0 0000 (01748)          EVEN
03FB1 E0703E04 (01749)      MOVPM R7,K6
                                ;
03FB3 06003F08 (01750)      CALL R0,GET1
03FB5 F6703E2 (01751)      IOR4R R7,K4
03FB7 C62E5D08 (01752)      PUSHMIL R2,DKTAB4(R7)
                                ;
03FB9 06003F08 (01753)      CALL R0,GET1
03FBB F6703E03 (01754)      IOR4R R7,K5
03FBD C62E5D0F (01755)      PUSHMIL R2,DKTAB5(R7)
                                ;
03FBE 06003F08 (01756)      CALL R0,GET1
03FBF F7703E04 (01757)      IOR4R R7,K6
                                ;
03FC1 06003FE (01758)      CALL R0,GET2
03FC3 E0703E2 (01759)      MOVPM R7,K7
                                ;
03FC5 06003ED2 (01760)      CALL R0,GET74
03FC7 506E0010 (01761)      MOVKR R6,R7,C32
03FC9 3061 (01762)          ARS R6,1
03FCB 0000 (01763)          EVEN
03FCD F6603E04 (01764)      IOR4R R6,K6
03FCE C62C5E10 (01765)      PUSHMIL R2,DKTAB6(R6)
03FCF 4C7E (01766)          ADDR R7,R7
03FD1 0000 (01767)          EVEN
03FD3 506E0008 (01768)      MOVKR R6,R7,C2
03FD5 F6603E2 (01769)      IOR4R R6,K7
03FD7 C62C5E30 (01770)      PUSHMIL R2,DKTAB7(R6)
03FD9 4C7E (01771)          ADDR R7,R7
03FDB 0000 (01772)          EVEN
03FDD 9A700008 (01773)      ANDIR R7,C2
                                ;
03FDF C62C5D10 (01774)      PUSHMIL R2,DKTAB1(R6)
03FE1 9A700002 (01775)      ANDIR R7,C0
03FE3 F6703E06 (01776)      IOR4R R7,K2
03FE5 C62E5D50 (01777)      PUSHMIL R2,DKTAB2(R7)
03FE7 06003ED2 (01778)      CALL R0,GET74
03FE9 4C7E (01779)          ADDR R7,R7
03FEB 0000 (01780)          EVEN
03FED E0703E2 (01781)      MOVPM R7,K3
                                ;
03FED 06003ED2 (01782)      CALL R0,GET74
03F01 506E0002 (01783)      MOVKR R6,R7,C0
03F01 F6603E2 (01784)      IOR4R R6,K3
03F01 C62C5D90 (01785)      PUSHMIL R2,DKTAB3(R6)
03F01 506E001C (01786)      MOVKR R6,R7,C321
03F01 E0603E2 (01787)      MOVPM R6,K4
                                ;
03F01 06003ED2 (01788)      CALL R0,GET74
03F01 506E001C (01789)      MOVKR R6,R7,C321
03F01 E0603E03 (01790)      MOVPM R6,K5
03F01 9A700002 (01791)      ANDIR R7,C0
03F01 3A73 (01792)          LLS R7,3
03F01 0000 (01793)          EVEN
03F01 E0703E04 (01794)      MOVPM R7,K6
                                ;
03F01 06003F08 (01795)      CALL R0,GET1
03F01 F6703E2 (01796)      IOR4R R7,K4
03F01 C62E5D08 (01797)      PUSHMIL R2,DKTAB4(R7)
                                ;
03F01 06003F08 (01798)      CALL R0,GET1
03F01 F6703E03 (01799)      IOR4R R7,K5
03F01 C62E5D0F (01800)      PUSHMIL R2,DKTAB5(R7)
                                ;
03F01 06003F08 (01801)      CALL R0,GET1
03F01 F7703E04 (01802)      IOR4R R7,K6
                                ;
03F01 06003FE (01803)      CALL R0,GET2
03F01 E0703E2 (01804)      MOVPM R7,K7
                                ;
03F01 06003ED2 (01805)      CALL R0,GET74
03F01 506E0010 (01806)      MOVKR R6,R7,C32
03F01 3061 (01807)          ARS R6,1
03F01 0000 (01808)          EVEN
03F01 F6603E04 (01809)      IOR4R R6,K6
03F01 C62C5E10 (01810)      PUSHMIL R2,DKTAB6(R6)
03F01 4C7E (01811)          ADDR R7,R7
03F01 0000 (01812)          EVEN
03F01 506E0008 (01813)      MOVKR R6,R7,C2
03F01 F6603E2 (01814)      IOR4R R6,K7
03F01 C62C5E30 (01815)      PUSHMIL R2,DKTAB7(R6)
03F01 4C7E (01816)          ADDR R7,R7
03F01 0000 (01817)          EVEN
03F01 9A700008 (01818)      ANDIR R7,C2
                                ;
03F01 C62C5D10 (01819)      PUSHMIL R2,DKTAB1(R6)
03F01 9A700002 (01820)      ANDIR R7,C0
03F01 F6703E06 (01821)      IOR4R R7,K2
03F01 C62E5D50 (01822)      PUSHMIL R2,DKTAB2(R7)
03F01 06003ED2 (01823)      CALL R0,GET74
03F01 4C7E (01824)          ADDR R7,R7
03F01 0000 (01825)          EVEN
03F01 E0703E2 (01826)      MOVPM R7,K3
                                ;
03F01 06003ED2 (01827)      CALL R0,GET74
03F01 506E0002 (01828)      MOVKR R6,R7,C0
03F01 F6603E2 (01829)      IOR4R R6,K3
03F01 C62C5D90 (01830)      PUSHMIL R2,DKTAB3(R6)
03F01 506E001C (01831)      MOVKR R6,R7,C321
03F01 E0603E2 (01832)      MOVPM R6,K4
                                ;
03F01 06003ED2 (01833)      CALL R0,GET74
03F01 506E001C (01834)      MOVKR R6,R7,C321
03F01 E0603E03 (01835)      MOVPM R6,K5
03F01 9A700002 (01836)      ANDIR R7,C0
03F01 3A73 (01837)          LLS R7,3
03F01 0000 (01838)          EVEN
03F01 E0703E04 (01839)      MOVPM R7,K6
                                ;
03F01 06003F08 (01840)      CALL R0,GET1
03F01 F6703E2 (01841)      IOR4R R7,K4
03F01 C62E5D08 (01842)      PUSHMIL R2,DKTAB4(R7)
                                ;
03F01 06003F08 (01843)      CALL R0,GET1
03F01 F6703E03 (01844)      IOR4R R7,K5
03F01 C62E5D0F (01845)      PUSHMIL R2,DKTAB5(R7)
                                ;
03F01 06003F08 (01846)      CALL R0,GET1
03F01 F7703E04 (01847)      IOR4R R7,K6
                                ;
03F01 06003FE (01848)      CALL R0,GET2
03F01 E0703E2 (01849)      MOVPM R7,K7
                                ;
03F01 06003ED2 (01850)      CALL R0,GET74
03F01 506E0010 (01851)      MOVKR R6,R7,C32
03F01 3061 (01852)          ARS R6,1
03F01 0000 (01853)          EVEN
03F01 F6603E04 (01854)      IOR4R R6,K6
03F01 C62C5E10 (01855)      PUSHMIL R2,DKTAB6(R6)
03F01 4C7E (01856)          ADDR R7,R7
03F01 0000 (01857)          EVEN
03F01 506E0008 (01858)      MOVKR R6,R7,C2
03F01 F6603E2 (01859)      IOR4R R6,K7
03F01 C62C5E30 (01860)      PUSHMIL R2,DKTAB7(R6)
03F01 4C7E (01861)          ADDR R7,R7
03F01 0000 (01862)          EVEN
03F01 9A700008 (01863)      ANDIR R7,C2
                                ;
03F01 C62C5D10 (01864)      PUSHMIL R2,DKTAB1(R6)
03F01 9A700002 (01865)      ANDIR R7,C0
03F01 F6703E06 (01866)      IOR4R R7,K2
03F01 C62E5D50 (01867)      PUSHMIL R2,DKTAB2(R7)
03F01 06003ED2 (01868)      CALL R0,GET74
03F01 4C7E (01869)          ADDR R7,R7
03F01 0000 (01870)          EVEN
03F01 E0703E2 (01871)      MOVPM R7,K3
                                ;
03F01 06003ED2 (01872)      CALL R0,GET74
03F01 506E0002 (01873)      MOVKR R6,R7,C0
03F01 F6603E2 (01874)      IOR4R R6,K3
03F01 C62C5D90 (01875)      PUSHMIL R2,DKTAB3(R6)
03F01 506E001C (01876)      MOVKR R6,R7,C321
03F01 E0603E2 (01877)      MOVPM R6,K4
                                ;
03F01 06003ED2 (01878)      CALL R0,GET74
03F01 506E001C (01879)      MOVKR R6,R7,C321
03F01 E0603E03 (01880)      MOVPM R6,K5
03F01 9A700002 (01881)      ANDIR R7,C0
03F01 3A73 (01882)          LLS R7,3
03F01 0000 (01883)          EVEN
03F01 E0703E04 (01884)      MOVPM R7,K6
                                ;
03F01 06003F08 (01885)     
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PAGE 40: [BBN-TENEXD]<MAP>880105.WS0.96, 31-Dec-79 13:53:28, Ed: WOLF  
CORRECT(AB): UNBITSTREAM, ERROR-CORRECT, AND DECODE

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03E82 E0703E03 (01512) MOVHM R7,K8 ;AND SAVE IT
03E84 06003EFE (01513) ;
03E86 F6703E03 (01514) CALL R0,GET2 ;GET LO 2 OF K8
03E88 C62E5E40 (01515) IORHR R7,K8
03E8A 9C50003R (01516) PUSHMIL R2,DKTAB8(R7) ;DECODE AND STORE K8
03E8C 301C (01517) ;
03E8E 507C0001 (01518) MOVIR R5,60-1 ;NUMBER-1 OF BASEBAND SAMPLES
03E90 0800 (01519) #1: POPXI R1,R6 ;GET BB DATA BIT FROM STREAM
03E92 4C7E (01520) EVEN
03E94 507C0001 (01521) MOVKR R7,R6,1 ;THIS CODE IS SAME AS GET3)
03E96 4C7E (01522) ADDR R7,R7 ;LEFT SHIFT 1 TO MAKE ROOM FOR NEXT
03E98 301C (01523) POPXI R1,R6
03E9A 4C7E (01524) IOKR R7,R6,1
03E9C 301C (01525) ADDR R7,R7
03E9E 567C0001 (01526) POPXI R1,R6
03EA0 4C7E (01527) IOKR R7,R6,1
03EA2 301C (01528) ADDR R7,R7
03EA4 567C0001 (01529) EVEN ;CONVERT TO FULLWORD INDEX
03EA6 4C7E (01530) PUSHMIL R2,08TAB(R7) ;DECODE AND STORE BB SAMPLE
03EA8 0800 (01531) DJP R5,#1 ;DONE YET?
03EAA 8C503E8C (01532) MOVLM CLR+G1,SYSSFLGS
03EAC F00FFCE (01533) RETURN ;YES1
03EAE 0E70 (01534) ;
03EB0 (01535) ;THESE UNBITSTREAMING ROUTINES RETURN THEIR VALUES IN R7 LEFT
03EB2 (01536) ; SHIFTED ONE PLACE, SO THAT THEY CAN ACT AS FULL-WORD OFFSETS INTO
03EB4 (01537) ; THE DECODING TABLES.
03EB6 (01538) ;
03EB8 (01539) ;PULL IN 7 SUCCESSIVE WORDS FROM THE BITSTREAM AND FORM THEIR DATA
03EBA (01540) ; BITS INTO A 7-BIT VALUE. THIS VALUE IS USED AS AN INDEX INTO
03EBC (01541) ; CORTAB TO YIELD AN ERROR-CORRECTED 4-BIT VALUE. IF RMOCOR IS NZ,
03EBE (01542) ; OMIT THE ERROR-CORRECTION AND RETURN ONLY THE 4 INFORMATION BITS.
03EC0 (01543) ; R1 = PTR-1 TO BITSTREAM
03EC2 (01544) ; R6 = SCRATCH
03EC4 (01545) ; R7 = HOLDS RETURNED VALUE
03EC6 (01546) ;THIS CAN BE CALLED WITH CALL R0,GET74.
03EC8 (01547) EVEN
03ECA (01548) GET74: POPXI R1,R6 ;GET MODEM WORD INTO R6
03ECB (01549) EVEN
03ECD (01550) MOVKR R7,R6,1 ;EXTRACT DATA BIT TO R7
03ECE (01551) ADDR R7,R7 ;SHIFT LEFT 1 FASTER THAN LLS
03EE0 567C0001 (01552) POPXI R1,R6
03EE2 4C7E (01553) IOKR R7,R6,1
03EE4 301C (01554) ADDR R7,R7
03EE6 567C0001 (01555) POPXI R1,R6
03EE8 301C (01556) IOKR R7,R6,1
03EEA 4C7E (01557) ADDR R7,R7
03EEC 567C0001 (01558) POPXI R1,R6
03EEE 4C7E (01559) IOKR R7,R6,1
03EEF 301C (01560) ADDR R7,R7
03EF0 567C0001 (01561) POPXI R1,R6
03EF2 4C7E (01562) IOKR R7,R6,1
03EF4 567C0001 (01563) ADDR R7,R7
03EF6 4C7E (01564) POPXI R1,R6
03EF8 301C (01565)

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PAGE 41: [BBN-TENEXDJ<MAP>8BMIDS.MSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
CORRECT(AB): UNBITSTREAM, ERROR-CORRECT, AND DECODE

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03EE0 567C0001 (01565) IOKR R7,R6,1
03EEA 4C7E (01566) ADDR R7,R7
03EEB 301C (01567) POPXI R1,R6
03EEC 567C0001 (01568) IOKR R7,R6,1
03EEE E200057D (01569) CPMNZ RNOCOR
03EEF 81103EF6 (01570) JMP NOCORR,MEZ
03EF2 F07E3F0A (01571) ERCORR: MOVNR R7,CORRAB(R7)
03EF4 0E7A (01572) RETURN
03EF5 0000 (01573) ;
03EF6 506E0010 (01574) EVEN
03EF8 4C7E (01575) NOCORR: MOVNR R6,R7,C3
03EF9 0000 (01576) ADDR R7,R7
03EFA 566E000E (01577) EVEN
03EFB 407C (01578) IOKR R6,R7,C210
03EFC 0E70 (01579) MOVNR R7,R6
03EFD 0E70 (01580) RETURN
03E0E 301C (01581) ;
03E0F 0000 (01582) ;GET1 AND GET2 UNBITSTREAM 1 AND 2 BITS, ALSO RETURNING RESULT
03E10 507C0001 (01583) ; LEFT-SHIFTED ONE BIT.
03E11 4C7E (01584) EVEN
03E12 301C (01585) GET2: POPXI R1,R6
03E13 0000 (01586) EVEN
03E14 4C7E (01587) MOVNR R7,R6,1
03E15 301C (01588) ADDR R7,R7
03E16 567C0001 (01589) GT1: POPXI R1,R6
03E17 4C7E (01590) IOKR R7,R6,1
03E18 0E70 (01591) ADDR R7,R7
03E19 0E70 (01592) RETURN
03E20 0E70 (01593) ;
03E21 0000 (01594) EVEN
03E22 0000 (01595) GET1: CLR R7
03E23 2107 (01596) NOP GT1
03E24 0000 (01597) ;
03E25 0000 (01598) ;HAMMING 7/4 CODE CORRECTION TABLE. INDEX TABLE WITH 7-BIT RECEIVED
03E26 0000 (01599) ; CODEWORD; TABLE VALUE IS THE CORRECTED 4-BIT VALUE,
03E27 0000 (01600) ; LEFT SHIFTED 1 BIT (TO BE USED AS A FULL-WORD OFFSET IN THE DECODING
03E28 0000 (01601) ; TABLES).
03E29 0000 (01602) CORRAB:
03E30 0000 (01603) DATA 0'00'22 ;RCVD 000
03E31 0000 (01604) DATA 0'00'22 ;RCVD 001
03E32 0000 (01605) DATA 0'00'22 ;RCVD 002
03E33 0000 (01606) DATA 0'03'22 ;RCVD 003
03E34 0000 (01607) DATA 0'00'22 ;RCVD 004
03E35 0000 (01608) DATA 0'05'22 ;RCVD 005
03E36 0000 (01609) DATA 0'16'22 ;RCVD 006
03E37 0000 (01610) DATA 0'07'22 ;RCVD 007
03E38 0000 (01611) DATA 0'00'22 ;RCVD 010
03E39 0000 (01612) DATA 0'11'22 ;RCVD 011
03E40 0000 (01613) DATA 0'02'22 ;RCVD 012
03E41 0000 (01614) DATA 0'07'22 ;RCVD 013
03E42 0000 (01615) DATA 0'04'22 ;RCVD 014
03E43 0000 (01616) DATA 0'07'22 ;RCVD 015
03E44 0000 (01617) DATA 0'07'22 ;RCVD 016

```

03F19 000E	(01618)	DATA 0'07**2	JRCVD 017
03F1A 0000	(01619)	DATA 0'00**2	JRCVD 020
03F1B 0012	(01620)	DATA 0'11**2	JRCVD 021
03F1C 001C	(01621)	DATA 0'16**2	JRCVD 022
03F1D 0016	(01622)	DATA 0'13**2	JRCVD 023
03F1E 001A	(01623)	DATA 0'16**2	JRCVD 024
03F1F 001A	(01624)	DATA 0'15**2	JRCVD 025
03F20 001C	(01625)	DATA 0'16**2	JRCVD 026
03F21 001C	(01626)	DATA 0'16**2	JRCVD 027
03F22 0012	(01627)	DATA 0'11**2	JRCVD 030
03F23 0012	(01628)	DATA 0'11**2	JRCVD 031
03F24 0014	(01629)	DATA 0'12**2	JRCVD 032
03F25 0012	(01630)	DATA 0'11**2	JRCVD 033
03F26 0018	(01631)	DATA 0'14**2	JRCVD 034
03F27 0012	(01632)	DATA 0'11**2	JRCVD 035
03F28 001C	(01633)	DATA 0'16**2	JRCVD 036
03F29 000E	(01634)	DATA 0'07**2	JRCVD 037
03F2A 0000	(01635)	DATA 0'00**2	JRCVD 040
03F2B 000A	(01636)	DATA 0'05**2	JRCVD 041
03F2C 0004	(01637)	DATA 0'02**2	JRCVD 042
03F2D 0016	(01638)	DATA 0'13**2	JRCVD 043
03F2E 000A	(01639)	DATA 0'05**2	JRCVD 044
03F2F 000A	(01640)	DATA 0'05**2	JRCVD 045
03F30 000C	(01641)	DATA 0'06**2	JRCVD 046
03F31 000A	(01642)	DATA 0'05**2	JRCVD 047
03F32 0004	(01643)	DATA 0'02**2	JRCVD 050
03F33 0002	(01644)	DATA 0'01**2	JRCVD 051
03F34 0004	(01645)	DATA 0'02**2	JRCVD 052
03F35 0004	(01646)	DATA 0'02**2	JRCVD 053
03F36 0018	(01647)	DATA 0'14**2	JRCVD 054
03F37 000A	(01648)	DATA 0'05**2	JRCVD 055
03F38 0004	(01649)	DATA 0'02**2	JRCVD 056
03F39 000E	(01650)	DATA 0'07**2	JRCVD 057
03F3A 0010	(01651)	DATA 0'10**2	JRCVD 060
03F3B 0016	(01652)	DATA 0'13**2	JRCVD 061
03F3C 0016	(01653)	DATA 0'13**2	JRCVD 062
03F3D 0016	(01654)	DATA 0'13**2	JRCVD 063
03F3E 0018	(01655)	DATA 0'14**2	JRCVD 064
03F3F 000A	(01656)	DATA 0'05**2	JRCVD 065
03F40 001C	(01657)	DATA 0'16**2	JRCVD 066
03F41 0016	(01658)	DATA 0'13**2	JRCVD 067
03F42 0018	(01659)	DATA 0'14**2	JRCVD 070
03F43 0012	(01660)	DATA 0'11**2	JRCVD 071
03F44 0004	(01661)	DATA 0'02**2	JRCVD 072
03F45 0016	(01662)	DATA 0'13**2	JRCVD 073
03F46 0018	(01663)	DATA 0'14**2	JRCVD 074
03F47 0018	(01664)	DATA 0'14**2	JRCVD 075
03F48 0018	(01665)	DATA 0'14**2	JRCVD 076
03F49 001E	(01666)	DATA 0'17**2	JRCVD 077
03F4A 0000	(01667)	DATA 0'00**2	JRCVD 100
03F4B 0006	(01668)	DATA 0'03**2	JRCVD 101
03F4C 0006	(01669)	DATA 0'03**2	JRCVD 102
03F4D 0006	(01670)	DATA 0'03**2	JRCVD 103

PAGE 43: I88N-TEWEXD3<4AP>88NI05.M50.96, 31-Dec-79 13:53:28, Ed: WOLF  
CORRECT(A8): UNBITSTREAM, ERROR-CORRECT, AND DECODE

03F4E 0008	(01671)	DATA 0'04'*2	RCVD 104
03F4F 001A	(01672)	DATA 0'15'*2	RCVD 105
03F50 000C	(01673)	DATA 0'06'*2	RCVD 106
03F51 0006	(01674)	DATA 0'03'*2	RCVD 107
03F52 0008	(01675)	DATA 0'04'*2	RCVD 110
03F53 0002	(01676)	DATA 0'01'*2	RCVD 111
03F54 0014	(01677)	DATA 0'12'*2	RCVD 112
03F55 0006	(01678)	DATA 0'03'*2	RCVD 113
03F56 0008	(01679)	DATA 0'04'*2	RCVD 114
03F57 0008	(01680)	DATA 0'04'*2	RCVD 115
03F58 0008	(01681)	DATA 0'07'*2	RCVD 116
03F59 000E	(01682)	DATA 0'10'*2	RCVD 117
03F5A 0010	(01683)	DATA 0'15'*2	RCVD 120
03F5B 001A	(01684)	DATA 0'12'*2	RCVD 121
03F5C 0014	(01685)	DATA 0'03'*2	RCVD 122
03F5D 0006	(01686)	DATA 0'15'*2	RCVD 123
03F5E 001A	(01687)	DATA 0'15'*2	RCVD 124
03F5F 001A	(01688)	DATA 0'15'*2	RCVD 125
03F60 001C	(01689)	DATA 0'16'*2	RCVD 126
03F61 001A	(01690)	DATA 0'15'*2	RCVD 127
03F62 0014	(01691)	DATA 0'12'*2	RCVD 130
03F63 0012	(01692)	DATA 0'11'*2	RCVD 131
03F64 0014	(01693)	DATA 0'12'*2	RCVD 132
03F65 0014	(01694)	DATA 0'12'*2	RCVD 133
03F66 0008	(01695)	DATA 0'04'*2	RCVD 134
03F67 001A	(01696)	DATA 0'15'*2	RCVD 135
03F68 0014	(01697)	DATA 0'12'*2	RCVD 136
03F69 001E	(01698)	DATA 0'17'*2	RCVD 137
03F6A 0010	(01699)	DATA 0'10'*2	RCVD 140
03F6B 0002	(01700)	DATA 0'01'*2	RCVD 141
03F6C 000C	(01701)	DATA 0'06'*2	RCVD 142
03F6D 0006	(01702)	DATA 0'03'*2	RCVD 143
03F6E 000C	(01703)	DATA 0'06'*2	RCVD 144
03F6F 000A	(01704)	DATA 0'05'*2	RCVD 145
03F70 000C	(01705)	DATA 0'06'*2	RCVD 146
03F71 000C	(01706)	DATA 0'06'*2	RCVD 147
03F72 0002	(01707)	DATA 0'01'*2	RCVD 150
03F73 0002	(01708)	DATA 0'01'*2	RCVD 151
03F74 0004	(01709)	DATA 0'02'*2	RCVD 152
03F75 0002	(01710)	DATA 0'01'*2	RCVD 153
03F76 0008	(01711)	DATA 0'04'*2	RCVD 154
03F77 0002	(01712)	DATA 0'01'*2	RCVD 155
03F78 000C	(01713)	DATA 0'06'*2	RCVD 156
03F79 001E	(01714)	DATA 0'17'*2	RCVD 157
03F7A 0010	(01715)	DATA 0'10'*2	RCVD 160
03F7B 0010	(01716)	DATA 0'10'*2	RCVD 161
03F7C 0010	(01717)	DATA 0'10'*2	RCVD 162
03F7D 0016	(01718)	DATA 0'13'*2	RCVD 163
03F7E 0010	(01719)	DATA 0'10'*2	RCVD 164
03F7F 001A	(01720)	DATA 0'15'*2	RCVD 165
03F80 000C	(01721)	DATA 0'06'*2	RCVD 166
03F81 001E	(01722)	DATA 0'17'*2	RCVD 167
03F82 0010	(01723)	DATA 0'10'*2	RCVD 170

PAGE 44: CBBN-TENEXDJ<MAP>8BNIDS.WSD.96, 31-Dec-79 13:53:28, Ed: WOLF  
 CORRECT(AB): UNBITSTREAM, ERROR-CORRECT, AND DECODE

03F83 0002	(01724)	DATA 0'01'*2	;RCVD 171
03F84 0014	(01725)	DATA 0'12'*2	;RCVD 172
03F85 001E	(01726)	DATA 0'17'*2	;RCVD 173
03F86 0018	(01727)	DATA 0'14'*2	;RCVD 174
03F87 001E	(01728)	DATA 0'17'*2	;RCVD 175
03F88 001E	(01729)	DATA 0'17'*2	;RCVD 176
03F89 001E	(01730)	DATA 0'17'*2	;RCVD 177
	(01731)		

PAGE 45: [BBN-TENEXD]<MAP>BBN105.M50-96, 31-Dec-79 13:53:28, Ed: WOLF  
 MPCSC -- G-FLAG SET/CLEAR

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(01732) MPCSC -- G-FLAG SET/CLEAR
(01733) ;JWM, 18 OCT 79
(01734) ;
(01735) ;FCB # 123
(01736) ; G-FLAG NUMBER IN 1(R1)
(01737) ; 0/1 = CLEAR/SET IN 2(R1)
(01738) EVEN
(01739) MPCSC: MOVNR R2,1(R1) ;GET G-FLAG NUMBER
(01740) INCR R2,4 ;ADD OFFSET FOR SYSSFLGS
(01741) MOVNR R3,2(R1) ;SET CLR/SET BIT
(01742) LLS R3,5 ;SHIFT TO PROPER POSITION
(01743) IORR R2,R3,S28 ;AND OFF AND PUT IN R2
(01744) MOVNR R2,SYSSFLGS ;DO THE MOVE THAT SETS/CLRS IT
(01745) RETURN
(01746) ;
(01747) SPARE = $3FFE - #L ;ROOM TO SPARE IN THIS "HOLE"
(01748) ;
(01749) ;
(01750) ;"BREAKPOINT" FOR DEBUGGING. TO HALT CSPU EXECUTION AND SAVE
(01751) ; REGISTERS ON THE STACK, PATCH A "CALL R1,BKPT" INTO THE PROGRAM
(01752) ; WITH MPOOK:
(01753) ; LOC
(01754) ; $0610,
(01755) ; $3FFE
(01756) ;
(01757) BKPT: JMP BKPT
(01758) ;
(01759) ;END OF FILE
  
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PAGE 46: [BBN-TENEXD]CHAP888NIOS.MSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
 MPSC -- G-FLAG SET/CLEAR

ACQTHR:	0000A (00010) (00917)	
ADSGN:	048A2 (00397) (00419)	
ADSCPYA:	0498A (00530) (00583)	
ADSCPYB:	04994 (00539) (00590)	
AD\$DISC:	04986 (00553) (00571)	
AD\$LOOP:	00004 (00403) (00415)	(00572)
ADSRIN:	04982 (00550) (00565)	
AD\$STOP:	00015 (00400) (00416)	
AD\$SZ:	0002C (00395) (00419)	
ADASEND:	0202B (00380) (00407)	
ADAMINT:	0495C (00106) (00135)	(00545)
AD\$SEND:	0200F (00381) (00414)	
ADBPTR:	04952 (00536) (00554)	
ADCNTRL:	00002 (00383) (00426)	
ADPO:	00542 (00221) (00232)	(00546) (00548)
ADMSNRDY:	03CA0 (01009) (01018)	
ADMSRIN:	03C9C (01005) (01014)	(01021)
ADMINT:	03C00 (00109) (00129)	(01000)
AV\$BGN:	04922 (00497) (00506)	
AV\$SZ:	00010 (00495) (00506)	
AVCNTRL:	00001 (00493) (00513)	
B0TAB:	05000 (00026) (00026)	(01530)
BITS15:	07FFF (00013) (00295)	(00286) (00287) (00288) (00289) (00380) (00381) (00437) (00438)
BAPT:	03F95 (01757) (01757)	
B007:	03078 (01240) (01245)	
C0:	00002 (00016) (00020)	(01460) (01470) (01479)
C1:	00004 (00017) (00020)	(00022) (01455)
C2:	00008 (00018) (00020)	(00021) (00022) (01450) (01506) (01511)
C210:	0000E (00020) (01578)	
C3:	00010 (00019) (00021)	(00022) (01429) (01575)
C32:	00018 (00021) (01499)	
C321:	0001C (00022) (01473)	(01477)
CLKGO:	00002 (00313) (00315)	
CLKRATES:	00006 (00293) (00315)	
CLKSET:	00000 (00313)	
CLR:	00000 (00046) (00565)	(00630) (00707) (01014) (01233) (01532)
CORRECT:	03E08 (00104) (01393)	
CORTAB:	03F0A (01571) (01602)	
D16\$INT1:	04022 (00038) (00116)	
D16\$INT2:	04030 (00039) (00122)	
D22\$INT1:	040FA (00040) (00120)	
D23\$INT1:	0411E (00041) (00134)	
DASBGN:	040E2 (00453) (00473)	
DASCPYA:	03CA0 (00091) (01024)	
DASCPYB:	03CB2 (00092) (01031)	
DASLOOP:	00001 (00455) (00469)	
DASSTOP:	00015 (00461) (00470)	
DAS\$SZ:	0002C (00451) (00473)	
DAA\$END:	03BE9 (00437) (00460)	
DAB\$END:	03C9D (00438) (00468)	
DABPTR:	03C76 (00092) (01010)	(01020)
DACKTRL:	00002 (00440) (00480)	
DAPO:	00549 (00231) (01001)	(01003)

PAGE 47: [88N-TENEXDJ<MAP>BBMIOS.MSD.96, 31-Dec-79 13:53:28, Ed: WOLF  
MPGSC -- G-FLAG SET/CLEAR

DKTAB1:	0501E (00026) (00027) (01459)	
DKTAB2:	05050 (00027) (00028) (01462)	
DKTAB3:	0509E (00028) (00029) (01472)	
DKTAB4:	050D0 (00029) (00030) (01486)	
DKTAB5:	050F0 (00030) (00031) (01490)	
DKTAB6:	05E10 (00031) (00032) (01503)	
DKTAB7:	05E30 (00032) (00033) (01508)	
DKTAB8:	05E40 (00033) (00034) (01516)	
DTQTAB:	05E00 (00035) (01447)	
EDTAB:	05E50 (00034) (00035) (01454)	
ERCORR:	03EF2 (01571)	
ERRPTR:	038DE (00007) (00021) (00023)	
ERRSYNC:	038DF (00028) (00924) (00958) (00965)	
FOTS:	007E8 (00043) (00102)	
GA:	00004 (00047)	
G1:	00005 (00048) (01103) (01393) (01532)	
G2:	00006 (00049) (00545) (00565) (00623) (00638) (00726) (00787) (01000) (01014)	
G3:	00007 (00050)	
GAIN:	03E04 (01392) (01398) (01410) (01416) (01453)	
GET1:	03F08 (01412) (01415) (01484) (01488) (01492) (01595)	
GET2:	03EFE (01495) (01514) (01585)	
GET74:	03ED2 (01399) (01404) (01407) (01418) (01423) (01428) (01464) (01469) (01476) (01498)	
GHIST:	00CEE (00055) (00056) (01123)	
GT1:	03F03 (01589) (01596)	
RS:	00001 (00007) (00054) (00055) (00056) (00057) (00058) (00059) (00060) (00061) (00062)	
R00:	03DF2 (01271) (01345)	
R017:	03DEB (01270) (01338)	
R026:	03DCE (01285) (01309)	
R031:	03DD4 (01280) (01315)	
R045:	03DCC (01276) (01307)	
R052:	03DE4 (01273) (01331)	
R063:	03DD5 (01282) (01316)	
R074:	03DED (01283) (01340)	
R103:	03DE9 (01274) (01336)	
R114:	03DD3 (01275) (01314)	
R125:	03DE3 (01284) (01330)	
R132:	03DE0 (01281) (01327)	
R146:	03DD2 (01277) (01313)	
R151:	03DCA (01272) (01305)	
R160:	03DEF (01279) (01342)	
R177:	03DDA (01286) (01321)	
WPTR:	03D92 (01243) (01271)	
IOWSC5:	00010 (00069)	
IOWSDH:	00000 (00070)	
IOWSIC:	00020 (00068)	
IOWSONHK:	00002 (00066)	
IOWSRO:	00001 (00073) (00080) (00092) (00927) (00967)	
IOWSRR:	00004 (00071)	
IOWSSI:	00040 (00067)	
IOWSSO:	00002 (00072)	
ISVTS:	00502 (00075) (00203) (00204) (00205) (00206) (00207) (00208) (00209) (00210) (00221)	



PAGE 48: [BBN-TENEXD]<MAP>BBNIO5.WSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
 MPSC -- G-FLAG SET/CLEAR

F1:	(00222)	(00224)	(00225)	(00227)	(00228)	(00230)	(00231)	(00237)	(00238)	(00239)
K1HIST:	(00240)	(00244)	(00249)	(00250)	(00251)	(00252)	(00253)	(00254)	(00261)	(00263)
K2:	(00265)									
K2HIST:	(01383)	(01421)	(01458)							
K3:	(00056)	(00057)	(01145)							
K3HIST:	(01394)	(01426)	(01461)							
K4:	(00058)	(01153)								
K4HIST:	(01385)	(01467)	(01471)							
K5:	(00059)	(01165)								
K5HIST:	(01386)	(01474)	(01485)							
K6:	(00060)	(01173)								
K6HIST:	(01387)	(01478)	(01489)							
K7:	(00061)	(01182)								
K7HIST:	(01388)	(01482)	(01493)							
K8:	(00062)	(01186)								
K8HIST:	(01389)	(01496)	(01507)							
LASTERR:	(00063)	(01202)								
LOSETHR:	(01390)	(01512)								
MBLENGTH:	(00064)									
MLC:	(00955)	(00959)	(00964)							
MOD\$BGN:	(00152)	(00661)								
MOD\$INIT:	(00311)	(00366)								
MOD\$LOOP:	(00316)	(00319)								
MOD\$RA:	(00323)	(00325)								
MOD\$RB:	(00360)	(00362)								
MOD\$RC:	(00319)	(00332)								
MOD\$RCVR:	(00330)	(00334)								
MOD\$S2:	(00331)	(00340)								
MOD\$TA:	(00323)	(00329)								
MOD\$TB:	(00309)	(00366)								
MOD\$XMT:	(00354)									
MPSC:	(00352)	(00359)								
MEMMAX:	(00324)	(00352)								
MODCOR:	(00105)	(01739)								
MODMAX:	(00898)	(01575)								
MOD\$RS:	(01570)									
MOD\$SD:	(00097)	(00904)								
MOD\$SR:	(00082)	(00091)								
MOD\$TR:	(00084)									
OLD\$YNC:	(00083)									
OUT2:	(00080)	(00081)								
OUT3:	(00083)	(00928)								
OUT7:	(01197)	(01221)								
P8:	(01250)									
P1:	(01127)	(01149)	(01157)	(01161)	(01169)	(01178)	(01193)	(01217)		
P2:	(01240)									
P3:	(01289)	(01346)								
	(01290)	(01319)								
	(01291)	(01310)								
	(01292)	(01328)								

PAGE 49: [BBN-TENEXD]<MAP>BBN105.WSO.96, 31-Dec-79 13:53:28, Ed: WOLF  
 MPCSC --- G-FLAG SET/CLEAR

P4:	030F1 (01293) (01344)		
P5:	030C0 (01294) (01306)		
P6:	030D6 (01295) (01317)		
P7:	030D8 (01296) (01322)		
PHIST:	00C9E (00053) (00054)	(01110)	
PITCH:	03E02 (01300) (01385)	(01386) (01389) (01402) (01413) (01432)	
PITCL:	0000A (00006) (01433)	(01435)	
PITCRU:	0004C (00007) (01436)	(01438)	
PRBLP:	0305E (01224) (01232)		
PROTECT:	03CC4 (00103) (01103)		
PIR2:	030C2 (01262) (01299)		
PIR3:	030B2 (01220) (01252)	(01289)	
Q0:	030F4 (01299) (01347)		
Q1:	030D9 (01300) (01320)		
Q2:	030D0 (01301) (01311)		
Q3:	030E2 (01302) (01329)		
RANDSL:	00BE0 (00008) (00020)	(00023)	
RANDSO:	01C77 (00009) (00018)		
ROFO:	00553 (00254) (00756)	(00762) (00766) (00922) (00949)	
RTA:	0AA6C (00158) (01373)		
RTB:	0AB72 (00159) (01374)		
RTBPTR:	030FC (00759) (00853)	(01374) (01395)	
RTFA:	00538 (00207) (00213)	(00710)	
RTFB:	0053C (00200) (00711)		
RTFPT:	03850 (00711) (00748)	(00772)	
RTPO:	00547 (00220) (00747)	(00774) (00851)	
RDABA:	00B36 (00182) (00437)	(00456) (01025) (01026) (01027)	
RDABB:	00B2A (00183) (00438)	(00464) (01032) (01033) (01034)	
RERSIM:	0057F (00263) (00737)		
RFRCTR:	0054F (00250) (00775)		
RLSCTR:	00550 (00251) (00773)		
RHSEND:	03354 (00205) (00333)		
RHSEND:	034EA (00206) (00339)		
RHCEND:	035F0 (00207) (00345)		
RMDMA:	002E0 (00172) (00205)	(00320) (00347) (00714)	
RMDMB:	003E6 (00173) (00206)	(00335) (00715)	
RMDMC:	004EC (00174) (00207)	(00341) (00713) (00716)	
RMFDC:	0054C (00239) (00793)		
RH18M01:	03098 (00760) (00765)		
RH18M02:	030A2 (00764) (00771)		
RH18M03:	0308E (00749) (00793)		
RH18M04:	030B2 (00746) (00783)		
RH18M05:	030BA (00733) (00776)	(00780) (00787) (00794)	
RH18M06:	030C2 (00739) (00805)		
RH18M07:	030AE (00742) (00779)		
RH18M08:	030B8 (00743) (00786)		
RH18M09:	030A4 (00767) (00772)		
RH18M10:	030SA (00100) (00123)	(00725)	
RH18M11:	00546 (00227) (00727)	(00731)	
RH18M12:	03050 (00716) (00734)	(00753) (00783) (00785)	
RH18M13:	00570 (00261) (00266)	(01569)	
RH18M14:	00551 (00252) (00555)	(00736)	
RH18M15:	03C7A (00995) (01007)		

PAGE 50: CBBN-TENEXIOJ<4AP>BBNIO5.MSO.96, 31-Dec-79 13:53:20, Ed: WOLF  
 MPCSC -- G-FLAG SET/CLEAR

RSIFPR:	03C7E (00997) (01008) (01011)		
RSIFA:	00530 (00209) (00996)		
RSNFB:	0053E (00210) (00997)		
RSNKA:	0091A (00179) (00994)		
RSNKB:	009CE (00180) (00995)		
RSNKC:	00A02 (00181) (01019)		
RSNKR:	00540 (00240) (01010)		
RSNPO:	00548 (00230) (01006)		
RSRA:	007FE (00177) (00849)		
RSRB:	0088C (00178) (00850)		
RSR8PTR:	03E00 (01377) (01397)		
RSSPF:	005F2 (00175) (00845)	(00893) (00910)	
RSSSS:	006F0 (00176) (00860)	(00863) (00895) (00896) (00898) (00909)	
RSYMC:	00552 (00253) (00740)	(00921) (00932) (00972)	
RUN:	00540 (00244) (00246)	(00627) (00732) (01004)	
RUNMOD:	00001 (00314)		
SBLNGTH:	00004 (00149) (00150)	(00380) (00381) (00437) (00438)	
SET:	00020 (00045) (00545)	(00623) (00726) (01000) (01393)	
SI\$BMO:	03C00 (00856) (00859)		
SI\$LOOP:	03B05 (00844) (00847)		
SL6:	0001E (00150) (00583)	(00585) (00586) (00590) (00592) (00593) (01024) (01026) (01027)	
	(01031) (01033) (01034)		
SPARE:	00069 (01747)		
SS\$ALT:	03C16 (00895)		
SS\$LOOP:	03C10 (00892) (00913)		
SS\$LPST:	03C20 (00901) (00904)	(00906) (00910)	
SS\$NO:	03C40 (00910) (00920)	(00932)	
SS\$NOALT:	03C26 (00894) (00909)		
SUS\$ELF:	03C5C (00956) (00959)		
SUS\$ERR:	03C60 (00954) (00963)		
SUS\$LOSE:	03C6E (00966) (00972)		
SUS\$VALID:	03C60 (00960) (00967)		
SYMCINIT:	03B02 (00779) (00784)	(00842)	
SYMC\$RCH:	03C0C (00706) (00809)		
SYNCUPDA:	03C4C (00744) (00949)		
SYSSFLCS:	1FFCE (00090) (00545)	(00565) (00623) (00630) (00726) (00787) (01014) (01103)	
	(01233) (01393)	(01532) (01744)	
TAD9A:	0AC78 (00161) (00380)	(00403) (00535)	
TAD8B:	0AD2C (00162) (00381)	(00410) (00536)	
TAD8C:	0ADE0 (00163) (00557)		
TAD\$DC:	0054A (00237) (00256)		
TAP:	03E03 (01381) (01387)	(00571)	
T8TA:	0A75A (00155) (01100)	(01390) (01405) (01446)	
T8T8:	0A600 (00156) (01101)		
T8T8PTR:	03CC2 (00632) (01101)	(01106)	
T8TC:	0A966 (00157) (00646)		
T8TFA:	00536 (00285) (00616)		
T8TFB:	00537 (00206) (00617)		
T8TFTPR:	03802 (00617) (00630)	(00635)	
T8TPO:	00544 (00224) (00629)	(00637)	
TFRCTR:	0054E (00249) (00564)		
THIST:	00C0E (00054) (00055)	(01118)	
THASEMO:	03108 (00200) (00355)		

[illegible]

LINES WITH ERRORS: 0 (MAP VERSION 800101.10) E- 0

## TABLE OF CONTENTS

NRN SPEECH CODE - PATCHES TO MAP-300 SNAP II EXEC PAGE 2  
PAGE 1: CBBN-TNEXD1CHAP38BNPAT.MSD-4 0-Jan-80 1855:30, Ed: KFIELD

PAGE (00001) ;[BBN-TENEXD]<MAP>BBNPAT.MSD.4, 8-Jan-80 18:55:30, Ed: KFIELD

2: [BBN-TENEXD]<MAP>BBNPAT.MSD.4, 8-Jan-80 18:55:30, Ed: KFIELD

BBN SPEECH CODER - PATCHES TO MAP-300 SNAPII EXEC

\* (000062) \* "BBN SPEECH CODER - PATCHES TO MAP-300 SNAP/II EXEC  
\* (000063) \*  
\* (000064) \* THIS PATCH FILE ASSUMES MAP HAS BEEN LOADED WITH  
\* (000065) \* SNAP/II EXEC, RELEASE 3-5.  
\* (000066) \*  
\* (000067) \* THIS PATCH ASSUMES REV. 18 (OR LATER) MICROCODE.  
\* (000068) \*  
\* (000069) \* PATCH PREBINDING ROUTINES TO WORK WITH PREBINDING  
\* (000070) \* BUFFERS ABOVE \$70FF.  
\* (000071) \* (NOTE: THIS PATCH USES PATCH SPACE FROM \$1C90 - \$1CA3.)  
\* (000072) \*  
\* (000073) \* DEFINE "SAF" (SET ADDRESS FIELD) AND "LAF" (LOAD  
\* (000074) \* ADDRESS FIELD) INSTRUCTIONS TO ASSEMBLER.  
\* (000075) \* THESE INSTRUCTIONS ARE NEW CS/P INSTRUCTIONS  
\* (000076) \* AVAILABLE WITH "REV 18" CS/P MICROCODE.  
\* (000077) \* THEY SET/LOAD THE LOW ORDER 17 BITS OF THE  
\* (000078) \* REFERENCED FULL-WORD FROM/INTO THE INDICATED  
\* (000079) \* REGISTER.  
\* (000080) \*

OPADD	SAF,(1 .LS. 14) + (29 .LS. 8) + \$EF
OPADD	LAF,(1 .LS. 14) + (29 .LS. 8) + \$EE

000000582 (00025) BCT\$BA = \$0582  
00000024A (00026) AP\$CL = \$024A  
000000E3B (00027) SCD\$04R= \$0E3B

```
00000E60 (0030) #L = $0E60
00E60 EE3A0582 (0031) LAF R3,BCT$BA(R5)
```

```
00000F6C (00033) #L = $0F6C
00F6C EE440582 (00034) LAF R4,BCT$0A(R2)
```

```

00F8C EF540582 (00037)
00F00F8C (00036)
#L = $0F8C
SAF R5,BCT$BA(R2)

```

```

00000FB1 (00039) AL = $0FB1
00FB1 80001C98 (00042) JMP PATCH1
(00041) RET1:

```

```

00000E58 (00043)    #L = $0E58
000E58 80001CA0 (00044)    JMP    PATCH2
000E58 (00045)    *

```

PAGE 3: CBBN-TELEXD3MAP>BBNPAT.MSO.4, 8-Jan-80 18:55:38, Ed: KFIELD  
(00673)  
BNN SPEECH CODER - PATCHES TO MAP-300 SNAPII EXEC

000001C98 (00046) #L = \$1C98

```

(00047) PATCH1:
01C98 C060024A (00048) MOVHRL R6,APSCSL
01C9A 3A62 (00049) LLS R6,2
01C98 3A72 (00050) LLS R7,2
01C9C 3C62 (00051) LRS R6,2
01C9D 3C72 (00052) LRS R7,2
01C9E 00000FB3 (00053) JMP RET1
(00054) *
00001CA0 (00055) #L = $1CA0
(00056) PATCH2:
01CA0 3A32 (00057) LLS R3,2
01CA1 3C32 (00058) LRS R3,2
01CA2 00000E3B (00059) JMP SCD$04H
(00060) *
(00061) *
01CA4 (00062) ENG
PAGE 4: (BBN-TENEXD)<MAP>BBNPAT.MSO.4, 0-Jan-00 10:55:30, Ed: KFIELD
BBN SPEECH CODER - PATCHES TO MAP-300 SNAP11 EXEC

APSCSL: 0024A (00026) (00048)
BCT$BA: 00582 (00025) (00031) (00034) (00037)
PATCH1: 01C98 (00040) (00047)
PATCH2: 01CA0 (00044) (00056)
RET1: 00FB3 (00041) (00053)
SCD$04H: 00E3B (00027) (00059)

```

LINES WITH ERRORS: 0 (MAP VERSION 000101-10) E- 0



```

C <DC196>8AHNSP.FOR.1 Sun 30-Dec-79 4:18PM Page 1:2
C
C U IS ARRAY OF CODED K'S
C V IS INPUT ARRA OF AUTOCORRELATION COEFFS
C
C INTEGER FCBCN,Y,A,U,B,V
MWLF=FCBCN(135,Y,A,U,B,V,0,0,9)
RETURN
END
C
C USAGE IER = MWLF(Y,A,U,V)
C
C
C V IS BASE BAND OUTPUT
C U IS REFLN COFF OUTPUT
C V IS INPUT PARAM BUFFER
C A IS PITCH
C R IS TAP
C
C INTEGER FCBCN,Y,A,U,B,V,C,D
VAPC=FCBCN(150,Y,A,U,B,V,C,0,16)
RETURN
END
C
C INTEGER FUNCTION DEAL(Y,A,U,B,V)
C
C USAGE IER = DEAL(Y,A,U,B,V)
C
C V IS BASE BAND OUTPUT
C U IS REFLN COFF OUTPUT
C V IS INPUT PARAM BUFFER
C A IS PITCH
C R IS TAP
C
C INTEGER FCBCN,Y,A,U,B,V
DEAL=FCBCN(190,Y,A,U,B,V,0,0,0,4)
RETURN
END
C
C INTEGER FUNCTION MWLF(Y,A,U,V)
C
C
C USAGE IER = MWLF(Y,A,U,V)
C
C
C V IS OUTPUT BASEBAND SAMPLES
C A IS TAP
C U IS INPUT
C B IS PITCH
C
C INTEGER FCBCN,Y,A,U,B
VIAPC=FCBCN(196,Y,A,U,B,0,0,0,2)
RETURN
END
C
C INTEGER FUNCTION VAPC(Y,A,U,B,V,C,D)
C
C USAGE IER = VAPC(Y,A,U,B,V,C,D)
C
C V IS ARRAY OF CODED PARAMETERS
C U IS ARRAY OF QUANTIZE ERRORS
C V IS INPUT
C A IS PITCH (REAL SCALAR)
C R IS TAP, FOLLOWED BY INVERSE OF GAIN (REAL SCALARS)
C C IS CODED TAP, CODED GAIN, 9 CODED REFL. COEFS
C D IS INTEGERS IN LEFT HALFWORD OF REAL SCALARS
C D IS 0-B. QUANT INFO (REAL SCALARS)
C (3 QUANT THRESHOLDS, 4 QUANT VALUES)
C
C INTEGER FCBCN,Y,A,U,B,V,C,D
VIAPC=FCBCN(196,Y,A,U,B,0,0,0,2)
RETURN
END
C
C INTEGER FUNCTION MPIFF(ISA,ISB,FLID)
C
C SUPPORT MODULE FOR THE INTEGER "IF A.NE.0 .AND. B.EQ.0"
C CONDITIONAL FCB.
C CALLING SEQUENCE:
C IER = MPIFF(ISA,ISB,FLID)
C WHERE:
C ISA = INTEGER SCALAR ID (0 -LE. ISA -LE. 127)
C ISB = INTEGER SCALAR ID (0 -LE. ISB -LE. 127)
C FLID = FUNCTION LIST ID (0 -LE. FLID -LE. 63)
C
C INTEGER FCBCG,FCBSZ,MWS,FCB,RUNMP,HRI,FLID
COMMON/MPIZZ/FCBCG(11),FCBSZ(11,7),MWS,FCB(6),MPDCB(4),HRI,LEVEL
C
C MPIFF = 0
C DO 10 I=1,11
FCBCG(I) = #
10
C FCB #105
C
FCBCG(2) = 105
C
IF ((FLID .LT. 0) .OR. (FLID .GT. 63)) MPIFF = -3
IF ((ISB .LT. 0) .OR. (ISB .GT. 127)) MPIFF = -2
IF ((ISA .LT. 0) .OR. (ISA .GT. 127)) MPIFF = -1

```



```

C <DCA96>BBNHSP.FOR.1 Sun 30-Dec-79 4:18PM Page 1:4
C
C IF (MPIFF .NE. 0) CALL MPERR(MPIFF)
C IF (MPIFF .NE. 0) RETURN
C
C FCBSZ(3) = ISA
C FCBSZ(5) = ISA
C FCBSZ(7) = FLID
C
C MPIFF = RUNMP(FCBSZ(1),FCBSZ(1,5))
C
C RETURN
C END
C
C
C INTEGER FUNCTION MPXBM(FCBND,Y,A,U,V)
C
C SUPPORT MODULE FOR THE EXECUTE BOUND VERSION OF MMLF FCB
C
C CALLING SEQUENCE:
C
C MAP = MPXBM(FCBND,Y,A,U,V)
C
C WHERE:
C
C FCBND = FCB NUMBER OF CREATED BOUND MMLF FUNCTION
C Y,A,U,V = PARAMETERS TO MMLF
C
C INTEGER FCBSZ,FCBSZ,HWS,FCB,HRI,RUNMP,FCBND,Y,A,U,V
C COMMON/MPZZZ/FCBSZ(11),FCBSZ(11,7),HWS,FCB(6),MPDCB(4),HRI,LEVEL
C
C MPXBM = 0
C DO 10 I=1,11
C FCBSZ(I) = 0
C
C IF ((Y .LT. 1) .OR. (Y .GT. 63)) MPXBM=-5
C IF ((U .LT. 1) .OR. (U .GT. 63)) MPXBM=-4
C IF ((A .LT. 0) .OR. (A .GT. 255)) MPXBM=-3
C IF ((Y .LT. 1) .OR. (Y .GT. 63)) MPXBM=-2
C IF ((FCBND .LT. 0) .OR. (FCBND .GT. 255)) MPXBM=-1
C IF (MPXBM .NE. 0) CALL MPERR(MPXBM)
C IF (MPXBM .NE. 0) RETURN
C
C FCBSZ(2) = FCBND
C FCBSZ(3) = 2
C FCBSZ(4) = Y
C FCBSZ(5) = A
C FCBSZ(6) = U
C FCBSZ(8) = V
C
C MPXBM = RUNMP(FCBSZ(1),FCBSZ(1,5))
C RETURN
C END

```



```

10  FCARG(1) = 0
    FCARG(2) = 111
    IF ( (WSC.LE.0) .OR. (SID.WSC.GT. 192) )  MPMS = -3
    IF( SID.LT.0 .OR. SID.GT.191 )  MPMS = -2
    IF( BID.LE.0 .OR. BID.GT.63 )  MPMS = -1
    IF(MPMS.NE.0)CALL MPERR(MPMS)
    IF(MPMS.NE.0) RETURN
    FCARG(3) = BID
    FCARG(4) = SID
    FCARG(5) = WSC
    MPMS = RUNMP(FCARG(1), FCARG(1,1))
    RETURN
    END

```

[illegible]

```

C
C      COMMON BLOCK -
C      BUFFER ADDRESSES AND SIZES NEEDED FOR INIT OF NON-SWAP BUFFERS
C      BY DCA96I (WRITTEN BY DCA96C)
C
C      COMMON /BAS/
C      JATADBC, STADBC,
C      JATBTC, STBTC,
C      JARLPU, SRLPU,
C      JARSNG, SRNGC,
C      JATBRL, STBRL,
C      JATHMA, STHMA,
C      JATHMB, STHMB,
C      JADABA, SRDABA,
C      JADABB, SRDABB
C
C      CCCCCC END OF DCA96A.FOR CCCCCC
C
C      CCCCCC END OF DCA96A.FOR CCCCCC
C
C      DATA PRCSR/1/,AP/1/
C      DATA RLW/,CPLX/1/,PID/2/,CNTG/1/,LNG/8/,SHRT/1/
C      DATA HNSZ/1/,FWSZ/2/,DWSZ/4./
C
C      OPEN MAP, INHIBIT BUFFER CHECKING
C
C      CALL MPDPH(0)
C      CALL MPIAC(0)
C
C      CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
C      CONFIGURE MAP BUFFERS.
C
C      THIS MODULE CONFIGURES THE SYSTEM MAP BUFFERS.
C      ALL MAP BUFFER ID'S (BID'S) ARE STARTING WITH
C      SYMBOLICALLY NAMED, WITH NAMES STARTING WITH
C      "T" FOR TRANSMITTER BUFFERS, AND NAMES STARTING
C      WITH "R" FOR RECEIVER BUFFERS.
C      AN "M" FOLLOWED BY A BUFFER NAME SPECIFIES
C      THE HOST VARIABLE CONTAINING THE ADDRESS OF
C      THAT BUFFER. SIGNICANTLY, AN "S" FOLLOWED BY A
C      BUFFER NAME SPECIFIES THE HOST VARIABLE
C      CONTAINING THE SIZE OF THAT BUFFER.
C
C      C
C
C      DEFINE MAP BUFFER ID'S
C
C      IRUS1 = 64
C      IRUS2 = 128
C      IRUS3 = 192
C
C      C A/D INPUT FROM ADAM
C      TADRA = 0
C
C      C A/D INPUT FROM ADAM
C      TADBB = 0
C
C      C A/D "TONE" DATA
C      TADRC = 0
C
C      C TSOURCE A BUFFER
C      TSRA = 1
C
C      C TSOURCE B BUFFER
C      TSRB = 2
C
C      C HANNING WINDOW COEFFS.
C      THAMW = 3
C
C      C WINDOWED TSRA OR TSRB
C      TWSR = 4
C
C      C TWSR WITH 8 ZEROS ON RIGHT
C      TWSRZ = 6
C
C      C AUTOCORRELATION VALUES
C      TACV = 7
C
C      C OUTPUT FROM MWLF, INCLUDING REFLECTION COEFFS AND ERRORS
C      TRFR = 8
C
C      C OUTPUT FROM MWLF: CODED & Q-TIZED RF COEFFS (TCRF & TQRF)
C      TCRF = 9
C
C      C CODED REFLECTION COEFFS
C      TCRF = 10
C
C      C Q-TIZED REFLECTION COEFFS
C      TQRF = 11
C
C      C LINEAR PREDICTION COEFFS
C      TLPC = 7
C

```

```

C REVERSE OF TLPC
  TLPCR = 5
C CURRENT TSOURCEX DATA IN LONG REAL FORMAT
  TSR = 13
C TSR WITH 8 MEMORY SAMPLES IN FRONT
  TSRL = 14
C FIRST 8 SAMPLES OF TSRL
  TSRL = 15
C LAST 8 SAMPLES OF TSRL
  TSRL = 16
C INVERSE FILTERED SAMPLES (CURRENT FRAME)
  TINFO = 17
C TINFO PLUS 37 PREVIOUS FRAME ELEMENTS IN FRONT
  TINFI = 18
C 257 INVERSE FILTERED SAMPLES CENTERED ON PREV FRAME
  TINF = 19
C LPF COEFFS TO GET BASEBAND EXCITATION
  TLPPB = 20
C DOWNSAMPLED BASEBAND (BB) EXCITATION
  TBEO = 21
C PREVIOUS FRAME'S TBEO
  TBEL = 22
C LAST HALF OF TBEL, ALL OF TBEO
  TBE = 23
C TRE PLUS 30 ZEROS
  TBZ = 24
C PITCH AUTOCORRELATION BUFFER
  TPAC = 25
C BB EXCITATION PITCH CALC PART (TPAC(5) - TPAC(38))
  TPACP = 29
C BB EXCITATION WITH PITCH REQUIRED
  TBPR = 40
C BB RESIDUAL DIFFERENCE BUFFER (LEFT HALF)
  TBRL = 0
C
C BB RESIDUAL DIFFERENCE BUFFER (RIGHT HALF)
  TBRR = 33
C
C TSINK A BUFFER
  TSKA = 35
C TSINK B BUFFER
  TSKB = 36
C TSINK "SILENCE" BUFFER (NO LONGER USED)
  TSNKC = 0
C
C TBITS A BUFFER
  TBTA = 0
C TBITS B BUFFER
  TBTB = 0
C TBITS C BUFFER
  TBTC = 0
C
C THODEN A BUFFER
  THODA = 0
C THODEN B BUFFER
  THOHB = 0
C THODEN A BUFFER
  THOMA = 0
C THODEN B BUFFER
  THOHB = 0
C THODEN C BUFFER
  THOHC = 0
C
C RBITS A BUFFER
  RBTA = 0
C RBITS B BUFFER
  RBTB = 0
C
C SYNC SEARCH PREVIOUS FRAME
  RSSPF = 0
C
C SYNC SEARCH SUM SYNC
  RSSSS = 0
C
C RSOURCE A BUFFER
  RSHA = 37
C

```

```
C <DCA96>DCA96A.FOR.1 Sat 29-Dec-79 7:33PM Page 1:7
C RUPB8 PLUS 37 PREV FRAME ELEMENTS IN FRONT
RUPR1 = 55
C C 254 UPSAMPLED AND PERTURBED 88 SAMPLES CENTERED ON PREV FRAME
RUPR = 56
C C HPF COEFFS (WITH GAIN OF 3) TO GET UPSAMPLED, PERTURBED SAMPLES
RHPV = 57
C C HPF'D, UPSAMPLED, PERTURBED SAMPLES
RHUP = 58
C C FILTERED EXCITATION SAMPLES
RFES = 58
C C PREV FRAME'S RRP0 (810 #39)
RRP1 = 59
C C SYNTHESIS FILTER MEMORY
RSFM = 60
C C SYNTHESIZED SPEECH
RSNKA = 61
C C SYNTHESIZED SPEECH
RSNKB = 62
C C D/A "SILENCE" BUFFER
RSNKC = 0
C C D/A OUTPUT TO ADM
ROADBA = 0
C C D/A OUTPUT TO ADM
ROADBB = 0
C C
C C
C C
C C DEFINE BUFFER SIZES (NUMBER OF SAMPLES PER BUFFER)
STADBA = 180.
STADBB = 180.
STADBC = 180.
SYSRA = 180.
SYSRB = 180.
STHAMV = 180.
SHMSR = 180.
SHWSRZ = 180.
SHVAC = 9.
SHVFER = 16.
```





```

C
ARMONA = EVEN(ATNDMB + WMSZ*STNDMB)
ARMOMB = EVEN(ARMONA + WMSZ*SRNDMB)
ARMOMC = EVEN(ARMOMB + WMSZ*SRNDMC)
ARSSPF = EVEN(ARMOMC + WMSZ*SRND4C)
ARSSSS = EVEN(ARSSPF + WMSZ*SRSSPF)
ARSRA = EVEN(ARSSSS + WMSZ*SRSSSS)
ARSRB = ARSRA + WMSZ*SRRA
ARSYKA = ARSRB + WMSZ*SRSRB
ARSMKB = EVEN(ARSMKA + WMSZ*SRSMKA)
ARSMKC = EVEN(ARSMKB + WMSZ*SRSMKB)
ARDA8A = EVEN(ARSMKC + WMSZ*SRSMKC)
ARDA8B = EVEN(ARDA8A + WMSZ*SRDA8A)
TOP1 = EVEN(ARDA8B + WMSZ*SRDA8B)

C
BUS2 BUFFERS:
BASE2 = 0.
ATBRDL = BASE2
ATBRDR = ATBRDL + FMSZ*STBRDL
ATHAMW = ATBRDR + FMSZ*STBRDR

C
ARRF0 = ATHAMW + FMSZ*STHAMW
ARDE = ARRF0 + FMSZ*SRRF0
ARRR = ARDE + FMSZ*SRBE
ARRND = ARRR + FMSZ*SRBR
ARRUP = ARRND + FMSZ*SRRND
ARFES = ARRUP
ARF1 = ARFES + FMSZ*SRFUP
ARSPM = ARF1 + FMSZ*SRF1
TOP2 = ARSPM + FMSZ*SRSPM

C
ATBPR = ARRR

C
BUS3 BUFFERS
BASE3 = 0.
ATINF = BASE3
ATINF0 = ATINF + FMSZ*STINF1
ATINF1 = ATINF0 + FMSZ*STINF0
ATWSR = ATINF1 + FMSZ*STWSR
ATWER = ATWSR + FMSZ*STWSR2
ATCORF = ATWER + FMSZ*STWER
ATCORF = ATCORF + WMSZ
ATQRF = ATCORF + FMSZ*STQRF
ATSRM = ATQRF + FMSZ*STSRM
ATSRF = ATSRM + FMSZ*STSRF
ATSR = ATSRF + FMSZ*STSR
ATSR1 = ATSR + FMSZ*STSR
ATACV = ATSR1 + FMSZ*STSRM

```

```

C <DCA96>DCA96A.FOR.1 Sat 29-Dec-79 7:33PM
ATLPC = ATACY
ATLPCR = ATLPC
ATLPFB = ATACY + FMSZ*STLPCF
ATBE1 = ATLPFB + FMSZ*STLPCF*.5
ATBE0 = ATBE1 + FMSZ*STBE1
ATBEZ = ATBE0 + FMSZ*STBEZ
ATPAC = ATBEZ + FMSZ*STBEZ
ATBPCP = ATPAC + FMSZ*5.

C
ARHRE = ATPAC + FMSZ*STPAC
ARHBE0 = ARHRE + FMSZ*SRHBE1
ARHBE1 = ARHBE0 + FMSZ*SRHBE0
ARLP0 = ARHBE1 + FMSZ*SRHBE0
ARLP03 = ARLP0 + FMSZ
ARLP02 = ARLP03 + FMSZ
ARLP01 = ARLP02 + FMSZ*SRLP0
ARLP0 = ARLP01 + FMSZ*SRLP0
ARUP00 = ARLP0 + FMSZ*SRUP01
ARUP01 = ARUP00 + FMSZ*SRUP00
ARUP0 = ARUP01 + FMSZ*SRUP00
ARUBE1 = ARUP0 + FMSZ*SRUBE1
ARUBE2 = ARUBE1 + FMSZ
ARUBE3 = ARUBE2 + FMSZ
TOP3 = ARUBE3 + FMSZ*SRUBE

C
CONFIGURE BUFFERS
BUS1 BUFFERS:
CALL MPCLB((BUS1+TSRA, TSRA, STSRA, RL,
CALL MPCLB((BUS1+TSRB, TSRA, STSRA, RL,
CALL MPCLB((BUS1+TSKA, TSKA, STSKA, FXD,
CALL MPCLB((BUS1+TSKB, ATSKB, STSKB, FXD,
CALL MPCLB((BUS1+RSRA, RSRA, SRSRA, RL,
CALL MPCLB((BUS1+RSRB, ARSRB, SRSRB, RL,
CALL MPCLB((BUS1+RSKA, ARSKA, SRSKA, RL,
CALL MPCLB((BUS1+RSKB, ARSKB, SRSKB, RL,
BUS2 BUFFERS:
CALL MPCLB((BUS2+TBDR, ATBDR, STBDR, RL,
CALL MPCLB((BUS2+THAMW, ATHAMW, STHAMW, RL,

```



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```

C C SOURCE FLAG A
C   TSRFA = 50
C C SOURCE FLAG B
C   TSRFB = 51
C C TRITS FLAG A
C   TRIFA = 52
C C TRITS FLAG B
C   TRIFB = 53
C C INTEGER PITCH
C   TIPTC = 54
C C RBITS FLAG A
C   R8TFA = 57
C C RBITS FLAG B
C   R8TFB = 58
C C PSINK FLAG A
C   RSNFA = 59
C C PSINK FLAG B
C   RSNFB = 60
C C DECODED (INTEGER) PITCH
C   RIPTC = 61
C C SYSTEM RUN FLAG (R => STOP)
C   RUN = 62
C C FREE FOR MPITM
C   I = 63
C C A/D POINTER OFFSET
C   ADPO = 64
C C SOURCE POINTER OFFSET
C   TSRPO = 65
C C TRITS POINTER OFFSET
C   TATPO = 66
C C TMODEM POINTER OFFSET
C   TMPO = 67
C C RMODEM POINTER OFFSET
C   RMPO = 68

```

```

C C RBITS POINTER OFFSET
C   RRPPO = 69
C C PSINK POINTER OFFSET
C   RSNPO = 70
C C D/A POINTER OFFSET
C   DAPPO = 71
C C A/D FRAME DISCARD COUNTER
C   TADFDC = 72
C C TMODEM FAKE FRAME COUNTER
C   TMFFC = 73
C C RMODEM FRAME DISCARD COUNTER
C   RMFDC = 74
C C PSINK DATA-NOT-READY COUNTER
C   RSNDR = 75
C C TRANSMITTER FRAME COUNTER
C   TFRCTR = 76
C C RECEIVER FRAME COUNTER
C   RFRCTR = 77
C C RECEIVER LOST-SYNC COUNTER
C   RLSCTR = 78
C C LOCAL HANDSET ON-HOOK STATE
C   RONHK = 79
C C STATE OF RMODEM SYNC
C   RSYNC = 80
C C BEGINNING-OF-FRAME OFFSET (SYNC BIT POSITION)
C   R8OFO = 81
C C FREE FOR MPITM
C   I = 82
C C NO-ERROR-CORRECTION SWITCH (NO CORRECTIONS IF NZ)
C   RNOCOR = 123
C C FREE FOR MPITM
C   I = 124
C C CHANNEL ERROR SIMULATOR FLAG (NON-ZERO => R SIM ERRORS PER FRAME)
C   RERSIM = 125

```



```

C
CALL SCOPY('BASE3' = ',NAME)
TYPE 111,NAME,BASE3
CALL SCOPY('ATINF' = ',NAME)
TYPE 111,NAME,ATINF
CALL SCOPY('ATINF0' = ',NAME)
TYPE 111,NAME,ATINF0
CALL SCOPY('ATINF1' = ',NAME)
TYPE 111,NAME,ATINF1
CALL SCOPY('ATMSR' = ',NAME)
TYPE 111,NAME,ATMSR
CALL SCOPY('ATMSR2' = ',NAME)
TYPE 111,NAME,ATMSR2
CALL SCOPY('ATRFER' = ',NAME)
TYPE 111,NAME,ATRFER
CALL SCOPY('ATCRF' = ',NAME)
TYPE 111,NAME,ATCRF
CALL SCOPY('ATCRF1' = ',NAME)
TYPE 111,NAME,ATCRF1
CALL SCOPY('ATSRF' = ',NAME)
TYPE 111,NAME,ATSRF
CALL SCOPY('ATSR' = ',NAME)
TYPE 111,NAME,ATSR
CALL SCOPY('ATSL' = ',NAME)
TYPE 111,NAME,ATSL
CALL SCOPY('ATACV' = ',NAME)
TYPE 111,NAME,ATACV
CALL SCOPY('ATLPC' = ',NAME)
TYPE 111,NAME,ATLPC
CALL SCOPY('ATLPCR' = ',NAME)
TYPE 111,NAME,ATLPCR
CALL SCOPY('ATLPEB' = ',NAME)
TYPE 111,NAME,ATLPEB
CALL SCOPY('ATBE' = ',NAME)
TYPE 111,NAME,ATBE
CALL SCOPY('ATBE2' = ',NAME)
TYPE 111,NAME,ATBE2
CALL SCOPY('ATBE1' = ',NAME)
TYPE 111,NAME,ATBE1
CALL SCOPY('ATBE0' = ',NAME)
TYPE 111,NAME,ATBE0
CALL SCOPY('ATPAC' = ',NAME)
TYPE 111,NAME,ATPAC
CALL SCOPY('ATBPCP' = ',NAME)
TYPE 111,NAME,ATBPCP
CALL SCOPY('ARHBE' = ',NAME)
TYPE 111,NAME,ARHBE
CALL SCOPY('ARHBE0' = ',NAME)
TYPE 111,NAME,ARHBE0
CALL SCOPY('ARHBE1' = ',NAME)
TYPE 111,NAME,ARHBE1
CALL SCOPY('ARLPU' = ',NAME)
TYPE 111,NAME,ARLPU
CALL SCOPY('ARLPU1' = ',NAME)
TYPE 111,NAME,ARLPU1
CALL SCOPY('ARLPU2' = ',NAME)
TYPE 111,NAME,ARLPU2
CALL SCOPY('ARLPU3' = ',NAME)
TYPE 111,NAME,ARLPU3
CALL SCOPY('ARHPU' = ',NAME)
TYPE 111,NAME,ARHPU
CALL SCOPY('ARUBE' = ',NAME)
TYPE 111,NAME,ARUBE
CALL SCOPY('ARUBE1' = ',NAME)
TYPE 111,NAME,ARUBE1
CALL SCOPY('ARUBE2' = ',NAME)
TYPE 111,NAME,ARUBE2
CALL SCOPY('ARUBE3' = ',NAME)
TYPE 111,NAME,ARUBE3
CALL SCOPY('ARUPB' = ',NAME)
TYPE 111,NAME,ARUPB
CALL SCOPY('ARUPB0' = ',NAME)
TYPE 111,NAME,ARUPB0
CALL SCOPY('ARUPB1' = ',NAME)
TYPE 111,NAME,ARUPB1
CALL SCOPY('TOP3' = ',NAME)
TYPE 111,NAME,TOP3

C 111 FORMAT(10A1,F7.0)
C
C RETURN
C END

C
C REAL FUNCTION EVEN(X)
C
C USAGE: Y = EVEN(X)
C
C K IS REAL INPUT
C Y IS: X IF X IS EVEN
C X+1 IF X IS ODD
C
C EVEN = X
C IF (AMOD(X,2.) .NE. 0.) EVEN = X+1.
C RETURN
C END

```

C &lt;DCA96&gt;DCA96C.FOR.1 Sat 29-Dec-79 7:34PM

C

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Sat 29-Dec-79 7:34PM

C

C LBBM-TENEXDJKKFIELD&gt;DCA96C.FOR.35, 11-Dec-79 17:29:49, Ed: MFIELD

SUBROUTINE DCA96C  
CCC  
C DCA96C  
C DCA96 CONFIGURATION - BUFFER AND SCALAR  
C CONFIGURATION.

CC

C MAP-RELATED VARIABLES

C  
C REAL HWSZ,FWSZ,DWSZ  
C INTEGER RL,CNPLX,FXD,CNTG,LNG,SHRT  
C INTEGER PRCSR,AP

C LBBM-TENEXDJKKFIELD&gt;DCA96C.FOR.8, 5-Dec-79 14:42:27, Ed: WMLF

C MAP BUFFER NAMES

C  
C INTEGER TADRA,TADRB,TADBC,TSRA,TSRB,THANW  
C INTEGER TNSR,TLPCR,TACY,TRFR,TCQRF  
C INTEGER TCRF,TQRF,TLPC,TSR,TSRM,TSRF  
C INTEGER TSRL,TINF,TINF1,TINF,TLPF8,TRF8  
C INTEGER TBE1,TBE,TBEZ,TNSRZ  
C INTEGER TPAC,TBPCP  
C INTEGER TPRR,TBRDL,TBRDR,TSNKA,TSNKB,TSNKC  
C INTEGER TOTA,TBTB,TBTC  
C INTEGER TMDMA,TMDMB  
C INTEGER RTA,RTB  
C INTEGER RDA4A,RDA4B,RDA4C,RSSPF,RSSSS,RSRA  
C INTEGER RSRB,RRF8,RRR,RBE,RHBE8,RHBE1  
C INTEGER RHBE,RLPU,RLPU1,RLPU2,RLPU3,RUBE  
C INTEGER RUBEL,RUBE2,RUBE3,RRND,RUPB8,RUPB1  
C INTEGER RUPB,RHPU,RHUP,RFES,RFEL,RSFM  
C INTEGER RSNKA,RSNKB,RSNKC,RDABA,RDAB8

C MAP SCALARS

C  
C INTEGER TDCM,TFSZ1,TKTHR,TPTC  
C INTEGER TE,TOTAP,TQG,TOCI,TBT1  
C INTEGER TBT2,TBT3,TBQ8,TBQ1,TBQ2,TBQ3  
C INTEGER TCTAP,TCG,TCRF1,TCRF2,TCRF3,TCRF4  
C INTEGER TCRF5,TCRF6,TCRF7,TCRF8,T5  
C INTEGER TOPSI  
C INTEGER RTAP,RWCL1,RWCL2,RWCL3,RWCL4  
C INTEGER RWM1,RWM2,RWM3,RWM4,RPCL1,RPCL2  
C INTEGER RPTC

C &lt;DCA96&gt;DCA96C.FOR.1 Sat 29-Dec-79 7:34PM

C

INTEGER TSRA,TSRB,TBTFA,TBTFB,TIPTC  
INTEGER RBFA,RBTFB,RSNFA,RSNFB,RIPTC  
INTEGER RUN  
INTEGER ADPO,TSRPO,TBTPO,IMPO,RMPO  
INTEGER RATPO,RSNPO,DAPO,TAPOFDC  
INTEGER IMFFC,RMFDC,RSNMR,IFRCTP  
INTEGER RPRCTR,RLSCTR  
INTEGER RONHK,RSYNC,RBOFO,RNOCOR  
INTEGER RERSIN, VSTATE

C COMMON BLOCK - BUFFER AND SCALAR IDS

C COMMON /BSIDS/

C  
C TADRA,TADRB,TADBC,TSRA,TSRB,THANW,  
C TNSR,TLPCR,TACY,TRFR,TCQRF,  
C TCRF,TQRF,TLPC,TSR,TSRM,TSRF,  
C TSRL,TINF,TINF1,TINF,TLPF8,TRF8,  
C TBE1,TBE,TBEZ,TNSRZ,  
C TPAC,TBPCP,  
C TPRR,TBRDL,TBRDR,TSNKA,TSNKB,TSNKC,  
C TOTA,TBTB,TBTC,  
C TMDMA,TMDMB,  
C RTA,RTB,  
C RDA4A,RDA4B,RDA4C,RSSPF,RSSSS,RSRA,  
C RSRB,RRF8,RRR,RBE,RHBE8,RHBE1,  
C RHBE,RLPU,RLPU1,RLPU2,RLPU3,RUBE,  
C RUBEL,RUBE2,RUBE3,RRND,RUPB8,RUPB1,  
C RUPB,RHPU,RHUP,RFES,RFEL,RSFM,  
C RSNKA,RSNKB,RSNKC,RDABA,RDAB8,  
C TDCM,TFSZ1,TKTHR,TPTC,  
C TE,TOTAP,TQG,TOCI,TBT1,  
C TBT2,TBT3,TBQ8,TBQ1,TBQ2,TBQ3,  
C TCTAP,TCG,TCRF1,TCRF2,TCRF3,TCRF4,  
C TCRF5,TCRF6,TCRF7,TCRF8,T5,  
C TOPSI,  
C RTAP,RWCL1,RWCL2,RWCL3,RWCL4,  
C RWM1,RWM2,RWM3,RWM4,RPCL1,RPCL2,  
C RPTC,  
C TSRA,TSRB,TBTFA,TBTFB,TIPTC,  
C RBFA,RBTFB,RSNFA,RSNFB,RIPTC,  
C RUN,  
C ADPO,TSRPO,TBTPO,IMPO,RMPO,RSNPO,  
C RATPO,DAPO,TAPOFDC,IMFFC,RMFDC,  
C RSNMR,IFRCTP,RFCTR,RLSCTR,  
C RONHK,RSYNC,RBOFO,RNOCOR,  
C RERSIN, VSTATE

C <DCA96>DCA96C.FOP.1 Sat 29-Dec-79 7:34PM

C AN "A" FOLLOWED BY A BUFFER NAME SPECIFIES  
C THE HOST VARIABLE CONTAINING THE ADDRESS OF  
C THAT BUFFER. SIMILARLY, AN "S" FOLLOWED BY A  
C BUFFER NAME SPECIFIES THE HOST VARIABLE  
C CONTAINING THE SIZE OF THAT BUFFER.

C DEFINE MAP BUFFER ID'S

C IBUS1 = 64  
C IBUS2 = 128  
C IBUS3 = 192

C A/D INPUT FROM ADAM

C TADBA = 12

C A/D INPUT FROM ADAM

C TADBD = 31

C A/D "TONE" DATA

C TADRC = 0

C TSOURCE A BUFFER

C TSRA = 1

C TSOURCE B BUFFER

C TSRB = 2

C HANNING WINDOW COEFFS.

C THAW = 3

C WINDOWED TSRA OR TSB

C TMSR = 4

C TMSR WITH 8 ZEROS ON RIGHT

C TMSRZ = 6

C AUTOCORRELATION VALUES

C TACY = 7

C OUTPUT FROM MWLF, INCLUDING REFLECTION COEFFS AND ERRORS

C TRPR = 8

C OUTPUT FROM MWLF: CODED & Q-TIZED RF COEFFS (TCRF & TORF)

C TCRF = 9

C CODED REFLECTION COEFFS

C TCRF = 10

C <DCA96>DCA96C.FOP.1 Sat 29-Dec-79 7:34PM

C COMMON BLOCK -  
C BUFFER ADDRESSES AND SIZES NEEDED FOR INIT OF NON-SNAP BUFFERS  
C BY DCA961 (WRITTEN BY DCA96C)

C COMMON /BAS/

C ;ATADBC,STADRC,

C ;ATHTC,STHTC,

C ;ARLP,SRLP,

C ;ARSKC,SRSKC,

C ;ATHRD,STHRL,

C ;ATHMD,STHMD,

C ;ATHMB,STHMB,

C ;ADARA,SRDARA,

C ;ADARB,SRDARB

C CCCCCC END OF DCA96C.FOR CCCCCC

C DATA PRCSR/1/,AP/1/  
C DATA RLX/1/,CNPLX/1/,FRD/2/,CNTG/1/,LNG/1/,SHRT/1/  
C DATA HWSZ/1/,PWSZ/2/,DWSZ/4/

C OPEN MAP, INHIBIT BUFFER CHECKING

C SET SNAP ERROR REPORT LEVEL:

C 0 => NO REPORT

C 1 => REPORT CODE; CONTINUE

C 2 => REPORT CODE; HALT

C 3 => REPORT MSG.; CONTINUE

C 4 => REPORT MSG.; HALT

C 5 => REPORT CODE; STATUS; HALT

C 6 => REPORT MSG.; STATUS; HALT

C IERPT = 3

C CALL MPDPM(IERPT)

C CALL MPISC(0)

C CCC

C CONFIGURE MAP BUFFERS.

C THIS MODULE CONFIGURES THE SYSTEM MAP BUFFERS.

C ALL MAP BUFFER ID'S (RID'S) ARE

C SYMBOLICALLY NAMED, WITH NAMES STARTING WITH

C "T" FOR TRANSMITTER BUFFERS, AND NAMES STARTING

C WITH "R" FOR RECEIVER BUFFERS.



C <DCA96>DCA96C.FOR.1 Sat 29-Dec-79 7:34PM

C  
C BB EXCITATION PITCH CALC PART (TPAC(5) - TPAC(30))  
TBPCT = 29  
C BB EXCITATION WITH PITCH REMOVED  
TBPCT = 40  
C BB RESIDUAL DIFFERENCE BUFFER (LEFT HALF)  
TBRDL = 0  
C  
C (NOTE: RMDMA = 30; TADBB = 31; RMDMB = 32)  
C BB RESIDUAL DIFFERENCE BUFFER (RIGHT HALF)  
TBRDR = 33  
C (NOTE: TMDMB = 34)  
C  
C TSINK A BUFFER  
TSNKA = 35  
C TSINK B BUFFER  
TSNKB = 36  
C  
C TSINK "SILENCE" BUFFER (NO LONGER USED)  
TSNKC = 0  
C  
C TBITS A BUFFER  
TBATA = 0  
C TBITS B BUFFER  
TBATB = 0  
C TBITS C BUFFER  
TBATC = 0  
C TMODEM A BUFFER  
TMDMA = 42  
C TMODEM B BUFFER  
TMDMB = 34  
C RMODEM A BUFFER  
RMDMA = 30  
C RMODEM B BUFFER  
RMDMB = 32  
C RMODEM C BUFFER  
RMDMC = 25  
C PBITS A BUFFER  
PBATA = 0

C <DCA96>DCA96C.FOR.1 Sat 29-Dec-79 7:34PM

C  
C Q-TI220 REFLECTION COEFFS  
TORF = 11  
C LINEAR PREDICTION COEFFS  
TLPC = 7  
C REVERSE OF TLPC  
TLPCR = 5  
C (NOTE: TADBA = 12)  
C CURRENT TSOURCEX DATA IN LONG REAL FORMAT  
TSR = 13  
C TSR WITH A MEMORY SAMPLES IN FRONT  
TSRM = 14  
C FIRST 8 SAMPLES OF TSRM  
TSRF = 15  
C LAST 8 SAMPLES OF TSRM  
TSRL = 16  
C INVERSE FILTERED SAMPLES (CURRENT FRAME)  
TIMF0 = 17  
C TIMF0 PLUS 37 PREVIOUS FRAME ELEMENTS IN FRONT  
TIMF1 = 19  
C 254 INVERSE FILTERED SAMPLES CENTERED ON PREV FRAME  
TIMF = 19  
C LPP COEFFS TO GET BASEBAND EXCITATION  
TLPPB = 20  
C DOWNSAMPLED BASEBAND (BB) EXCITATION  
TBEB = 21  
C PREVIOUS FRAME'S TBEB  
TBEB1 = 22  
C LAST HALF OF TBEB1, ALL OF TBEB  
TBE = 23  
C TBE PLUS 30 ZEROS  
TBEBZ = 24  
C (NOTE: RMDMC = 25; RMDMA = 26; RMDMB = 27)  
C PITCH AUTOCORRELATION BUFFER  
TPAC = 28

C <DCA96>DCA96C.FOR.1 Sat 29-Dec-79 7:34PM

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C EVERY 3RD ELEMENT OF RURE STARTING WITH FIRST
  RURE1 = 50
C EVERY 3RD ELEMENT OF RURE STARTING WITH SECOND
  RURE2 = 51
C EVERY 3RD ELEMENT OF RURE STARTING WITH THIRD
  RURE3 = 52
C RANDOM NUMBER ARRAY
  RRND = 53
C CURRENT FRAME OF UPSAMPLED AND PERTURBED 88 SAMPLES
  RUPR0 = 54
C RUPR0 PLUS 37 PREV FRAME ELEMENTS IN FRONT
  RUPR1 = 55
C 254 UPSAMPLED AND PERTURBED 88 SAMPLES CENTERED ON PREV FRAME
  RUPR = 56
C HPF COEFFS (WITH GAIN OF 3) TO GET UPSAMPLED, PERTURBED SAMPLES
  RUPU = 57
C HPF'D, UPSAMPLED, PERTURBED SAMPLES
  RHUP = 58
C FILTERED EXCITATION SAMPLES
  RFES = 58
C PREV FRAME'S RRP0 (RID #39)
  RRF1 = 59
C SYNTHESIS FILTER MEMORY
  RSPM = 60
C SYNTHESIZED SPEECH
  RSMKA = 61
C SYNTHESIZED SPEECH
  RSMKB = 62
C D/A "SILENCE" BUFFER
  RSMKC = 0
C D/A OUTPUT TO ADM
  RDABA = 26
C D/A OUTPUT TO ADM
  RDARB = 27
C

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C <DCA96>DCA96C.FOR.1 Sat 29-Dec-79 7:34PM

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C RPTS B BUFFER
  RRTB = 0
C SYNC SEARCH PREVIOUS FRAME
  RSSPF = 0
C SYNC SEARCH SYN SYNC
  RSSSS = 0
C SOURCE A BUFFER
  RSRA = 37
C SOURCE B BUFFER
  RSRB = 38
C CURRENT FRAME REFLECTION COEFFS
  RRF0 = 39
C RB RESIDUAL
  RRR = 40
C BB EXCITATION
  RRE = 41
C (NOTE: TMDXA = 42)
C HPF'D BB EXCITATION
  RHRE0 = 43
C CURRENT FRAME HPF'D BB EXCITATION, PLUS 12 PREV FRAME ELEMENTS
  RHRE1 = 44
C R4 HPF'D BB EXCITATION SAMPLES CENTERED ON PREV FRAME
  RHRE = 45
C LPF COEFFS (WITH GAIN OF 3) TO GET UPSAMPLED 88 EXCITATION
  RLPU = 46
C EVERY 3RD ELEMENT OF RLPU STARTING WITH 3RD
  RLPU1 = 46
C EVERY 3RD ELEMENT OF RLPU STARTING WITH SECOND
  RLPU2 = 47
C EVERY 3RD ELEMENT OF RLPU STARTING WITH 1ST
  RLPU3 = 48
C UPSAMPLED, LPF'D BB EXCITATION
  RURE = 49
C

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UUUUU

DEFINE BUFFER SIZES (NUMBER OF SAMPLES PER BUFFER)

[illegible]

C <DCA96>DCA96C.FOR.I Sat 29-Dec-79 7:34PM

4

SRNDMA = 261.  
SRNDAB = 261.  
SRNDMC = 261.  
SRNTA = 261.  
SRNTB = 261.  
SRNTC = 261.  
SRSSP = 261.  
SRSSS = 261.  
SRRA = 71.  
SRRB = 71.  
SRRC = 0.  
SRRE = 60.  
SRRE = 60.  
SRRE = 60.  
SRRE1 = 72.  
SRRE1 = 04.  
SRPU = 75.  
SRPU1 = SRPU/3.  
SRPU2 = SRPU/3.  
SRPU3 = SRPU/3.  
SRUBE = 100.  
SRUBE1 = SRUBE/3.  
SRUBE2 = SRUBE/3.  
SRUBE3 = SRUBE/3.  
SRUD = 60.  
SRUD0 = 100.  
SRUPB1 = 217.  
SRUPB = 250.  
SRUPC = 75.  
SRUPH = 100.  
SRPES = 100.  
SRRF1 = 0.  
SRRFM = 0.  
SRSMKA = 100.  
SRSMKB = 100.  
SRSMKC = 100.  
SRSDAA = 100.  
SRSDAB = 100.

**UUUUUUUUUUUUUUUUUUUU**

## DEFINE BUFFER ADDRESSES

## AUS1 BUFFERS!

**PUT BUS1 BUFFERS AT TOP END OF BUS1 MEMORY:  
 SET BUFFER BASE ON BUS1 TO HW SIZE OF BUS1  
 (\$C000 = 49152. = 48K HWORDS) MINUS APPROX HW SIZE  
 OF BUS1 BUFFERS.**

**BASE1 = 49152. - 6319.**





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C <DCA96>DCA96C.FOR.1 Sat 29-Dec-79 7:34PM Page 1:14
C DEFINE MAP (REAL) SCALAR ID'S
C
C NEGATIVE D.C. VALUE OF CURRENT FRAME
  TDCM = 5#
C
C NEGATIVE INVERSE OF FRAMESIZE
  TFSZI = 51
C
C THRESHOLD FOR MWLF
  TKTHR = 52
C
C PITCH (BETWEEN 5 AND 30, INCLUSIVE)
  TPTC = 55
C
C ENERGY OF PITCH-REMOVED BASEBAND EXCITATION
  TE = 57
C
C QUANTIZED ITAP (TOTAP,TQG,TQCI MUST BE CONSECUTIVE)
  TOTAP = 58
C
C QUANTIZED GAIN (GAIN=SQRT(TE)) (TOTAP,TQG,TQCI MUST BE CONSEC.)
  TQG = 59
C
C INVERSE OF QUANTIZED GAIN (TOTAP,TQG,TQCI MUST BE CONSECUTIVE)
  TQCI = 60
C
C R.B. QUANTIZATION THRESHOLD 1 (TBT1,2,3,TBQ0,1,2,3 MUST BE CONSEC.)
  TBT1 = 61
C
C R.B. QUANT. THRESH. 2 (TBT1,2,3,TBQ0,1,2,3 MUST BE CONSEC.)
  TBT2 = 62
C
C R.B. QUANT. THRESH. 3 (TBT1,2,3,TBQ0,1,2,3 MUST BE CONSEC.)
  TBT3 = 63
C
C B.B. QUANT. VALUE 0 (TBT1,2,3,TBQ0,1,2,3 MUST BE CONSEC.)
  TBQ0 = 64
C
C B.B. QUANT. VALUE 1 (TBT1,2,3,TBQ0,1,2,3 MUST BE CONSEC.)
  TBQ1 = 65
C
C B.B. QUANT. VALUE 2 (TBT1,2,3,TBQ0,1,2,3 MUST BE CONSEC.)
  TBQ2 = 66
C
C B.B. QUANT. VALUE 3 (TBT1,2,3,TBQ0,1,2,3 MUST BE CONSEC.)
  TBQ3 = 67
C
C DECODED TAP
  RTAP = 68
C

C <DCA96>DCA96C.FOR.1 Sat 29-Dec-79 7:34PM Page 1:15
C COEFF 1 FOR BUTTERWORTH FILTER (RBWC1,2,3,4 MUST BE CONSEC.)
  RBWC1 = 70
C
C COEFF 2 FOR B.W. FILTER (RBWC1,2,3,4 MUST BE CONSEC.)
  RBWC2 = 71
C
C COEFF 3 FOR B.W. FILTER (RBWC1,2,3,4 MUST BE CONSEC.)
  RBWC3 = 72
C
C COEFF 4 FOR B.W. FILTER (RBWC1,2,3,4 MUST BE CONSEC.)
  RBWC4 = 73
C
C MEMORY 1 FOR B.W. FILTER (RBWM1,2,3,4 MUST BE CONSEC.)
  RBWM1 = 74
C
C MEMORY 2 FOR B.W. FILTER (RBWM1,2,3,4 MUST BE CONSEC.)
  RBWM2 = 75
C
C MEMORY 3 FOR B.W. FILTER (RBWM1,2,3,4 MUST BE CONSEC.)
  RBWM3 = 76
C
C MEMORY 4 FOR B.W. FILTER (RBWM1,2,3,4 MUST BE CONSEC.)
  RBWM4 = 77
C
C FIRST CONSTANT FOR PERTURBATION
  RPC1 = 78
C
C SECOND CONSTANT FOR PERTURBATION
  RPC2 = 79
C
C CODED TAP (INTEGER: 1ST WORD OF REAL SCALAR) (TCTAP,TCG,TCRF1-8 CONS
  *%EC)
  TCTAP = 80
C
C CODED GAIN (INTEGER) (TCTAP,TCG,TCRF1-8 MUST BE CONSEC)
  TCG = 81
C
C PREV FRAME CODED REFL COEF(1) (INTEGER) (TCTAP,TCG,TCRF1-8 CONSEC)
  TCRF1 = 82
C
C PREV FRAME CODED REFL COEF(2) (INTEGER) (TCTAP,TCG,TCRF1-8 CONSEC)
  TCRF2 = 83
C
C PREV FRAME CODED REFL COEF(3) (INTEGER) (TCTAP,TCG,TCRF1-8 CONSEC)
  TCRF3 = 84
C
C PREV FRAME CODED REFL COEF(4) (INTEGER) (TCTAP,TCG,TCRF1-8 CONSEC)
  TCRF4 = 85
C
C PREV FRAME CODED REFL COEF(5) (INTEGER) (TCTAP,TCG,TCRF1-8 CONSEC)
  TCRF5 = 86

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C C PREV FRAME CODED REFL COEF(6) (INTEGER) (TCAP, TCG, TCRF1-8 CONSEC)
C   TCRF6 = 97
C C PREV FRAME CODED REFL COEF(7) (INTEGER) (TCAP, TCG, TCRF1-8 CONSEC)
C   TCRF7 = 98
C C PREV FRAME CODED REFL COEF(8) (INTEGER) (TCAP, TCG, TCRF1-8 CONSEC)
C   TCRF8 = 99
C C DECODED PITCH (INTEGER)
C   RPTC = 91
C C CONSTANT FIVE
C   T5 = 92
C C INVERSE OF DOWNSAMPLED FRAME SIZE
C   TDFSI = 94
C
C C CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
C   CONFIGURE MAP INTEGER SCALARS.
C C THIS MODULE CONFIGURES THE SYSTEM MAP INTEGER SCALARS.
C C ALL MAP INTEGER SCALAR ID'S (ISID'S) ARE
C C SYMBOLICALLY NAMED, WITH NAMES STARTING WITH
C C "T" FOR TRANSMITTER SCALARS, AND NAMES STARTING
C C WITH "R" FOR RECEIVER SCALARS.
C
C C DEFINE MAP INTEGER SCALAR ID'S
C C (FOR BUFFER STATUS FLAGS: 0=EMPTY, 1=FULL)
C
C C TSOURCE FLAG A
C   TSRFA = 50
C C TSOURCE FLAG R
C   TSRFB = 51
C C TBITS FLAG A
C   TBTA = 52
C C TBITS FLAG R
C   TBTR = 53
C C INTEGER PITCH
C   TIPTC = 54
C
C C RBITS FLAG A
C   RTFA = 57
C C RBITS FLAG B
C   RTFB = 58
C C RSINK FLAG A
C   RSNFA = 59
C C RSINK FLAG B
C   RSNFB = 60
C C DECODED (INTEGER) PITCH
C   RIPTC = 61
C C SYSTEM RUN FLAG (0 => STOP)
C   RUN = 62
C C FREE FOR NPITH
C   I = 63
C C A/D POINTER OFFSET
C   ADPO = 64
C C TSOURCE POINTER OFFSET
C   TSRPO = 65
C C TBITS POINTER OFFSET
C   TBTP = 66
C C TMODEM POINTER OFFSET
C   TMPO = 67
C C PMODEM POINTER OFFSET
C   RMPD = 68
C C RBITS POINTER OFFSET
C   RTPD = 69
C C RSINK POINTER OFFSET
C   RSNPD = 70
C C D/A POINTER OFFSET
C   DAPD = 71
C C A/D FRAME DISCARD COUNTER
C   TADFC = 72
C C TMODEM FAKE FRAME COUNTER
C   TMFFC = 73

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C C RHODEN FRAME DISCARD COUNTER
C   R4FDC = 74
C C RSINK DATA-NOT-READY COUNTER
C   RSNHR = 75
C C TRANSMITTER FRAME COUNTER
C   TFCR= 76
C C RECEIVER FRAME COUNTER
C   RFCR= 77
C C RECEIVER LOST-SYNC COUNTER
C   RLSCR= 78
C C LOCAL HANDSET ON-HOOK STATE
C   RONHR = 79
C C STATE OF RHODEN SYNC
C   RSYNC = 80
C C BEGINNING-OF-FRAME OFFSET (SYNC BIT POSITION)
C   RNOFO = 81
C C FREE FOR HP1TH
C   I = 82
C C NO-ERROR-CORRECTION SWITCH (NO CORRECTIONS IF NZ)
C   RNCOR= 123
C C FREE FOR HP1TH
C   I = 124
C C CHANNEL ERROR SIMULATOR FLAG (NON-ZERO => # SIM ERRORS PER FRAME)
C   RERSIM= 125
C C FREE FOR HP1TH
C   I = 126
C C VOCODER STATE (FOR DISPLAY)
C   VSTATE= 127
C
C
C   RETURN
C   END
C
C   REAL FUNCTION EVEN(X)

```

```

C
C   USAGE: V = EVEN(X)
C   X IS REAL INPUT
C   V IS: X IF X IS EVEN
C       X+1 IF X IS ODD
C
C   EVEN = X
C   IF (AMOD(X,2.) .NE. 0.) EVEN = X+1.
C   RETURN
C   END

```









```

C <DCA96>DCA96E.FOR.1 Mon 31-Dec-79 2:07PM Page 1:4
C
C ALL MODES RETURN HERE
C
C 990 CALL MPCLS(0)
C
C RETURN
C -L

C <DCA96>DCA96E.FOR.1 Mon 31-Dec-79 2:07PM Page 2
C
C CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
C C
C C EXECUTE DCA96 REAL TIME SYSTEM
C C
C 100 CONTINUE
C C
C C CONFIGURE A BUFFER OVER EACH SCROLL PROGRAM AND LOAD IT
C C
C CALL MPCLB(18USI+63,ADMPRG,512,,FXD,CHTG,LNG)
C CALL MPLOS(MDMSCL,IOS2,63)
C CALL MPCLB(18USI+63,ADMPRG,512,,FXD,CHTG,LNG)
C CALL MPLOS(ADAN,IOS2,63)
C CALL MPCLB(18USI+63,ADMPRG,512,,FXD,CHTG,LNG)
C CALL MPLOS(ADN,IOS2,63)
C
C
C C START THE REAL TIME SYSTEM
C C
C CALL MPXFL(DCARTS)
C
C TYPE 100
C FORMAT(' VOCODER IS IN OPERATION.')

```

```

C <DCA96>DCA96E.FOR.1 Mon 31-Dec-79 2:07PM Page 2:1
C "Q" COMMAND -- QUIT (HALT VOCODER)
12# CALL MP1TH(RUN,0,0)
CALL MPSAA(ADM)
CALL MPSAA(ADM)
C (THERE'S NO WAY TO STOP THE MODEM SCROLL)
C
C RETURN TO COMMON CODE
GO TO 9#

C "E" COMMAND -- SET ERROR SIMULATION
13# IF(N-1).0 .OR. N-CT.1#) GO TO 115
CALL MP1TH(RERSIN,N,0)
GO TO 112

C "C" COMMAND -- CONTROL ERROR CORRECTION
14# IF(N-NE.0) N=1
CALL MP1TH(RNOCOR,N,0)
GO TO 112

C "L" COMMAND -- CAUSE THE RCVR TO LOSE SYNC BY JAMMING INTO RBOFO
15# CALL MP1TH(RSYNC,1,5#)
GO TO 112

C "T" COMMAND -- STOP VOCODER, READ INTEGER SCALARS, THEN RESTART,
C THEN TYPE OUT THEIR VALUES
16# CALL MP1TH(RUN,0,0)
CALL MP1TH(TSPFA,IST(TSRFA),32,0)
CALL MPXFL(DRSTRT)
CALL TST(IST)
GO TO 112

C "S" COMMAND -- SUSPEND THIS TASK, BUT LET MAP CONTINUE.
C (UPON RESUMPTION OF TASK, IT WILL CONTINUE OUT OF THE PAUSE)
17# PAUSE "(VOCODER CONTINUING)".
GO TO 11#
-L

C <DCA96>DCA96E.FOR.1 Mon 31-Dec-79 2:07PM Page 3
C
CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
C EXECUTE FILE TO FILE SYSTEM
C
C 2# CONTINUE
C
C DEFINE INPUT AND OUTPUT FILES
C
C 2# TYPE 21#
FORMAT(" READ INPUT SIGNAL FROM FILE: '$')
NBLKS = KOPK(XTSRA,0,0)
IF(NBLKS.LE.0) GO TO 2#3
CALL SCOPY("SKIP HEADER BLOCK (256 WORDS)? ",SKPHDR)
IF (.NOT. CONFIRM(SKPHDR)) GO TO 215
I$STAT = K$IN(XTSRA,HDRBLK,256,MAXFRD)
C
C 215 TYPE 22#
FORMAT(" WRITE OUTPUT SIGNAL TO FILE: '$')
NBLKS = KOPK(XRSMKA,0,0,TRUE.)
IF(NBLKS.LE.0) GO TO 215
C
C CONFIGURE DUMMY MAP BUFFER (RL,LANG) FOR NPWDB'S
C
C 21# I$TIMEOUT: THIS BUFFER RESIDES ON BUS 2 AT 15000-15359
C (I.E. THE TOP OF BUS 2 MEMORY).IIIIII
C
C 21# I$OUT = 63
C
C CALL MPCLB(1BUS2+IDUM,15000.,100.,RL,CNTG,LANG)
C
C
C 21# I$OF = -1
C
C NFRAME=0
C
C NCLIPS=0
C
C CALL MP1ST(RUN,1)
C
C ASK ABOUT FRAME TYPEOUTS
C
C L$SMK=.FALSE.
C
C L$IST=.FALSE.
C
C 22# TYPE 22#
FORMAT(" FRAME TYPEOUTS? (Y, R, OR NIL): '$')
ACCEPT 227,IC4D
FORMAT(A1)
IF(IC4D-50.CHRR) L$IST=.TRUE.
IF(IC4D-NE.CHRT) GO TO 23#
L$SMK=.TRUE.
C
C 22# TYPE 22#
FORMAT(" FILE FOR TSINK DATA: '$')
CALL ASSIGN(2,XTSRA,-1)
WRITE(2,229)
C
C 229 FORMAT(/- F# P T C K1 K2 K3 K4 K5 K6 K7 K8 BASEBAND'/)

```

```

C TOP OF LOOP
230 ISTAT = KSIN(ITSRA,ISIG,100,MWIFRD)
IF (ISTAT.EQ.120F) GOTO 290
235 I=1,100
SIG(I) = FLOAT(1SIG(I)) / 32769.
C SET UP FOR PROPER A/D AND D/A BUFFERS
IDBUF=TA00A
IDBUF=RDABA
ITSNK=TSNKA
IF(MOD(MFRAME,2).EQ.0) GO TO 240
IDBUF=TA00B
IDBUF=RDABB
ITSNK=TSNKB
C XFER SIG TO MAP
C CALL MPWB(IDUM,SIG,4,1,SIG(100))
CALL VMOWN(IDBUF,IDUM)
C EXECUTE 1 FRAME OF SYSTEM
CALL MPKPL(DCAFFT)
C NOW MOVE BITS FROM TMODEM BUFFER TO MMODEM BUFFER, AS A CHANNEL
DOES.
GO TO(241,242,243,244,245,246) 1+MOD(MFRAME,6)
241 CALL VMOWN(MMODEB,TMDMA)
GO TO 240
242 CALL VMOWN(MMODEC,TMDMB)
GO TO 240
243 CALL VMOWN(MMODEA,TMDMA)
GO TO 240
244 CALL VMOWN(MMODEH,TMDMH)
GO TO 240
245 CALL VMOWN(MMODEC,TMDMA)
GO TO 240

246 CALL VMOWN(RMDMA,TMDMR)
GO TO 240
C READ OUTPUT (RSNKA) AND WRITE TO FILE
C RECONFIGURE DUMMY BUFFER "IDUM"
248 CALL MPCLB(1BUS2>IDUM,15000.,100.,RL,CHTG,LNG)
C CALL VMOWN(IDUM,TDARUF)
CALL MPORD(IDUM,SIG,4,1,SIG(100))
C ON 250 I=1,100
XSIG = SIG(I) * 32768.
IF (ABS(XSIG) .LE. 32767.) GO TO 250
NCLIPS=NCLIPS+1
XSIG=SIGN(32767.,XSIG)
ISIG(I) = IPX(XSIG)
WRITE OUT INTEGER OUTPUT
ISTAT = KSOUT(XRSNKA,ISIG,100,MWIFRD)
LOOP.
C IF REQUESTED, DO FRAME TYPEOUT
IF(.NOT.LTSNK) GO TO 260
CALL MPORD(ITSNK,ISIG,2,0,ISIG(71))
WRITE(2,255) MFRAME,(SIG(I), I=1,71)
FORMAT(15,1X,1113,12,2911,/,40X,3011)
IF(LRIST) CALL RIST
MFRAME=MFRAME+1
GOTO 230
C HERE ON EOF.
C
290 CALL RIST
TYPE 295,MFRAME,NCLIPS
FORMAT(17," FRAMES",IS," OUTPUT SAMPLES WERE CLIPPED.")
ISTAT = KCLOSE(ITSRA)
ISTAT = KCLOSE(XRSNKA)
IF(LTSNK) CALL CLOSE(2)
C
C RETURN TO COMMON CODE
C GOTO 900
-L

```

```

CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
C      EXECUTE SCOPE TIMING SYSTEM
C
C      400 CONTINUE
C
C      PUT SOMETHING REASONABLE IN TADRA, TADBB
C      CALL VCOST(TADRA,17.)
C      CALL VCOST(TADBB,17.)
C
C      START UP INFINITE MPIWL ON DCATIN
C      CALL MPIST(RUN,1)
C      CALL MPIWL(RUN,ME,0,DCATIN)
C      WAIT FOR USER TO SAY QUIT
C
C      TYPE 410
C      FORMAT(' TYPE ANY CHARACTER TO HALT MAP: 'S)
C      ACCEPT 411,ICHAR
C      FORMAT(A1)
C      STOP MPIWL
C      CALL MPIWL(RUN,0,0)
C      CALL RIST
C      RETURN TO COMMON CODE
C      GOTO 900
C
C      END

```

```

CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
C      EXECUTE TIMING SYSTEM
C
C      300 CONTINUE
C
C      GET TIMING PARAMETERS.
C
C      TYPE 310
C      FORMAT(' ENTER ITERATION VALUE (N/6): 'S)
C      ACCEPT 311,N
C      FORMAT(I3)
C
C      PUT SOMETHING REASONABLE IN TADRA, TADBB
C      CALL VCOST(TADRA,17.)
C      CALL VCOST(TADBB,17.)
C
C      SET UP TIMING (100 USFC TICKS)
C      READ CLOCK
C      CALL SETCLK(1)
C      IT1 = IPOCLK(N)
C      EXECUTE DCAFFT '6N' TIMES
C      CALL MPIST(RUN,1)
C      CALL MPIWL(RUN,1,N,DCATIN)
C      WAIT ON MAP, READ CLOCK
C      CALL MPIST(RUN,1)
C      IT2 = IPOCLK(N)
C      TIME = FLOAT(IT2-IT1)/60.
C      CONVERT TIME, PRINT IT
C
C      TIMINGS = TIME * .1
C      TYPE 320,6*N,TIMINGS
C      FORMAT('6N' ITERATION(S) TOOK',F10.4,' MSEC.S.')
C
C      CALL RIST
C      RETURN TO COMMON CODE
C      GOTO 900
C
C      -L

```





```

C
C      COMMON BLOCK -
C      BUFFER ADDRESSES AND SIZES NEEDED FOR INIT OF NON-SNAP BUFFERS
C      BY DCA96I (WRITTEN BY DCA96C)
C
C      COMMON /RAS/
C      JATARC, STARC,
C      JATRC, STRC,
C      JARLP, SRLP,
C      JARSMRC, SRSMRC,
C      JATBRL, STBRL,
C      JATDMA, STDMA,
C      JATDNO, STDNO,
C      JADABA, SRDABA,
C      JADABR, SRDABR
C
C      CCCCCC END OF DCA96C.FOR CCCCCC
C
C      DIMENSION IST(1)
C
C      TYPE 10
C      TYPE 20, IST(TSRPA), IST(ADPO), IST(TFRCTR), IST(RSYNC)
C      TYPE 30, IST(TSRFB), IST(TSRPO), IST(RFRCTR), IST(RBOFO)
C      TYPE 40, IST(TBFA), IST(TBPO), IST(RLSCTR)
C      TYPE 50, IST(TBFB), IST(TBPD), IST(TADPDC)
C      TYPE 60, IST(RBTPA), IST(RMPD), IST(TMFFC), IST(ROWHK)
C      TYPE 70, IST(RBTFB), IST(RBYPO), IST(RWDE)
C      TYPE 80, IST(RSMFA), IST(RSNPO), IST(RSNMR)
C      TYPE 90, IST(RSMFB), IST(ORPO)
C
C      10  FORMAT(' VOCODER STATE VARIABLES:',/)
C      20  FORMAT('  TSRPA:',12,'  ADPO:',13,'  TFRCTR:',16,'  RSYNC:'
C      )16)
C      30  FORMAT('  TSRFB:',12,'  TSRPO:',13,'  RFRCTR:',16,'  RBOFO:'
C      )16)
C      40  FORMAT('  TBFA:',12,'  TBPO:',13,'  ',6X,'  RLSCTR'
C      )16)
C      50  FORMAT('  TBFB:',12,'  TMPO:',13,'  TADPDC:',16)
C      60  FORMAT('  RBTPA:',12,'  RMPD:',13,'  TMFFC:',16,'  ROWHK:'
C      )16)
C      70  FORMAT('  RBTFB:',12,'  RBTPD:',13,'  RWDC:',16)
C      80  FORMAT('  RSMFA:',12,'  RSNPO:',13,'  RSNMR:',16)
C      90  FORMAT('  RSMFB:',12,'  ORPO:',13)
C
C      RETURN
C      END

```









[illegible]

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C <DCA96>DCA96F.FOR.1 Sat 29-Dec-79 7:36PM Page 4
CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
C
C
C ANLZA: F.L. FOR ANALYSIS USING "A" BUFFERS:
C CALL MPRFL(ANLZA)
C
C FOR SCOPE SYNC...
C CALL MPSC((IG3,ISET)
C
C LPC...
C
C
C USE TSRA; REMOVE DC; MULT BY HANNING WINDOW:
C
C CALL SSUM(TDCN,TSRA,TFSTZ)
C CALL MPRFL(214)
C CALL VMUL(TSR,1,TSRA,TDCN,THANW,0)
C CALL MPRFL(215)
C CALL VMUL(TSR,TSRA)
C CALL MPRFL(216)
C CALL VMUL(TSR,TSRA)
C CALL MPRFL(217)
C
C
C AUTOCORRELATE TMSR:
C CALL DCOR(TACT,TMSR,TMSRZ)
C CALL MPRFL(221)
C
C FINISHED WITH TSRA
C CALL MPRFL(TSRPA,0)
C
C DELAY REFLECTION COEFFICIENTS:
C PUT LAST FRAME CODED QUANTIZED REFL COEFFS (TCORF)
C INTO MEMORY (SCALARS TCRF1-TCRFA)
C
C CALL MPRFL(TCORF,TCRF1,0)
C
C COMPUTE REFLECTION COEFFS, CODE & QUANTIZE THEM:
C
C CALL MPRFL(TPRF,TKTHR,TCORF,TACV)
C CALL MPRFL(222,TPRF,TKTHR,TCORF,TACV)
C
C COMPUTE LP COEFFS (TLPC):
C
C CALL VKTDA(TLPC,TCORF)
C CALL MPRFL(223)
C
C COMPUTE INVERSE FILTERED SAMPLES AFTER
C MOVING LAST FRAME MEMORY INTO PLACE:

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C <DCA96>DCA96F.FOR.1 Sat 29-Dec-79 7:36PM Page 4:1
C
C CALL VMUL(TIMF,TINF1)
C CALL MPRFL(224)
C CALL DCOR(TIMF,TLPCR,TSRM)
C CALL MPRFL(225)
C
C RTEXT...
C
C
C SAVE LAST FRAME DMSHPLD B.B. EXCITATION;
C COMPUTE CURRENT FRAME:
C
C CALL VMUL(TBE1,TBE0)
C CALL MPRFL(226)
C CALL DCOR(TBE0,3,TIMF,TLPPB)
C CALL MPRFL(227)
C
C ERROR-PROTECT PREVIOUS XNTR FRAME (MOSTLY UNDER OCTM)
C CALL PROTECT(R)
C CALL MPRFL(TBTFB,1)
C
C PEDET...
C
C
C DO PITCH AND TAP DETERMINATION & PITCH REMOVAL
C
C CALL DCOR(TPAC,TBE,TBEZ)
C CALL MPRFL(228)
C
C CALL PTAP(TBPR,TPTC,TBPCP,TQTAP,TBEB,TCTAP,TPAC)
C CALL MPRFL(230)
C
C DO ENERGY CALCULATION:
C
C CALL ENRG(TQG,TQGI,TCG,TBPR,TDFSI)
C CALL MPRFL(235)
C
C APC...
C
C CALL VAPC(TSKKA,TPTC,TBROR,TQTAP,TBPR,TCTAP,TBFI)
C CALL MPRFL(236)
C
C CALL MPRFL(ANLZA)

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C <DCA96>DCA96F.FOR.1 Sat 29-Dec-79 7:36PM Page 5
CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
C ANL28: F.I. FOR ANALYSIS USING '8' BUFFERS:
C CALL MPRFL(ANL28)
C FOR SCOPE SYNC...
C CALL WPGSC(IG3,ISFT)
C LPC...
C USE TSB; REMOVE DC; MULT BY HANNING WINDOW:
C CALL SSUM(TDCN,TSRB,TFSZ1)
C CALL MPXBF(219)
C CALL VNUL(TWSR,1,TSRB,TDCN,THANW,0)
C CALL MPRBF(219)
C CALL VMOV(TSRF,TSRL)
C CALL VMOV(TSR,TSRD)
C CALL MPXBF(220)
C AUTOCORRELATE TWSR:
C CALL DCOR(TACV,TWSR,TWSPZ)
C CALL MPRBF(221)
C FINISHED WITH TSRB
C CALL MPIST(TSRFR,#)
C DELAY REFLECTION COEFFICIENTS:
C PUT LAST FRAME CODEQUANTIZED REFL COEFFS (TCQRF)
C INTO MEMORY (SCALARS TCRF1-TCRFR)
C CALL MPXRS(TCQRF,TCRF1,8)
C COMPUTE REFLECTION COEFFS, CODE & QUANTIZE THEM:
C CALL MWLF( TRFER,TKTHR,TCORF,TACV)
C CALL MPXRM(222,TRFER,TKTHR,TCORF,TACV)
C COMPUTE LP COEFFS (TLPC):
C CALL VKTQA(TLPC,TQRF)
C CALL MPXRF(223)
C COMPUTE INVERSE FILTERED SAMPLES AFTER
C MOVING LAST FRAME MEMORY INTO PLACE:
C CALL VMOV(TINF,TINF1)
C CALL MPXBF(224)

C <DCA96>DCA96F.FOR.1 Sat 29-Dec-79 7:36PM Page 5:1
C- CALL DCOR(TINF,TLPCR,TSRM)
C CALL MPXBF(225)
C BBEXT...
C SAVE LAST FRAME DMSMPLD 8.8. EXCITATION;
C COMPUTE CURRENT FRAME:
C CALL VMOV(TBE1,TBE0)
C CALL MPXBF(226)
C CALL DCWM(TBE0,3,TINF,TLPEB)
C CALL MPXBF(227)
C ERROR-PROTECT PREVIOUS XMTA FRAME (MOSTLY UNDER DCWM)
C CALL PROTECTCA)
C CALL MPIST(TRTFA,1)
C DO PITCH AND TAP DETERMINATION & PITCH REMOVAL
C CALL DCOR(TPAC,TBE,TBEZ)
C CALL MPXBF(228)
C CALL PTAP(TBPR,TPIC,TBPCP,TOTAP,TBE0,TCTAP,TPAC)
C CALL MPXBF(230)
C DO ENERGY CALCULATION:
C CALL ENRG(TCG,TOGI,TCG,TBPR,TDFSI)
C CALL MPXBF(235)
C APC...
C CALL VAPC(TSNEB,TPIC,TBROR,TOTAP,TBPR,TCTAP,TBT1)
C CALL MPXBF(237)
C CALL MPEFL(ANL28)
C-

```



AD-A083 238

BOLT BERANEK AND NEWMAN INC CAMBRIDGE MA

F/G 17/2

DESIGN AND REAL-TIME IMPLEMENTATION OF A BASEBAND LPC CODER FOR--ETC(U)

FEB 80 R VISWANATHAN, J WOLF, L COSELL

DCA100-79-C-0003

UNCLASSIFIED

BBN-4327-VOL-2

NL

3 of 3



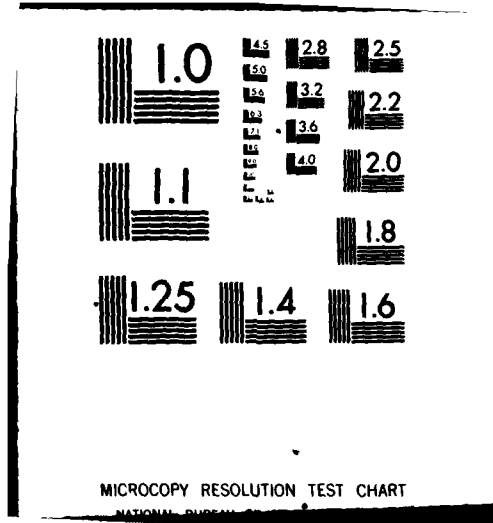
END

DATE

FORMED

6-80

DTIC



C <DCA96>DCA96F.FOR.1 Sat 29-Dec-79 7:36PM

C- C

CALL VSYA2(RFES,1,RUBE,1,RHUP,0)  
CALL MPXRF(249)

C- C

CALL VLTST(RSHKA,RSFN,RPFL,RFES)  
CALL MPXRF(250)  
CALL VMOV(RRF1,RRF0)  
CALL MPXRF(251)

C- C

SET FLAG TO SAY THAT RSHKA IS FULL  
CALL MP1ST(RSMFA,1)

C- C

CALL MPEPL(SYNZA)

~L

Page 6

C <DCA96>DCA96F.FOR.1 Sat 29-Dec-79 7:36PM

CC

SYNZA: F.L. FOR SYNTHESIS USING "A" BUFFERS

CALL MPBFL(SYNZA)

C- C

CALL WPGSC(IG3,ICLR)

C- C

CALL DEAL(RBR,RPIC,RRFB,RTAP,RSRA)  
CALL MPXRF(238)  
CALL VIAPC(RBR,RTAP,RRR,RPIC)  
CALL MPXRF(239)

C- C

CALL MFR...

SAVE PREV FRAME RESULT,  
HIPASS FILTER B.R. EXCITATION SAMPLES:

CALL VMOV(RHBE,RRBE1)  
CALL MPXRF(241)  
CALL DFL22(RHBE0,RRWCL,RBF,RBWM1)  
CALL MPXRF(242)

C- C

UPSAMPLE (3:1) AND LOWPASS FILTER:

CALL DCOR(RHBE1,RLPU1,RHBE)  
CALL MPXRF(243)  
CALL DCOR(RHBE2,RLPU2,RHBE)  
CALL MPXRF(244)  
CALL DCOR(RHBE3,RLPU3,RHBE)  
CALL MPXRF(245)

C- C

SAVE PREV FRAME RESULT,  
PERTURB AND UPSAMPLE, HIPASS FILTER:

CALL VMOV(RUPB,RUPB1)  
CALL MPXRF(246)  
CALL PTRR(RUPB0,RPCL,RHBE0,RPCL2,RRND)  
CALL MPXRF(247)  
CALL DCOR(RHUP,RHPU,RUPB)  
CALL MPXRF(248)

C- C

CORRECT NEXT RCVR FRAME (UNDER DCUR)  
CALL CORRECT(0)  
CALL MP1ST(RHFB,0)

C- C

SUM HPP AND LPP OUTPUTS:





```

JRTFA,RTFPA,RSNFA,RSNPA,RTFPA,
JRTFA,RTFPA,RSNFA,RSNPA,RTFPA,
JRTFA,RTFPA,RSNFA,RSNPA,RTFPA,
JRTFA,RTFPA,RSNFA,RSNPA,RTFPA,
JRTFA,RTFPA,RSNFA,RSNPA,RTFPA,
JRTFA,RTFPA,RSNFA,RSNPA,RTFPA,
JRTFA,RTFPA,RSNFA,RSNPA,RTFPA,
JRTFA,RTFPA,RSNFA,RSNPA,RTFPA,
JRTFA,RTFPA,RSNFA,RSNPA,RTFPA,
JRTFA,RTFPA,RSNFA,RSNPA,RTFPA,

```

```

C
C COMMON BLOCK -
C BUFFER ADDRESSES AND SIZES NEEDED FOR INIT OF NUM-SNAP BUFFERS
C BY DCA96I (WRITTEN BY DCA96C)
C

```

```

COMMON /BAS/
;ATADBC,STADBC,
;ATBTC,STBTC,
;ARLPU,SRPU,
;ARLPU,SRPU,
;ARLPU,SRPU,
;ARLPU,SRPU,
;ARLPU,SRPU,
;ARLPU,SRPU,
;ARLPU,SRPU,
;ARLPU,SRPU,
;ARLPU,SRPU,

```

```

C
C CCCCCC END OF DCA96I.FOR CCCCCC
C
C
C

```

```

DATA PRCSR/1,AP/1/
DATA RL/0,CPLX/1,PD/2,CNTG/1,LNG/1,SHRT/1/
DATA NWSZ/1,1,FWSZ/2,1,DWSZ/4,1/

```

```

IRUS1 = 64
IRUS2 = 128
IRUS3 = 192

```

```

C
C CCCCCC MAP BUFFERS AND SCALARS.
C
C
C

```

```

THIS MODULE INITIALIZES ALL MAP BUFFERS AND SCALARS (REAL &
INTEGER) WHICH REQUIRE INITIALIZATION.
AN "M" FOLLOWED BY A SCALAR NAME SPECIFIES THE
MOST VARIABLE CONTAINING THE HOST COPY OF THE
INITIAL CONDITIONS OF THAT SCALAR.
MOST ARRAYS "HWORK"(REAL) AND "HWORK"(INTEGER) ARE
USED TO CONTAIN THE HOST COPIES OF THE INITIAL
CONDITIONS OF ALL BUFFERS.

```

```

C
C INITIALIZATION CONSTANTS:
C

```

```

IUPFSZ = 100
IOWFSZ = 60
ILPF = #
IHPF = 1

```

```

C
C IUIUSE BUFFER "RUBE" AS TEMP BUFFER FOR INITIALIZATION
C IUIOF "IADBC", SINCE MPWDB CAN'T GO DIRECTLY TO A
C IUIOF "IADBC", SINCE MPWDB CAN'T GO DIRECTLY TO A
C IUIOF "IADBC", SINCE MPWDB CAN'T GO DIRECTLY TO A
C IUIOF "IADBC", SINCE MPWDB CAN'T GO DIRECTLY TO A
C IUIOF "IADBC", SINCE MPWDB CAN'T GO DIRECTLY TO A

```

```

C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"

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```

C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"

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C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"

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C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"

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C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"

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C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"

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C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"

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C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"

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C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"

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C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"

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C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"

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C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"

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```

C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"
C IUIOF "IADBC"

```

```

C USE BTD 63 AS TEMP RID
C (TBYC IS NOT A SNAP BUFFER)
C
C TBYC = 63
C CALL MPCLB(1BU51+TBYC,ATBYC,STBYC,FRO,CHTG,LNG)
C
C DO 5 I=1,IFIX(STBYC)
C   IWORK(I) = 12
C
C CALL MPWDB(TBYC,IWORK,2,1,IWORK(IFIX(STBYC)))
C
C INITIALIZE 'TMDNA' TO $00C'S (=12)
C SYNC BIT (= LOW BIT OF 1ST HWORD) = 0
C
C DO 7 I=1,IFIX(STQNA)
C   IWORK(I) = 12
C
C CALL MPWDB(TMDNA,IWORK,2,1,IWORK(IFIX(STMDNA)))
C
C INITIALIZE 'TMDNB' TO $00C'S (=12)
C EXCEPT 1ST HWORD <= $00D (=13)
C SYNC BIT (= LOW BIT OF 1ST HWORD) = 1
C
C DO 9 I=1,IFIX(STMDNB)
C   IWORK(I) = 12
C   IWORK(I) = IWORK(I) + 1
C
C CALL MPWDB(TMDNB,IWORK,2,1,IWORK(IFIX(STMDNB)))
C
C INITIALIZE 'RSHB' TO ZEROS
C CALL VCLR(RSHB)
C
C INITIALIZE 'RBE' TO ZEROS
C CALL VCLR(RBE)
C
C INITIALIZE 'RMBE1' TO ZEROS
C CALL VCLR(RMBE1)
C
C INITIALIZE 'RLPO' TO LPP COEFFS + 3
C (75 COEFFS WITH NO PADDING)

```

```

C C INITIALIZE "TCORF" TO ZEROS
C C
C C CALL VCLR(TCORF)
C C
C C INITIALIZE "TINF1" TO ZEROS
C C
C C CALL VCLR(TINF1)
C C
C C INITIALIZE "TLPFB" TO LPP COEFFS
C C (75 COEFFS WITH NO PADDING)
C C
C C NZRPO = 0
C C NCNEFS = 75
C C CALL LPPFPF(HWORK,NZRPD,NCNEFS,ILPF)
C C CALL MPWAB(TLPFB,HWORK,4,1,HWORK(NCNEFS))
C C
C C INITIALIZE "TBEZ" TO ZEROS
C C
C C CALL VCLR(TBEZ)
C C
C C INITIALIZE "TBADL" TO ZEROS
C C
C C USE BID 63 AS TEMP BID
C C (TBADL IS NOT A SNAP BUFFER)
C C
C C TBADL = 63
C C CALL MPCLB(18057,TBADL,ATBOLD,STBOLD,RL,CNTG,LNC)
C C
C C CALL VCLR(TBADL)
C C
C C INITIALIZE "TBDR" TO ZEROS
C C
C C CALL VCLR(TBDR)
C C
C C INITIALIZE "TSKFB" TO ZEROS
C C
C C CALL VCLR(TSKFB)
C C
C C INIT "DYBC" TO THE BITSTREAM EQUIV OF "SILENCE".
C C (ASSUME CODED ENERGY = 0 WILL TAKE CARE OF IT,
C C SO INITIALIZE TO $09WC = 12)
C C

```

```

C <DCA96>DCA96I.FOR.1
C
C USE RID 63 AS TEMP BIO
C (RLPU IS NOT A SNAP BUFFER)
C
C RLPU = 63
C CALL MCLB(18U51+RLPU,ARLPU,SRLPU,RL,CNTG,LNG)
C
C NZRPD = 0
C NCOEFS = 75
C CALL LPPHPF(HWORK,NZRPD,NCOEFS,ILPF)
C DO 10 I=1,NCOEFS
C   10 HWORK(I) = HWORK(I) * 3.
C   CALL MPWB(RLPU,HWORK,4,1,HWORK(NCOEFS))
C
C INITIALIZE 'RRND' TO GAUSSIAN RANDOM VALUES
C
C CALL RANDOM('WORK,IDNFSZ')
C CALL MPWB(RRND,HWORK,4,1,HWORK(IDNFSZ))
C
C INITIALIZE 'RUP1' TO ZEROS
C
C CALL VCLR(RUP1)
C
C INITIALIZE 'HNP1' TO HPF COEFS * 3
C (75 COEFS WITH NO PADDING)
C
C NZRPD = 0
C NCOEFS = 75
C CALL LPPHPF(HWORK,NZRPD,NCOEFS,IMP)
C DO 20 I=1,NCOEFS
C   20 HWORK(I) = HWORK(I) * 3.
C   CALL MPWB(RNP1,HWORK,4,1,HWORK(NCOEFS))
C
C INITIALIZE 'RRP1' TO ZEROS
C
C CALL VCLR(RRP1)
C
C INITIALIZE 'RSP1' TO ZEROS
C
C CALL VCLR(RSP1)
C
C INITIALIZE 'RSNC' TO "SILENCE"
C
C USE BIO 63 AS TEMP BIO
C (RSNC IS NOT A SNAP BUFFER)
C
C <DCA96>DCA96I.FOR.1
C
C RSNC = 63
C CALL MCLB(18U51+RSNC,ARSNC,SRSNKC,RL,CNTG,SHRT)
C
C CALL VCLR(RSNC)
C
C INITIALIZE 'RDAB' TO ZEROS
C
C CALL VCLR(RDAB)
C
C INITIALIZE 'RDAB0' TO ZEROS
C
C CALL VCLR(RDAB0)
C
C CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
C INITIALIZE (REAL) SCALARS.
C
C INITIALIZE 'TFSZ' (NEGATIVE INVERSE OF FRAMESIZE)
C
C TFSZ = -1./100.
C CALL MPWT(TFSZ,HTFSZ,1,1)
C
C INITIALIZE 'TKTHR' (MULT THRESHOLD)
C
C TKTHR = .9995
C CALL MPWT(TKTHR,HTKTHR,1,1)
C
C INITIALIZE 'TBT1'-'TBT3' (B-B. QUANT. THRESHOLDS)
C
C TBT1 = 0.531
C TBT2 = 1.249
C TBT3 = 2.371
C CALL MPWT(TBT1,HTBT1,1,1)
C CALL MPWT(TBT2,HTBT2,1,1)
C CALL MPWT(TBT3,HTBT3,1,1)
C
C INITIALIZE 'TRQ0'-'TRQ3' (B-B. QUANT. VALUES)
C

```





```

C
C
C INITIALIZE 'TMFFC' TO 0
C CALL MPIST(TMFFC,0)
C
C INITIALIZE 'RMFDC' TO 0
C CALL MPIST(RMFDC,0)
C
C INITIALIZE 'RSNHR' TO 0
C CALL MPIST(RSNHR,0)
C
C INITIALIZE 'TFRCTR' TO 0
C CALL MPIST(TFRCTR,0)
C
C INITIALIZE 'RFRCTR' TO 0
C CALL MPIST(RFRCTR,0)
C
C INITIALIZE 'RLSCTR' TO 0
C CALL MPIST(RLSCTR,0)
C
C INITIALIZE 'RDNHR' TO 0
C CALL MPIST(RDNHR,0)
C
C INITIALIZE 'RSYNC' TO 0
C CALL MPIST(RSYNC,0)
C
C INITIALIZE 'RNDOR' TO 0
C CALL MPIST(RNDOR,0)
C
C INITIALIZE 'RERSIN' TO 0
C CALL MPIST(RERSIN,0)
C

```

```

C
C CALL MPIST(RSNHR,0)
C
C INITIALIZE 'RUN' TO 0 (WILL SET TO 1 WHEN STARTING VOCODER)
C CALL MPIST(RUN,0)
C
C INITIALIZE 'ADPO' TO 0
C CALL MPIST(ADPO,0)
C
C INITIALIZE 'TSRPO' TO -2
C CALL MPIST(TSRPO,-2)
C
C INITIALIZE 'TOTPO' TO 0
C CALL MPIST(TOTPO,0)
C
C INITIALIZE 'TMPQ' TO 0
C CALL MPIST(TMPQ,0)
C
C INITIALIZE 'RMPQ' TO 0
C CALL MPIST(RMPQ,0)
C
C INITIALIZE 'RBTPO' TO -2
C CALL MPIST(RBTPO,-2)
C
C INITIALIZE 'RSNPO' TO 0
C CALL MPIST(RSNPO,0)
C
C INITIALIZE 'DAPQ' TO 0
C CALL MPIST(DAPQ,0)
C
C INITIALIZE 'TADPOC' TO 0
C CALL MPIST(TADPOC,0)
C

```

C <DCA96>DCA961.FOR.1 Mon 31-Dec-79 2:47PM

C  
C INITIALIZE 'VSTATE' TO 0  
C  
C CALL MPIST(VSTATE,0)

C  
C  
C  
C  
C  
C

RETURN  
END

C [BBM-TENEXD]KFFIELD>DCA964.FOR.16, 5-Dec-79 22:39:28, Ed: KFIELD

DCA9.6

CC

C THIS ROUT SEGMENT CONTAINS CALLS TO SUBROUTINES  
C DCA96C (CONFIGURATION - BUFFER AND SCALAR

C CONFIGURATION)

C DCA96I (INITIALIZATION - BUFFER AND SCALAR

C INITIALIZATION)

C DCA96F (FUNCTION PREBINDING AND FUNCTION LIST

C DEFINITIONS)

C DCA96E (EXECUTION)

C COMMUNICATION BETWEEN THESE THREE SUBROUTINES

C IS VIA MAP-300 MEMORY, WHICH CONTAINS

C SYSTEM BUFFERS, SCALARS AND FUNCTION LISTS,

C AND VIA COMMON BLOCKS "BSIDS" (BUFFER AND

C SCALAR ID NAME DEFINITIONS), "BAS" (BUFFER

C ADDRESSES AND SIZES USED BY DCA96I),

C "PLS" (FUNCTION LIST NAME DEFINITIONS).

CC

C USER INTERACTION VARIABLE(S)

C INTEGER Q

C MAP FUNCTION LIST NAMES

C INTEGER DCARTS,DCALP,DCAFFT,DCATIM,DRSTRT

C INTEGER AMLZA,ANLZ8,SYNZA,SYNZR

C [BBM-TENEXD]KFFIELD>DCA964.FOR.8, 5-Dec-79 14:42:27, Ed: WOLF

C MAP BUFFER NAMES

C INTEGER TADRA, TADBB, TADBC, TSRA, TSRR, TRAMW

C INTEGER TNSR, TLPCR, TACY, TRFR, TCRF

C INTEGER TCRF, TQRF, TLPC, TSR, TSM, TSNF

C INTEGER TSL, TINF, TINF1, TINF, TLPFB, TREH

C INTEGER TRB1, TRE, TREZ, TNSRZ

C INTEGER TPAC, TPACP

C INTEGER TRPP, TRDL, TRDR, TSKA, TSNKB, TSNKC

C INTEGER TRTA, TRTB, TRTC

C INTEGER TRDRA, TRDRB

INTEGER RTTA, RTTB  
INTEGER RMDA, RMDM, RMDMC, RSSPF, RSSSS, RSRA  
INTEGER RSRE, RRF, RRE, RREB, RREB1  
INTEGER RHE, RLP, RLP1, RLP2, RLP3, RUBE  
INTEGER RUB1, RUB2, RUB3, RRD, RUPB, RUPB1  
INTEGER RUPD, RUP, RPF, RPF, RPF1, RPFM  
INTEGER RSKA, RSKB, RSKC, RDA, RDA8

MAP SCALARS

INTEGER TDCN, TFSZ, TKTR, TPTC  
INTEGER TE, TOTAP, TCG, TQGI, TBT1  
INTEGER TB2, TB3, TB4, TBQ1, TBQ2, TBQ3  
INTEGER TCTAP, TCG, TCRF1, TCRF2, TCRF3, TCRF4  
INTEGER TCRF5, TCRF6, TCRF7, TCRF8, T5  
INTEGER TCRF9  
INTEGER RTAP, RMC1, RMC2, RMC3, RMC4  
INTEGER RBM1, RBM2, RBM3, RBM4, RPL1, RPL2  
INTEGER RPTC

INTEGER TSRA, TSRR, TBFA, TBFB, TPTC  
INTEGER RTFA, RTFB, RSMFA, RSMFB, RLTC  
INTEGER RUN  
INTEGER APO, TSRO, TBPO, TMO, RMO  
INTEGER RTRP, RSMPO, DAPO, TADFC  
INTEGER TRFC, RMOFC, RSMR, TRCTR  
INTEGER RFRCTR, RLCTR  
INTEGER RORHK, RSYMC, RBOFO, RMOGOR  
INTEGER RERSIM, VSTATE

COMMON BLOCK - BUFFER AND SCALAR IDS

COMMON /BSIDS/

!TADRA, TADBB, TADBC, TSRA, TSRR, TRAMW,  
!TSR, TLPCR, TACY, TRFR, TCRF, TSM,  
!TSR, TQRF, TLPC, TSR, TSM, TSNF,  
!TSL, TINF, TINF1, TINF, TLPFB, TREH,  
!TRE, TREZ, TNSRZ,  
!TPAC, TPACP,  
!TRPP, TRDL, TRDR, TSKA, TSNKB, TSNKC,  
!TRTA, TRTB, TRTC,  
!TRDRA, TRDRB,  
!RTA, RTB,  
!RMDA, RMDM, RMDMC, RSSPF, RSSSS, RSRA,  
!RSR, RRF, RRE, RREB, RREB1,  
!RHE, RLP, RLP1, RLP2, RLP3, RUBE,  
!RUB1, RUB2, RUB3, RRD, RUPB, RUPB1,  
!RUPD, RUP, RPF, RPF, RPF1, RPFM,  
!RSKA, RSKB, RSKC, RDA, RDA8,  
!TDCN, TFSZ, TKTR, TPTC,

```

1 TYPE 2
2 FORMAT(' BBN 9600 BPS MAP-3PM VOCODER SYSTEM"/)
3 CONTINUE
4 TYPE 10
5 FORMAT(' CONFIGURING MAP BUFFERS AND SCALARS....')
6 CALL DCA96C
7 TYPE 20
8 FORMAT(' INITIALIZING MAP BUFFERS AND SCALARS....')
9 CALL DCA96I
10 TYPE 30
11 FORMAT(' PREBINDING MAP FUNCTIONS AND DEFINING FUNCTION LISTS
12 **')
13 CALL DCA96F
14 TYPE 40
15 FORMAT(' EXECUTING DCA96 SYSTEM....')
16 CALL DCA96E
17 TYPE 100
18 FORMAT(' ENTER "Q" TO QUIT, ANY OTHER CHAR. TO RESTART SYSTEM:
19 **$)
20 ACCEPT 101,ICHR
21 FORMAT(1)
22 IF (ICHR .NE. Q) GOTD 5
23
24 STOP
25 END

```

```

3TE, TQAP,TQG, TQGI, TB11,
3T12, TB13, TBQ4, TBQ1, TBQ2, TBQ3,
3TAP,TCG, TCRF1,TCRF2,TCRF3,TCRF4,
3TCRF5,TCRF6,TCRF7,TCRF8,TS,
3TFSI,
3TAP, RBUC1,RBUC2,RBUC3,RBUC4,
3RBUM1,RBUM2,RBUM3,RBUM4,RPCL, RPC2,
3RPTC,
3TSRFA,TSRFB,TBFA,TBFB,TBFC,
3RTFA,RTFB,RSNFA,RSNFB,RTPC,
3RWN,
3TSRPO,TSRPO,TBPO,TSRPO, RSNPO,
3TSPO,TSPO,TBPO,TSPO, RSNPO,
3RSNMR,TSFCTR, RFRCTR, RLSCTR,
3RSNMR,RSNMR,RSNMR,RSNMR,
3RERSIN, VSTATE

```

```

COMMON BLOCK -
BUFFER ADDRESSES AND SIZES NEEDED FOR INIT OF NON-SNAP BUFFERS
BY DCA96I (WRITTEN BY DCA96C)

```

```

COMMON /BAS/
3ATADR,STADR,
3ATBTC,STBTC,
3RLPU,STLPU,
3RSNMC,STSNMC,
3ATBRL,STBRL,
3ATBMD,STBMD,
3ATBMD,STBMD,
3ATBMD,STBMD,
3ATBMD,STBMD

```

CCCCCCCC END OF DCA96M.FOR CCCCCCCC

```

COMMON BLOCK -
FUNCTION LIST NAMES

```

```

COMMON /FLS/
3DCARTS,DCALP,DCARTT,DCATIM,DRSTNT,
3ANLZA,ANLZB,SYNZA,SYNZB

```

```

DATA Q/'Q'/

```

CC

LOOP THRU SYSTEM UNTIL USER SAYS TO QUIT.

```

C <DCA96>DCA96S.FOR.1 Sat 29-Dec-79 7:37PM Page 1
C (BROW-76MEXD)<KFIELD>DCA96S.FOR.2, 17-Dec-79 15:21:19, Ed: KFIELD
CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
C SUBROUTINE SQNAV(OUTBUF,ISIZE,NCYCLE,AMP)
C
C PURPOSE COMPUTES A SQUARE WAVE OF AMPLITUDE "AMP",
C WITH "NCYCLE" CYCLES OCCURRING IN "ISIZE" SAMPLES.
C
C PARAMETERS
C OUTBUF - (REAL) OUTPUT ARRAY
C ISIZE - (INTEGER) NUMBER OF OUTPUT POINTS
C (MUST BE A MULTIPLE OF "NCYCLE")
C NCYCLE - (INTEGER) NUMBER OF SQUARE WAVE CYCLES
C AMP - (REAL) SQUARE WAVE AMPLITUDE
C
C SUBROUTINE SQNAV(OUTBUF,ISIZE,NCYCLE,AMP)
C
C REAL OUTBUF(1)
C
C IBLKSZ = ISIZE/NCYCLE
C
C DO 10 I=0,NCYCLE-1
C
C DO 20 J=1,IBLKSZ/2
C OUTBUF((I * IBLKSZ) + J) = AMP
C
C DO 30 J=(IBLKSZ/2)+1,IBLKSZ
C OUTBUF((I * IBLKSZ) + J) = -AMP
C
C 10 CONTINUE
C
C RETURN
C END
CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
C SUBROUTINE HANNING(OUTBUF,ISIZE)
C
C SUBROUTINE HANNING(OUTBUF,ISIZE)
C
C PURPOSE GENERATES HANNING WINDOW OF SIZE "ISIZE" (INTEGER)
C INTO REAL ARRAY "OUTBUF".
C
C SUBROUTINE HANNING(OUTBUF,ISIZE)
C
C REAL OUTBUF(1)
C
C DARG = 3.1415926535 * 2./ISIZE
C DO 10 I=1,ISIZE
C OUTBUF(I) = .54 - .46*COS(DARG*(I-1))
C
C 10 CONTINUE

```



```

SUBROUTINE GAUSS(IX,JX,S,AM,V)
  A = 0.0
  DO 50 I=1,12
    V = RAN(IX,JX)
    A = A + V
  50 CONTINUE
  V = (A - 6.0)*S + AM
  RETURN
END

```